

# RKD700KL

## Silicon Schottky Barrier Diode for Backflow prevention

REJ03G1337-0100

Rev.1.00

Jan 19, 2006

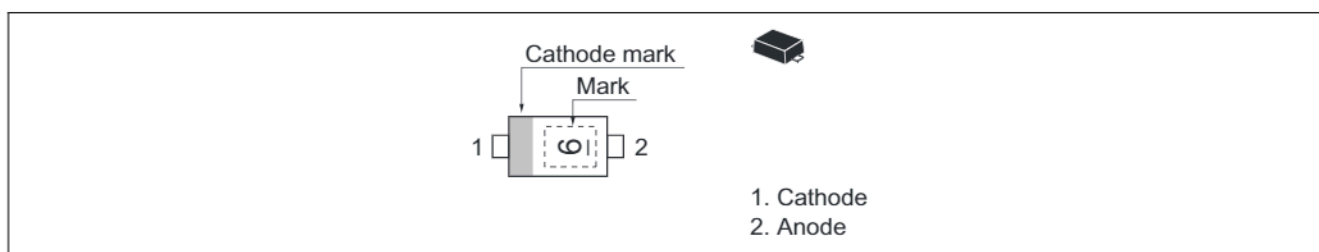
### Features

- Low reverse current, Low capacitance.
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

### Ordering Information

Type No.	Laser Mark	Package Name	Package Code
RKD700KL	6	EFP	PXSF0002ZA-A

### Pin Arrangement



## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$	30	V
Reverse voltage	$V_R$	30	V
Non-Repetitive peak forward surge current	$I_{FSM}^*$	200	mA
Average rectified current	$I_O$	50	mA
Junction temperature	$T_J$	125	°C
Storage temperature	$T_{stg}$	-55 to +125	°C

Note: 10 ms Sinewave 1 pulse

## Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	$V_{F1}$	—	0.11	0.14	V	$I_F = 1 \mu A$
	$V_{F2}$	—	—	0.33		$I_F = 1 \text{ mA}$
	$V_{F3}$	—	—	0.43		$I_F = 10 \text{ mA}$
Reverse current	$I_{R1}$	—	—	45	nA	$V_R = 3 \text{ V}$
	$I_{R2}$	—	—	1	$\mu A$	$V_R = 30 \text{ V}$
Capacitance	C	—	—	2.8	pF	$V_R = 1 \text{ V}, f = 1 \text{ MHz}$

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

## Main Characteristic

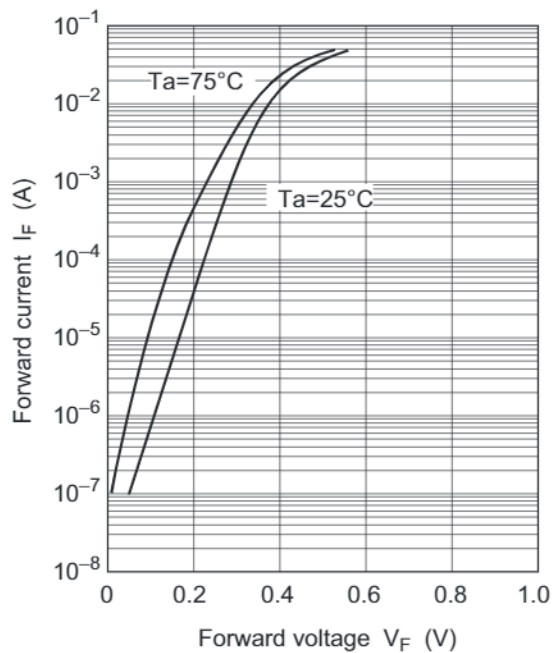


Fig.1 Forward current vs. Forward voltage

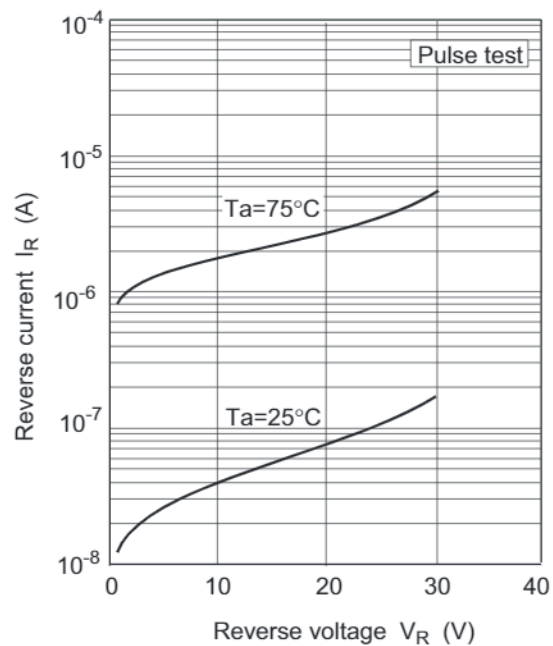


Fig.2 Reverse current vs. Reverse voltage

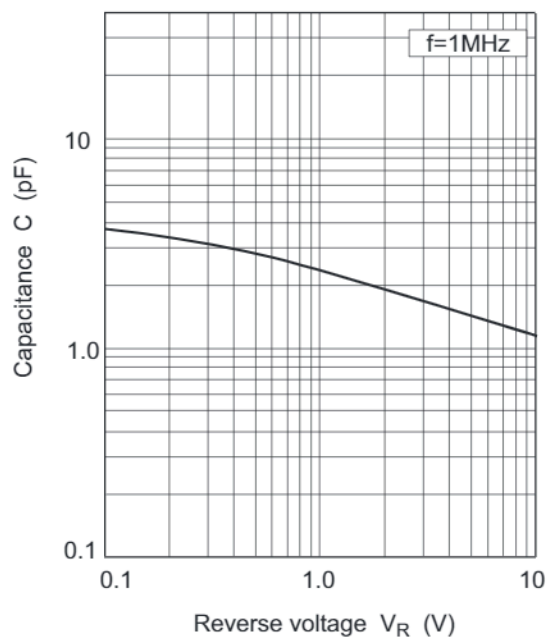
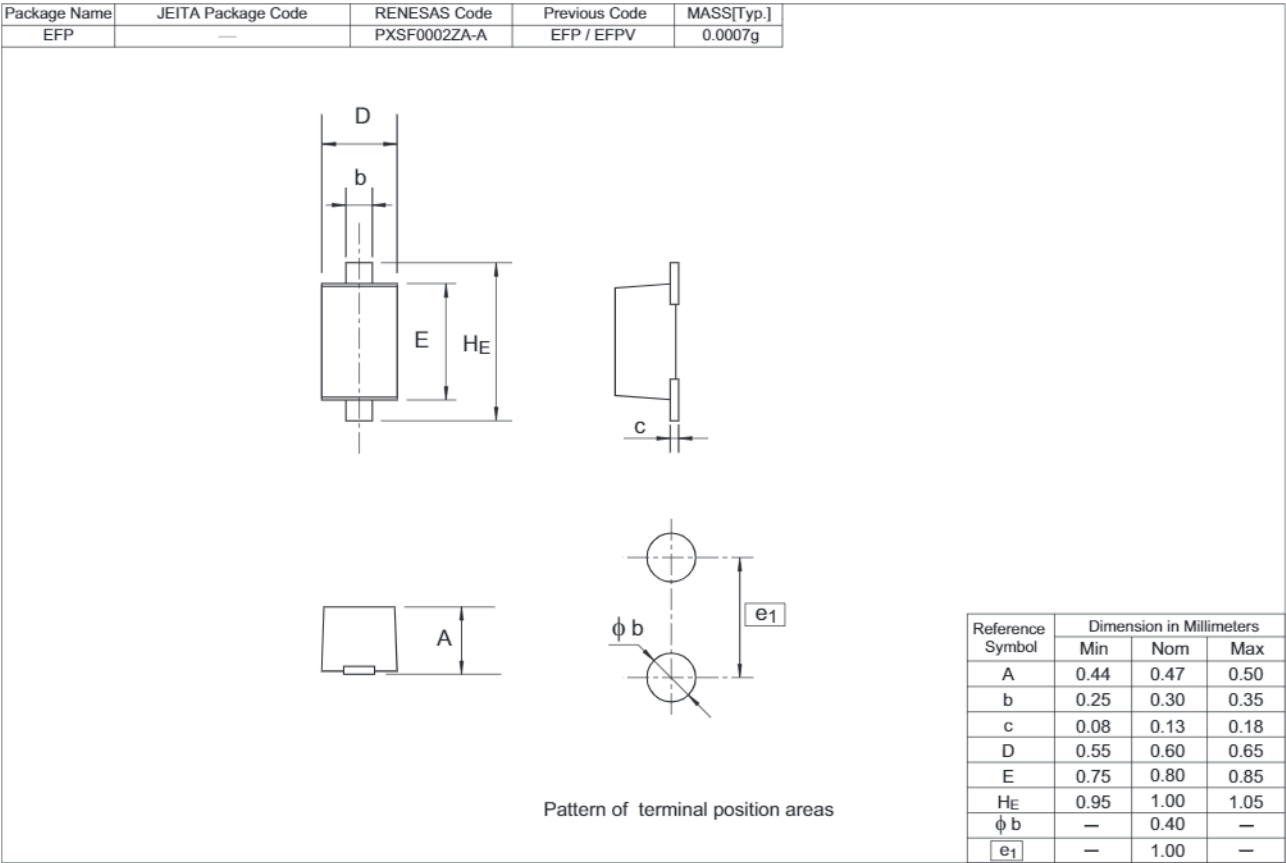


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions



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