

# RJK5012DPP

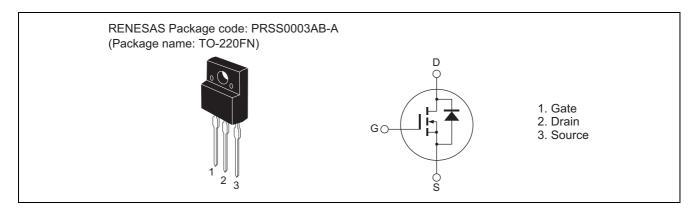
# Silicon N Channel MOS FET High Speed Power Switching

REJ03G1545-0100 Rev.1.00 May 10, 2007

#### **Features**

- Low on-resistance
- Low leakage current
- High speed switching

## **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	500	V
Gate to source voltage	$V_{GSS}$	±30	V
Drain current	I <sub>D</sub> Note4	12	Α
Drain peak current	I <sub>D (pulse)</sub> Note1	24	Α
Body-drain diode reverse drain current	I <sub>DR</sub>	12	Α
Body-drain diode reverse drain peak current	I <sub>DR (pulse)</sub> Note1	24	Α
Avalanche current	I <sub>AP</sub> Note3	4	Α
Avalanche energy	E <sub>AR</sub> Note3	0.88	mJ
Channel dissipation	Pch Note2	30	W
Channel to case thermal impedance	θch-c	4.17	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tc = 25°C
- 3. STch =  $25^{\circ}$ C, Tch  $\leq 150^{\circ}$ C
- 4. Limited by maximum safe operation area

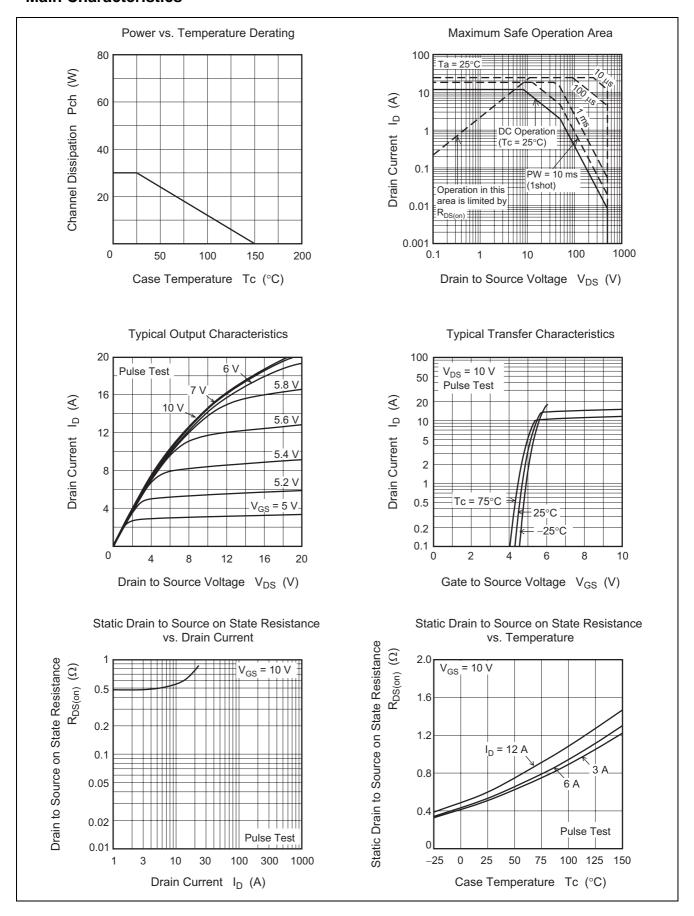
# **Electrical Characteristics**

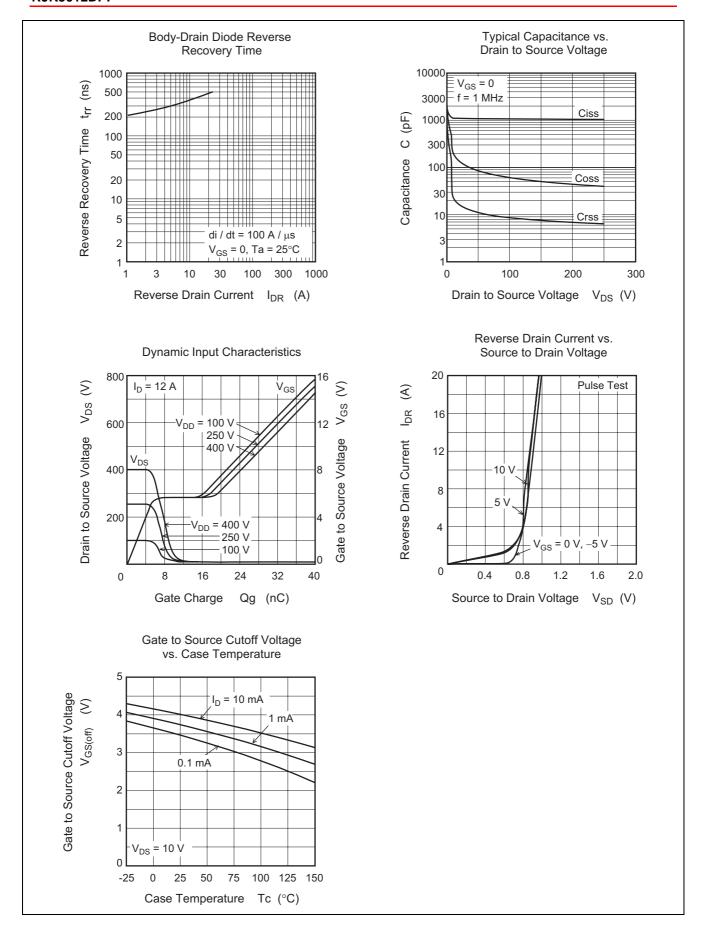
 $(Ta = 25^{\circ}C)$ 

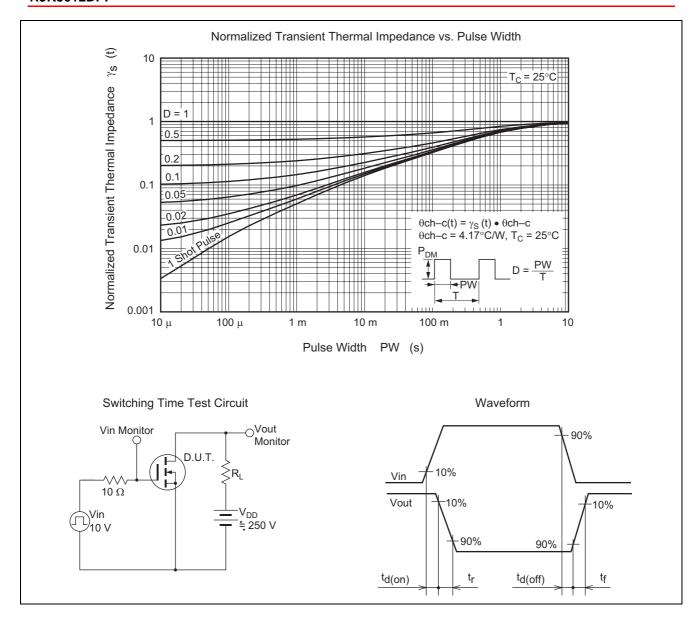
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	500	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		_	1	μΑ	$V_{DS} = 500 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>		_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	3.0	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS(on)</sub>	1	0.515	0.620	Ω	$I_D = 6 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note5}}$
Input capacitance	Ciss		1100	_	pF	V <sub>DS</sub> = 25 V
Output capacitance	Coss	_	120	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	15	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d(on)</sub>		30	_	ns	I <sub>D</sub> = 6 A
Rise time	t <sub>r</sub>	_	23	_	ns	V <sub>GS</sub> = 10 V
Turn-off delay time	$t_{d(off)}$	_	77	_	ns	$R_L = 41.6 \Omega$
Fall time	t <sub>f</sub>	_	16	_	ns	$Rg = 10 \Omega$
Total gate charge	Qg	_	29	_	nC	V <sub>DD</sub> = 400 V
Gate to source charge	Qgs	_	5.5	_	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd		13	_	nC	I <sub>D</sub> = 12 A
Body-drain diode forward voltage	$V_{DF}$	_	0.89	1.50	V	$I_F = 12 \text{ A}, V_{GS} = 0^{\text{Note5}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>		280	_	ns	$I_F = 12 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 5. Pulse test

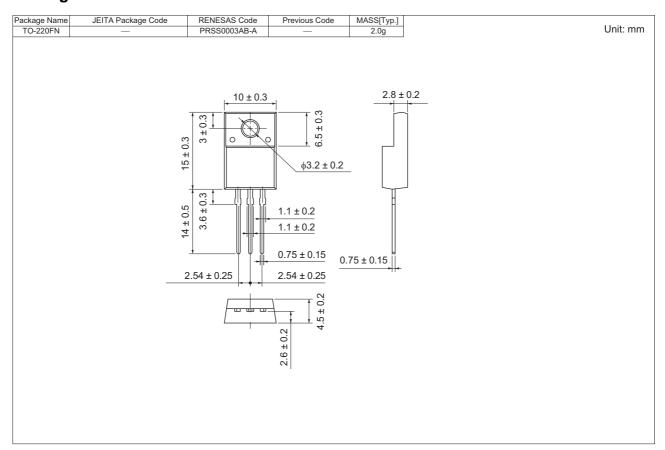
### **Main Characteristics**







# **Package Dimensions**



# **Ordering Information**

Part No.	Quantity	Shipping Container
RJK5012DPP-00-T2	1050 pcs	Box (Tube)

Renesas Technology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

- Renesas lechnology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Notes:

  1. This document is provided for reference purposes only so that Renesas customers may select the appropriate Renesas products for their use. Renesas neither makes warrantes or representations with respect to the accuracy or completeness of the information in this document nor grants any license to any intellectual property girbs to any other rights of representations with respect to the information in this document in this document of the purpose of the respect of the information in this document in the product data, diagrams, charts, programs, algorithms, and application circuit examples.

  3. You should not use the products of the technology described in this document for the purpose of military use. When exporting the products or technology described herein, you should follow the applicable export control laws and regulations, and procedures required by such laws and regulations, and procedures required to change without any plan protein. Before purchasing or using any Renesas products listed in this document, in the development is satisfied. The procedure is such as the development of the dev



### **RENESAS SALES OFFICES**

http://www.renesas.com

Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

#### Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120 Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

**Renesas Technology Taiwan Co., Ltd.** 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510