

PART NUMBER

54ALS162BJB-ROCV

Rochester Electronics

Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer. (OCM)

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
 - Class Q Military
 - Class V Space Level

Qualified Suppliers List of Distributors (QSLD)

- Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Convert to military drawing format. Change V_{IL} , f_{MAX} , t_P , CLOCK, I_{IL} , and propagation delays. Remove vendor CAGE 04713.	86-09-02	N. A. Hauck
B	Change fanout, synchronous clear pulse width, enable P/enable T setup time, and I_{CC} . Editorial changes.	87-05-29	N. A. Hauck
C	Changes in accordance with NOR 5962-R133-92.	92-02-20	M. L. Poelking
D	Changes in accordance with NOR 5962-R335-92	92-10-05	M. L. Poelking
E	Revise for 'D' certification. Editorial changes. New boilerplate. -ljs	99-09-20	Raymond Monnin
F	Update to current requirements. Editorial changes throughout. - gap	05-11-17	Raymond Monnin
G	Update drawing to current MIL-PRF-38535 requirements. - jt	15-10-20	Charles F. Saffle
H	Update drawing to latest MIL-PRF-38535 requirements. - jt	20-08-03	James R. Eschmeyer



THE ORIGINAL FIRST PAGE OF THIS DRAWING HAS BEEN REPLACED

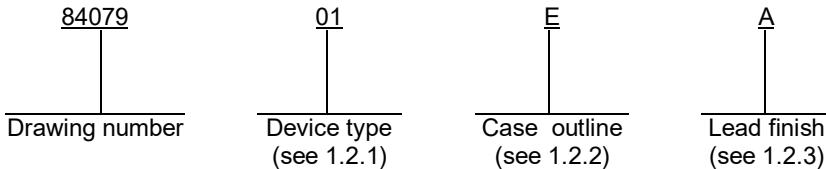
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REV														
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REV STATUS OF SHEETS	REV	H	H	H	H	H	H	H	H	H	H			
PMIC N/A	SHEET	1	2	3	4	5	6	7	8	9	10			
STANDARD MICROCIRCUIT DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE	PREPARED BY David W. Queenan				DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 https://www.dla.mil/LandandMaritime									
	CHECKED BY D. A. DiCenzo													
	APPROVED BY N. A. Hauck				MICROCIRCUIT, DIGITAL, ADVANCED LOW POWER SCHOTTKY TTL, DECADE COUNTER, MONOLITHIC SILICON									
	DRAWING APPROVAL DATE 31 July 1984													
	REVISION LEVEL H				SIZE A	CAGE CODE 14933	84079			SHEET 1 OF 10				

1. SCOPE

1.1 Scope. This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.

1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:



1.2.1 Device type. The device type identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	54ALS162	Synchronous 4-bit decade counter with synchronous clear

1.2.2 Case outlines. The case outlines are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
E	GDIP1-T16 or CDIP2-T16	16	dual-in-line
F	GDFP2-F16 or CDFP3-F16	16	flat
2	CQCC1-N20	20	square chip carrier

1.2.3 Lead finish. The lead finish is as specified in MIL-PRF-38535, appendix A.

1.3 Absolute maximum ratings.

Supply voltage range	-0.5 V dc to +7.0 V dc
Input voltage range	-1.5 V dc at -18 mA to +7.0 V dc
Storage temperature range	-65°C to +150°C
Maximum power dissipation (P_D) 1/	115 mW
Lead temperature (soldering, 10 seconds)	+300°C
Thermal resistance, junction-to-case (θ_{JC})	See MIL-STD-1835
Junction temperature (T_J)	+175°C

1.4 Recommended operating conditions.

Maximum low level output current (I_{OL})	4.0 mA
Supply voltage (V_{CC})	4.5 V minimum to 5.5 V dc maximum
Minimum high level input voltage (V_{IH})	2.0 V dc
Maximum low level input voltage (V_{IL})	0.7 V dc
Normalized fanout (each output):	
Low level	20 maximum
High level	10 maximum
Input clock frequency (f_{MAX})	22 MHz
Minimum width of clock pulse (t_P clock)	20 ns

1/ Must withstand the added P_D due to short circuit test; (e.g., I_o).

Minimum setup times before clock ↑:	
DATA	20 ns
Minimum synchronous clear setup time:	
(Low)	20 ns
(inactive)	20 ns
Minimum enable P/enable T setup time	30 ns
Hold times (t_{HOLD})	0 ns
Case operating temperature range (T_c)	-55°C to + 125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://quicksearch.dla.mil>)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

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**SIZE
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3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used. This drawing has been modified to allow the manufacturer to use the alternate die/fabrication requirements of paragraph A.3.2.2 of MIL-PRF-38535.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535, appendix A and herein.

3.2.1 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.3 Logic diagram. The logic diagram shall be as specified on figure 2.

3.2.4 Truth table. The truth table shall be as specified on figure 3.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full case operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked.

3.5.1 Certification/compliance mark. A compliance indicator "C" shall be marked on all non-JAN devices built in compliance to MIL-PRF-38535, appendix A. The compliance indicator "C" shall be replaced with a "Q" or "QML" certification mark in accordance with MIL-PRF-38535 to identify when the QML flow option is used.

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DLA Land and Maritime -VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DLA Land and Maritime -VA shall be required for any change that affects this drawing.

3.9 Verification and review. DLA Land and Maritime, DLA Land and Maritime 's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

STANDARD MICROCIRCUIT DRAWING DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE A		84079
		REVISION LEVEL H	SHEET 4

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_{\text{C}} \leq +125^{\circ}\text{C}$ unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
High level output voltage	V_{OH}	$V_{\text{CC}} = 4.5 \text{ V}$, $I_{\text{OH}} = -400 \mu\text{A}$, $V_{\text{IN}} = 0.7 \text{ V}$ or 2.0 V	1, 2, 3	01	2.5		V
Low level output voltage	V_{OL}	$V_{\text{CC}} = 4.5 \text{ V}$, $I_{\text{OL}} = 4 \text{ mA}$, $V_{\text{IN}} = 0.7 \text{ V}$ or 2.0 V	1, 2, 3	01		0.4	V
Input clamp voltage	V_{IC}	$V_{\text{CC}} = 4.5 \text{ V}$, $T_{\text{C}} = +25^{\circ}\text{C}$, $I_{\text{IN}} = -18 \text{ mA}$	1	01		-1.5	V
Low level input current	I_{IL}	$V_{\text{CC}} = 5.5 \text{ V}$, $V_{\text{IN}} = 0.4 \text{ V}$	1, 2, 3	01		-200	μA
High level input current	I_{IH1}	$V_{\text{CC}} = 5.5 \text{ V}$, $V_{\text{IN}} = 2.7 \text{ V}$	LOAD CLK or ENT	1, 2, 3	01	40	μA
			All other inputs	1, 2, 3	01	20	μA
	I_{IH2}	$V_{\text{CC}} = 5.5 \text{ V}$, $V_{\text{IN}} = 7.0 \text{ V}$	LOAD CLK or ENT	1, 2, 3	01	200	μA
			All other inputs	1, 2, 3	01	100	μA
Short circuit current	I_{o}	$V_{\text{CC}} = 5.5 \text{ V}$, $V_{\text{IN}} = 2.25 \text{ V}$ <u>1/</u> (all outputs)	1, 2, 3	01	-20	-112	mA
Supply current	I_{CC}	$V_{\text{CC}} = 5.5 \text{ V}$	1, 2, 3	01		25	mA
Maximum input A, clock, or count up frequency	f_{MAX}	$V_{\text{CC}} = 5.0 \text{ V}$, $C_{\text{L}} = 50 \text{ pF} \pm 10\%$, $R_{\text{L}} = 500 \Omega \pm 5\%$	9, 10, 11	01	22		MHz
Propagation delay time, CLK to Q	t_{PLH1}		9, 10, 11	01		22	ns
	t_{PHL1}		9, 10, 11	01		28	ns
Propagation delay time, CLK to RCO	t_{PLH2}		9, 10, 11	01		34	ns
	t_{PHL2}		9, 10, 11	01		27	ns
Propagation delay time, ENT to RCO	t_{PLH3}		9, 10, 11	01		20	ns
	t_{PHL3}		9, 10, 11	01		16	ns
Functional tests		See 4.3.1c	7	01			

1/ The output conditions have been chosen to produce a current that closely approximates one-half of the true short-circuit output current, I_{os} .

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**SIZE
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84079

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SHEET
5

Case outlines	E and F	2
Terminal number	Terminal symbol	
1	CLR	NC
2	CLK	CLR
3	A	CLK
4	B	A
5	C	B
6	D	NC
7	ENP	C
8	GND	D
9	LOAD	ENP
10	ENT	GND
11	Q _D	NC
12	Q _C	LOAD
13	Q _B	ENT
14	Q _A	Q _D
15	RCO	Q _C
16	V _{CC}	NC
17	---	Q _B
18	---	Q _A
19	---	RCO
20	---	V _{CC}

FIGURE 1. Terminal connections.

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**SIZE
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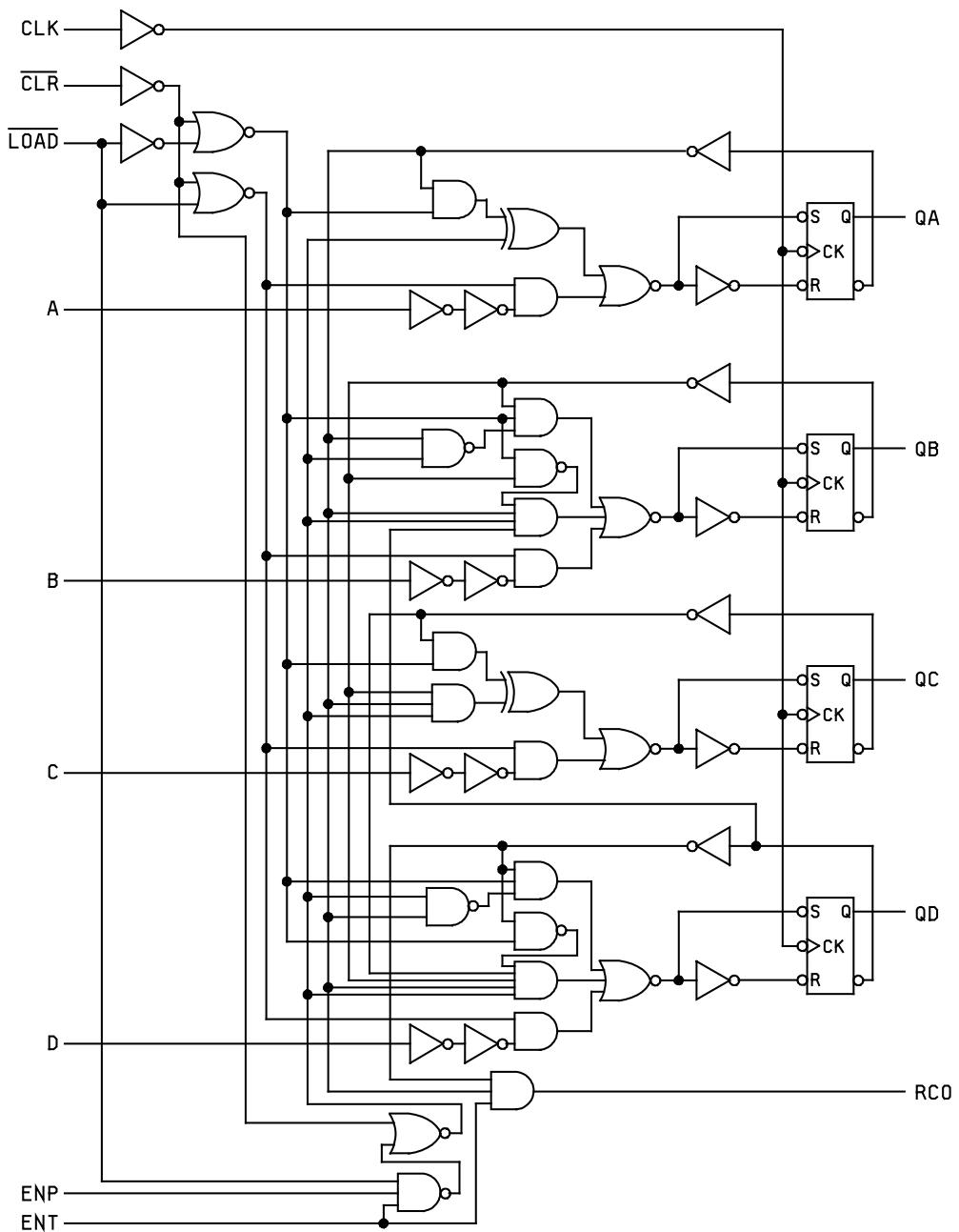


FIGURE 2. Logic diagram.

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**SIZE
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REVISION LEVEL
H

SHEET

7

Inputs at time t_n										Outputs at time t_{n+1}				
Clock	Enable P	Enable T	<u>LOAD</u>	A	B	C	D	<u>CLEAR</u>	Q _A	Q _B	Q _C	Q _D	Carry output	
CP	L	X	H	X	X	X	X	H	NC	NC	NC	NC	NC	
CP	X	L	H	X	X	X	X	H	NC	NC	NC	NC	L	
CP	H	H	H	X	X	X	X	H	Previous count plus 1					
CP	X	H	L	X	X	X	X	H	A	B	C	D	H if count = 9, L if count < 9	
CP	X	L	L	X	X	X	X	H	A	B	C	D	L	
CP	X	X	X	X	X	X	X	L	L	L	L	L	L	

L = low level voltage

H = high level voltage

X = irrelevant

CP = clock pulse

NC = no change

FIGURE 3. Truth table.

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**SIZE
A**

84079

REVISION LEVEL
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**SHEET
8**

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*, 2, 3, 9
Group A test requirements (method 5005)	1, 2, 3, 7, 9, 10, 11**
Groups C and D end-point Electrical parameters (method 5005)	1, 2, 3

* PDA applies to subgroup 1.

** Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.

(2) $T_A = +125^\circ\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

**STANDARD
MICROCIRCUIT DRAWING**
DLA LAND AND MARITIME
COLUMBUS, OHIO 43218-3990

**SIZE
A**

84079

REVISION LEVEL
H

SHEET
9

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, 6 and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.
- c. Subgroups 7 shall include verification of the truth table.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.
 - (2) $T_A = +125^\circ\text{C}$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.4 Record of users. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DLA Land and Maritime -VA, telephone (614) 692-8108.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime -VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0540.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-HDBK-103 and QML-38535. The vendors listed in MIL-HDBK-103 and QML-38535 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DLA Land and Maritime -VA.

STANDARD MICROCIRCUIT DRAWING DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE A	REVISION LEVEL H	84079
			10

STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 20-08-03

Approved sources of supply for SMD 84079 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime -VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <https://landandmaritimeapps.dla.mil/Programs/Smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>	Reference military specification PIN
8407901EA	57300	AS54ALS162C16/883C	M38510/38102BEX
	3V146	54ALS162B/BEA	
8407901FA	3V146	54ALS162B/BFA	M38510/38102BFX
	<u>3/</u>	SNJ54ALS162AW	
84079012A	3V146	54ALS162B/B2A	M38510/38102B2X
	<u>3/</u>	SNJ54ALS162AFK	

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ No longer available from an approved source of supply.

<u>Vendor CAGE number</u>	<u>Vendor name and address</u>
57300	Micross components 7725 N. Orange Blossom trail Orlando, FL 32810-2696
3V146	Rochester Electronics Inc. 16 Malcolm Hoyt Drive Newburyport, MA 01950

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.