

---

**PART NUMBER****54H62JB-ROCV**

---

**Rochester Electronics****Manufactured Components**

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer. (OCM)

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

**Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
  - Class Q Military
  - Class V Space Level

**Qualified Suppliers List of Distributors (QSLD)**

- Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

---

*The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.*

✓ **54H/74H62** 011187

### 3-2-2-3-INPUT AND-OR EXPANDER

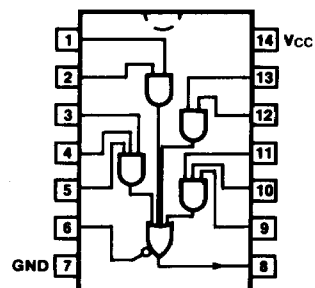
**ORDERING CODE:** See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0 \text{ V} \pm 5\%$ , $T_A = 0^\circ \text{C to } +70^\circ \text{C}$	$V_{CC} = +5.0 \text{ V} \pm 10\%$ , $T_A = -55^\circ \text{C to } +125^\circ \text{C}$	
Plastic DIP (P)	A	74H62PC		9A
Ceramic DIP (D)	A	74H62DC	54H62DM	6A
Flatpak (F)	B	74H62FC	54H62FM	3I

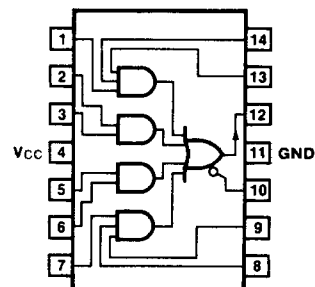
**INPUT LOADING/FAN-OUT:** See Section 3 for U.L. definitions

PINS	54/74H (U.L.) HIGH/LOW
Inputs	1.25/1.25
Outputs <sup>1</sup>	Note 2

#### CONNECTION DIAGRAMS PINOUT A



#### PINOUT B



#### DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

SYMBOL	PARAMETER	54/74H		UNITS	CONDITIONS <sup>3</sup>	
		Min	Max			
V <sub>ON</sub>	Output ON Voltage		0.4	V	T = -55°C I <sub>ON</sub> = 5.85 mA	V <sub>CC</sub> = Min, V <sub>IN</sub> = 2.0 V, V <sub>I</sub> = 1.0 V
			0.4		T <sub>A</sub> = 0°C I <sub>ON</sub> = 6.3 mA	
V <sub>ON</sub>	Output ON Voltage		0.4	V	T <sub>A</sub> = +125°C I <sub>ON</sub> = 7.85 mA	V <sub>CC</sub> = Max, V <sub>IN</sub> = 2.0 V, V <sub>I</sub> = 0.6 V
			0.4		T <sub>A</sub> = +70°C I <sub>ON</sub> = 7.4 mA	
I <sub>OFF</sub>	Output OFF Current		320	μA	T <sub>A</sub> = -55°C	V <sub>CC</sub> = Min, V <sub>IN</sub> = 0.8 V, V <sub>I</sub> = 4.5 V, R = 575 Ω
			570		T <sub>A</sub> = 0°C	
I <sub>ON</sub>	Output ON Current	-470 -600		μA	T <sub>A</sub> = -55°C	V <sub>CC</sub> = Min, V <sub>IN</sub> = 2.0 V, V <sub>I</sub> = 1.0 V
					T <sub>A</sub> = 0°C	
I <sub>CC(ON)</sub> I <sub>CC(OFF)</sub>	Power Supply Current		7.0	mA	V <sub>IN</sub> = Open	V <sub>CC</sub> = Max, V <sub>I</sub> = 0.85 V
			9.0		V <sub>IN</sub> = Gnd	

1. A maximum of one expander may be connected to one expandable AND-OR-Invert gate

2. Expander Outputs

3. V<sub>I</sub> is applied to x output terminal during test

**OUTPUT CAPACITANCE: V<sub>CC</sub> and Ground Terminals Open**

SYMBOL	PARAMETER	54/74H		UNITS	CONDITIONS
		Min	Max		
C <sub>X</sub>	Effective Capacitance of Output Transistor Q <sub>1</sub>		1.3*	pF	f = 1.0 MHz, T <sub>A</sub> = +25°C

\*Typical Value