

15W/8Ωx2ch

デジタルパワーアンプ

Digital Power Amplifier with
15W/8Ωx2ch

R2A15108FP**

11~24V電源に対応し、15W+15Wの連続出力が可能

従来品より部品を大幅に削減

It corresponds to 11~19V power supply, and the continuation output of 15W+15W is possible. Reduce many parts from former.

10W/8Ωx2ch

デジタルパワーアンプ

Digital Power Amplifier with
10W/8Ωx2ch

R2A15108SN**

11~24V電源に対応し、片面1層基板上で

10W+10Wの連続出力が可能

It corresponds to 11~19V power supply, and the continuation output of 10W+10W is possible at the single layer board.

30W/4Ωx2ch

デジタルパワーアンプ

Digital Power Amplifier with
30W/4Ωx2ch

R2A15108SP**

11~24V電源に対応し、外部ヒートシンクを使用することで

30W+30Wの出力が可能

It corresponds to 11~19V power supply, and the output of 30W+30W is possible by using an external heat sink.

15W/8Ωx2ch

デジタルパワーアンプ

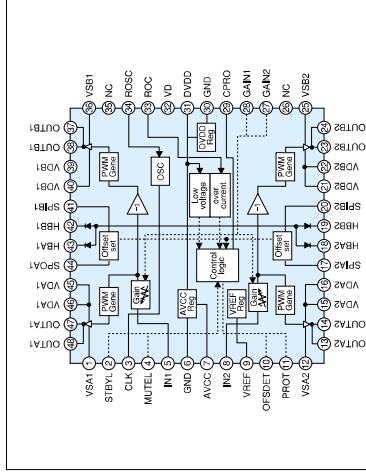
Digital Power Amplifier with
15W/8Ωx2ch

R2A15113FP**

11~19V電源に対応し、PWM入力型15W+15Wの

連続出力が可能。従来品より部品を大幅に削減

It corresponds to 11~19V power supply, and the continuation output of 15W+15W is possible by PWM input. Reduce many parts from former.



■ 特長

- 出力パワー：15Wx2ch @ RL=8Ω, VD=15V, THD=10%
(注)連続10W(以上)x2ch時は、露出ダイパッドへの最高放熱部分への半田接続、または、外部放熱器の装着が必要です。
- 出力ドライバ用Nch-MOSFETをBTL2ch分内蔵
- 過電流、過熱、減電圧の各種検出機能を内蔵
- 出力DCオフセット検出回路を内蔵
- 過電流検出レベルを外部抵抗で調整可能
- キャリア周波数を外部抵抗で調整可能
- ゲイン設定はプログラマブルに4段階切換可能

■ Features

- Output power: 15Wx2ch when RL=8Ω, VD=15V, and THD=10%
Note that when continuously operated at 10W(or more)x2ch, soldering on-board heating part to exposed die pad or mounting an external radiator is required.
- Built-in Nch MOSFET for 2 channels of BTL for output drivers
- Built-in various detection function for overcurrent, overheat, and low voltage
- Built-in output DC offset detection circuit
- Built-in monitor pin for error detection
- Overcurrent detection level adjustable by external resistor
- Carrier frequency adjustable by external resistor
- Gain setting changeable for programmable 4 stages

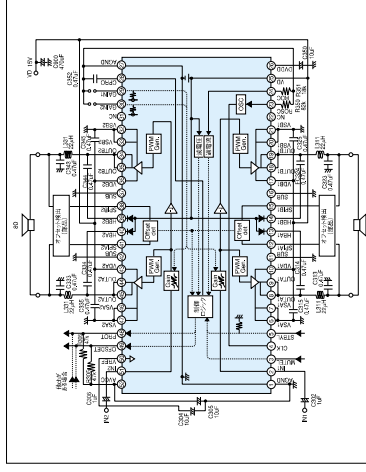
■ 標準性能

- 電源電圧：単一電源動作 11V ~ 24V推奨
- スピーカ(公称インピーダンス値): 4~8Ω ●パッケージ: 48ピンHQFP

■ Characteristics

- Power-supply voltage: single-power operation with recommended voltage of 11 to 24 V
- Speaker Impedance: operating from 4 to 8Ω
- Package: 48pin HQFP

**Under Development



■ 特長

- 出力パワー：30Wx2ch @ RL=4Ω, VD=17V, THD=10%
(注)外部放熱器の装着が必要です。
- 出力ドライバ用Nch-MOSFETをBTL2ch分内蔵
- 過電流、過熱、減電圧の各種検出機能を内蔵
- 出力DCオフセット検出回路を内蔵
- 過電流検出レベルを外部抵抗で調整可能
- キャリア周波数を外部抵抗で調整可能
- ゲイン設定はプログラマブルに4段階切換可能

■ Features

- Output power: 30Wx2ch when RL=4Ω, VD=17V, and THD=10%
Note that mounting an external radiator is required.
- Built-in Nch MOSFET for 2 channels of BTL for output drivers
- Built-in various detection function for overcurrent, overheat, and low voltage
- Built-in output DC offset detection circuit
- Built-in monitor pin for error detection
- Overcurrent detection level adjustable by external resistor
- Carrier frequency adjustable by external resistor
- Gain setting changeable for programmable 4 stages

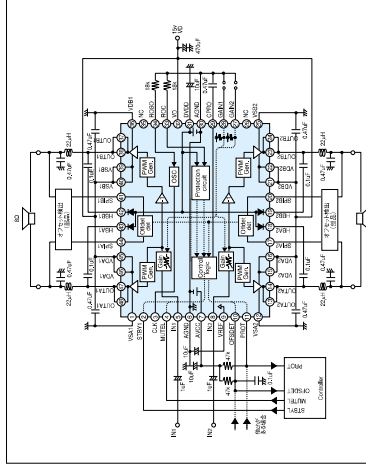
■ 標準性能

- 電源電圧：単一電源動作 11V ~ 24V推奨
- スピーカ(公称インピーダンス値): 4~8Ω ●パッケージ: 52ピンSSOP

■ Characteristics

- Power-supply voltage: single-power operation with recommended voltage of 11 to 24 V
- Speaker Impedance: operating from 4 to 8Ω
- Package: 52pin SSOP

**Under Development



■ 特長

- PWM入力対応
- 出力パワー：15Wx2ch @ RL=8Ω, VD=18V, THD=1%
●出力ドライバ用Nch-MOSFETをBTL2ch分内蔵
- 過電流、過熱、減電圧の各種検出機能を内蔵
- 出力DCオフセット検出回路を内蔵
- 過電流検出レベルを外部抵抗で調整可能
- ゲイン設定はプログラマブルに4段階切換可能

■ Features

- Correspond to PWM input
- Output power: 15Wx2ch when RL=8Ω, VD=18V, and THD=1%
Note that mounting an external radiator is required.
- Built-in Nch MOSFET for 2 channels of BTL for output drivers
- Built-in various detection function for overcurrent, overheat, and low voltage
- Built-in output DC offset detection circuit
- Built-in monitor pin for error detection
- Overcurrent detection level adjustable by external resistor
- Gain setting changeable for programmable 4 stages

■ 標準性能

- 電源電圧：単一電源動作 11V ~ 19V推奨
- スピーカ(公称インピーダンス値): 4~8Ω ●パッケージ: 48ピンHQFP

■ Characteristics

- Power-supply voltage: single-power operation with recommended voltage of 11 to 19 V
- Speaker Impedance: operating from 4 to 8Ω
- Package: 48pin HQFP

**Under Development