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**PART NUMBER****74HC804N-ROCV**

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**Rochester Electronics****Manufactured Components**

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer. (OCM)

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

**Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
  - Class Q Military
  - Class V Space Level

**Qualified Suppliers List of Distributors (QSLD)**

- Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

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*The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.*

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# SN74AS1804 HEX 2-INPUT NAND DRIVER

SDAS042C – AUGUST 1984 – REVISED JANUARY 1995

- High Capacitive-Drive Capability
- Typical Delay Time of 2.6 ns ( $C_L = 50$  pF) and Typical Power Dissipation of Less Than 9 mW Per Gate
- Center  $V_{CC}$  and GND Configuration Provides Minimum Lead Inductance in High-Current Switching Applications
- Package Options Include Plastic Small-Outline (DW) Packages and Standard Plastic (N) 300-mil DIPs

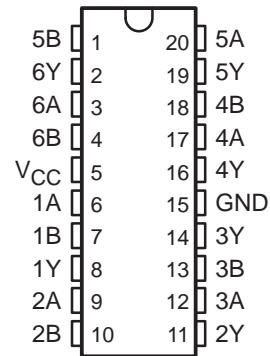
## description

This device contains six independent 2-input NAND drivers. It performs the Boolean functions  $Y = \overline{A \cdot B}$  or  $Y = \overline{A + B}$  in positive logic.

The center-pin configuration reduces lead inductance when compared to the 'AS804B. The reduced lead inductance minimizes noise generated onto either the  $V_{CC}$  or GND bus. This reduction is significant in high-current switching applications.

The SN74AS1804 is characterized for operation from 0°C to 70°C.

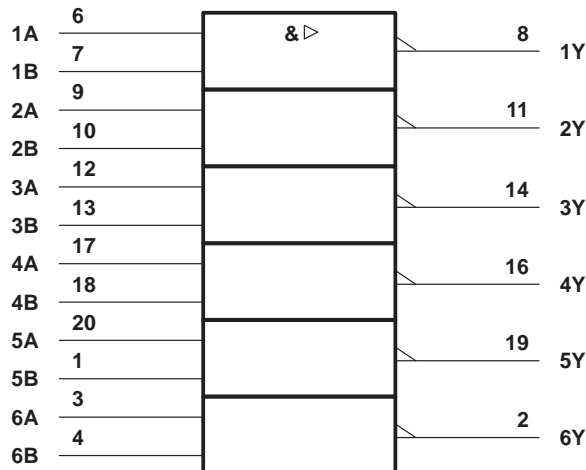
DW OR N PACKAGE  
(TOP VIEW)



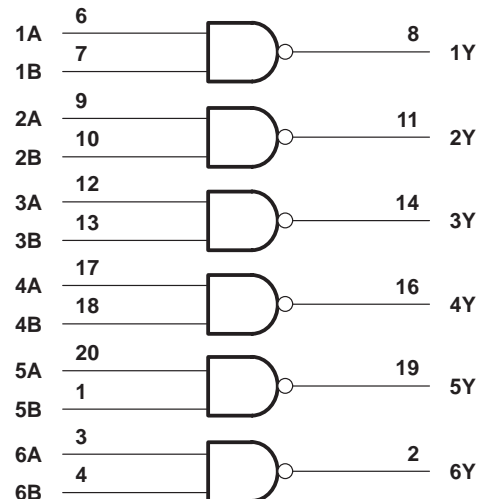
FUNCTION TABLE  
(each driver)

INPUTS		OUTPUT
A	B	Y
H	H	L
L	X	H
X	L	H

## logic symbol†



## logic diagram (positive logic)



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

# SN74AS1804

## HEX 2-INPUT NAND DRIVER

SDAS042C – AUGUST 1984 – REVISED JANUARY 1995

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, $V_{CC}$	7 V
Input voltage, $V_I$	7 V
Operating free-air temperature range, $T_A$	0°C to 70°C
Storage temperature range	–65°C to 150°C

† Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

### recommended operating conditions‡

	MIN	NOM	MAX	UNIT
$V_{CC}$ Supply voltage	4.5	5	5.5	V
$V_{IH}$ High-level input voltage	2			V
$V_{IL}$ Low-level input voltage			0.8	V
$I_{OH}$ High-level output current			–48	mA
$I_{OL}$ Low-level output current			48	mA
$T_A$ Operating free-air temperature	0		70	°C

‡ This high sink- or source-current device is not recommended for use above 40 MHz.

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP§	MAX	UNIT
$V_{IK}$	$V_{CC} = 4.5$ V, $I_I = -18$ mA			–1.2	V
$V_{OH}$	$V_{CC} = 4.5$ V to 5.5 V, $I_{OH} = -2$ mA	$V_{CC} - 2$			V
	$V_{CC} = 4.5$ V, $I_{OH} = -3$ mA	2.4	3.2		
		2			
$V_{OL}$	$V_{CC} = 4.5$ V, $I_{OL} = 48$ mA		0.35	0.5	V
$I_I$	$V_{CC} = 5.5$ V, $V_I = 7$ V			0.1	mA
$I_{IH}$	$V_{CC} = 5.5$ V, $V_I = 2.7$ V			20	µA
$I_{IL}$	$V_{CC} = 5.5$ V, $V_I = 0.4$ V			–0.5	mA
$I_O^¶$	$V_{CC} = 5.5$ V, $V_O = 2.25$ V	–50		–200	mA
$I_{CCH}$	$V_{CC} = 5.5$ V, $V_I = 0$		3.5	5	mA
$I_{CCL}$	$V_{CC} = 5.5$ V, $V_I = 4.5$ V		16	27	mA

§ All typical values are at  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$ .

¶ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current,  $I_{OS}$ .

### switching characteristics (see Figure 1)

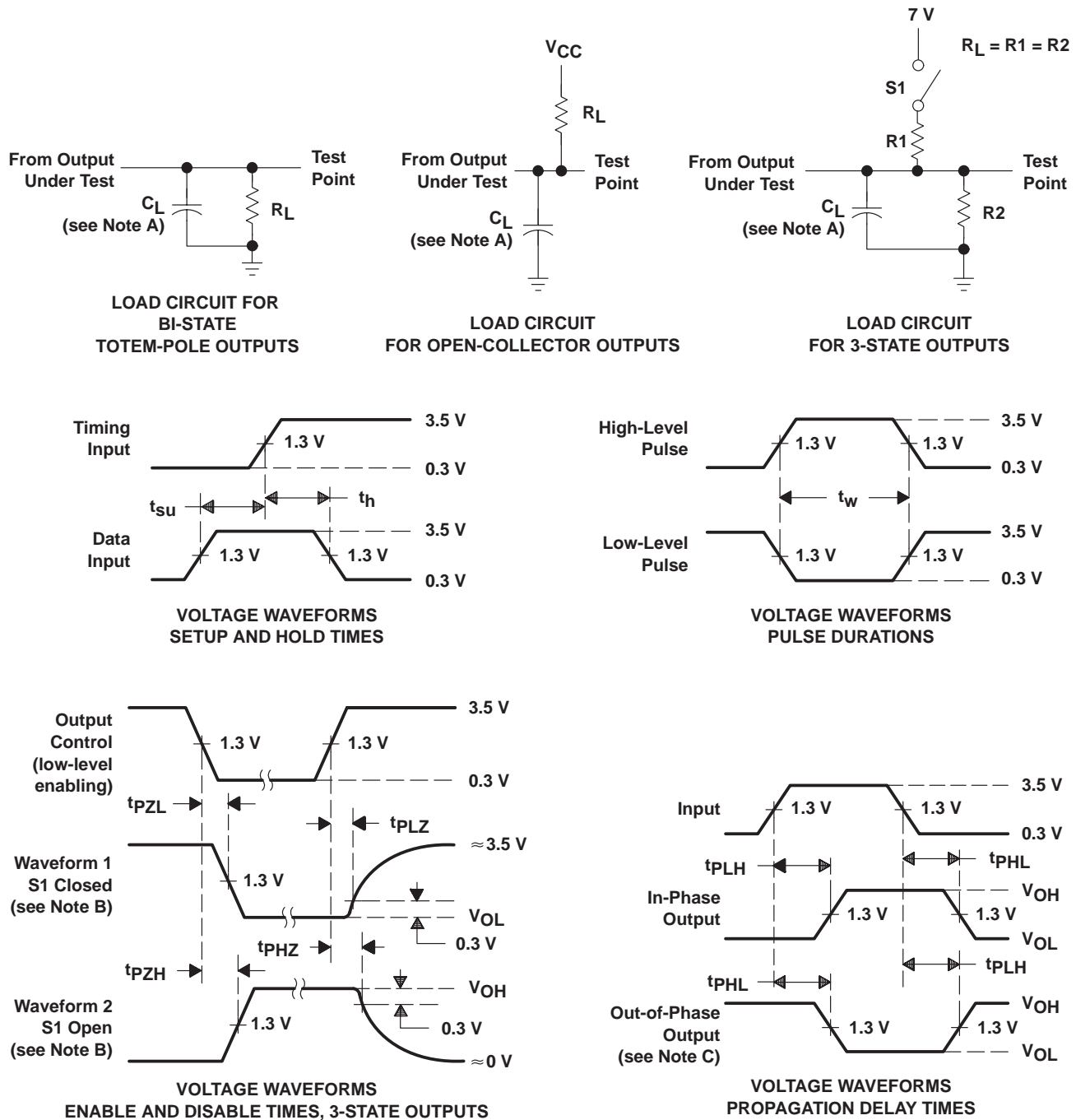
PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5$ V to 5.5 V, $C_L = 50$ pF, $R_L = 500 \Omega$ , $T_A = \text{MIN to MAX}^\#$		UNIT
			MIN	MAX	
$t_{PLH}$	A or B	Y	1	4	ns
$t_{PHL}$			1	4	

# For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.



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PARAMETER MEASUREMENT INFORMATION  
SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



- NOTES: A.  $C_L$  includes probe and jig capacitance.  
B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.  
C. When measuring propagation delay items of 3-state outputs, switch S1 is open.  
D. All input pulses have the following characteristics:  $PRR \leq 1$  MHz,  $t_r = t_f = 2$  ns, duty cycle = 50%.  
E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms

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