

### PART NUMBER 54LS28JB-ROCV

## Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer. (OCM)

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

#### **Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
  - Class Q Military
  - Class V Space Level

Qualified Suppliers List of Distributors (QSLD)

 Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

INCH-POUND
MIL-M-38510/302D
10 January 2003
SUPERSEDING
MIL-M-38510/302C
9 August 1983

#### MILITARY SPECIFICATION

### MICROCIRCUITS, DIGITAL, BIPOLAR LOW-POWER SCHOTTKY TTL, BUFFERS, MONOLITHIC SILICON

Inactive for new design after 18 April 1997.

This specification is approved for use by all Departments and Agencies of the Department of Defense.

#### 1. SCOPE

- 1.1 <u>Scope.</u> This specification covers the detail requirements for monolithic silicon, low-power Schottky TTL, positive buffer microcircuits. Two product assurance classes and a choice of case outlines and lead finishes are provided for each type and are reflected in the complete part number. For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535, (see 6.3).
  - 1.2 Part number. The part number shall be in accordance with MIL-PRF-38535, and as specified herein.
  - 1.2.1 <u>Device types</u>. The device types shall be as follows:

Device type	<u>Circuit</u>
01	Dual, 4-input, positive NAND buffer
02	Quadruple, 2-input positive NAND buffer
03	Quadruple, 2-input positive NAND buffer (open collector output)
04	Quadruple, 2-input positive NOR buffer

- 1.2.2 Device class. The device class shall be the product assurance level as defined in MIL-PRF-38535.
- 1.2.3 Case outlines. The case outlines shall be as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style
Α	GDFP5-F14 or CDFP6-F14	14	Flat pack
В	GDFP4-14	14	Flat pack
С	GDIP1-T14 or CDIP2-T14	14	Dual-in-line
D	GDFP1-F14 or CDFP2-F14	14	Flat pack
2	CQCC1-N20	20	Square leadless chip carrier

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAS, 3990 East Broad St., Columbus, OH 43216-5000, by using the self addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

AMSC N/A FSC 5962

#### 1.3 Absolute maximum ratings.

Supply voltage range	
Input voltage range	-1.5 V dc at -18 mA to +5.5 V dc
Storage temperature range	-65° to +150°C
Maximum power dissipation, (P <sub>D</sub> ) 1/	17 mW dc per buffer
Lead temperature (soldering, 10 seconds)	+300°C
Thermal resistance, junction to case ( $\theta_{JC}$ ):	
Cases A, B, C, D, and 2	(See MIL-STD-1835)
Junction temperature (T <sub>J</sub> )	+175°C <u>2</u> /

#### 1.4 Recommended operating conditions.

Supply voltage (V <sub>CC</sub> )	+4.5 V dc minimum to 5.5 V dc
	maximum
Minimum high level input voltage (V <sub>IH</sub> )	+2.0 V
Maximum low level input voltage (V <sub>IL</sub> )	+0.7 V
Normalized fanout (each output) 3/	30 maximum
Case operating temperature range (T <sub>C</sub> )	-55° to +125°C

#### 2. APPLICABLE DOCUMENTS

#### 2.1 Government documents.

2.1.1 <u>Specifications and Standards</u>. The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Departments of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation.

#### **SPECIFICATION**

#### DEPARTMENT OF DEFENSE

MIL-M-38510 - Microcircuits, General Specification for.

MIL-PRF-38535 - Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

#### **STANDARDS**

#### DEPARTMENT OF DEFENSE

MIL-STD-883 - Test Method Standard for Microelectronics.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines

(Copies of the specification and standard required by contractors in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 <u>Order of precedence.</u> In the event of a conflict between the text of this specification and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

 $<sup>\</sup>underline{1}$ / Must withstand the added  $P_D$  due to short-circuit test (e.g.,  $I_{OS}$ ).

<sup>2/</sup> Maximum junction temperature shall not be exceeded except for allowable short duration burn-in screening condition per method 5004 of MIL-STD-883.

<sup>3/</sup> The device shall fanout in both high and low levels to the specified number of inputs for the same device type as that being tested.

#### 3. REQUIREMENTS

- 3.1 <u>Qualification</u>. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.4).
- 3.2 <u>Item requirements</u>. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.
- 3.3 <u>Design, construction, and physical dimensions.</u> The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein.
- 3.3.1 <u>Logic diagrams and terminal connections.</u> The logic diagrams and terminal connections shall be as specified on figure 1.
  - 3.3.2 Truth tables and logic equations. The truth tables and logic equations shall be as specified on figure 2.
- 3.3.3 <u>Schematic circuits.</u> The schematic circuits shall be maintained by the manufacturer and made available to the qualifying activity and the preparing activity (DSCC-VAS) upon request.
  - 3.3.4 Case outlines. The case outlines shall be as specified in 1.2.3.
  - 3.4 Lead material and finish. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).
- 3.5 <u>Electrical performance characteristics</u>. The electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range, unless otherwise specified.
- 3.6 <u>Electrical test requirements.</u> The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.
  - 3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.
- 3.8 <u>Microcircuit group assignment.</u> The devices covered by this specification shall be in microcircuit group number 9 (see MIL-PRF-38535, appendix A).

#### 4. VERIFICATION

- 4.1 <u>Sampling and inspection</u>. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.
- 4.2 <u>Screening.</u> Screening shall be in accordance with, MIL-PRF-38535 and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:
  - a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
  - b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
  - c. Additional screening for space level product shall be as specified in MIL-PRF-38535.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions 1/	Device	Li	imits	Unit
		-55°C ≤ T <sub>C</sub> ≤ +125°C	types	Min	Max	
High level output voltage	V <sub>OH</sub>	$V_{CC}$ = +4.5 V, $V_{IL}$ = +0.7 V, $I_{OH}$ = -1.2 mA	01, 02 04	2.5		V
Low level output voltage	V <sub>OL</sub>	$V_{CC}$ = +4.5 V, $I_{OL}$ = +12 mA, $V_{IH}$ = 2.0 V	All		0.4	V
Input clamp voltage	V <sub>IC</sub>	$V_{CC} = +4.5 \text{ V}, I_{IN} = -18 \text{ mA},$ $T_{C} = +25^{\circ}\text{C}$	All		-1.5	V
High level input current	I <sub>IH1</sub>	$V_{CC} = +5.5 \text{ V}, V_{IN} = +2.7 \text{ V}$	All		20	μА
	I <sub>IH2</sub>	V <sub>CC</sub> = +5.5 V, V <sub>IN</sub> = +5.5 V	All		100	μΑ
Low level input current	I <sub>IL</sub>	V <sub>CC</sub> = +5.5 V, V <sub>IL</sub> = +0.4 V	All	-5	-400	μА
Short circuit output current	Ios	V <sub>CC</sub> = +5.5 V <u>2</u> /	01, 02 04	-15	-130	mA
Maximum collector cut-off current	I <sub>CEX</sub>	$V_{CC} = +4.5 \text{ V}, V_{IN} = +0.7 \text{ V},$ $V_{OH} = +5.5 \text{ V}$	03		250	μА
High level supply	I <sub>CCH</sub>	$V_{CC} = +5.5 \text{ V}, V_{IN} = +0 \text{ V}$	01		1	mA
current			02, 03		2	
			04		3.6	
Low level supply	I <sub>CCL</sub>	$V_{CC} = +5.5 \text{ V}, V_{IN} = +5.5 \text{ V}$	01		6	mA
current			02, 03		12	
			04		13.8	
Propagation delay time	t <sub>PHL</sub>	$C_L = 125 \text{ pF} \pm 10\%,$	01, 02	2	30	ns
high-to-low level		$R_L = 667 \Omega \pm 5\%$	03	3	51	
			04	2	30	
Propagation delay time	t <sub>PLH</sub>	$C_L = 125 \text{ pF} \pm 10\%,$	01, 02	2	30	ns
low-to-high level		$R_L = 667 \Omega \pm 5\%$	03	3	56	
			04	2	30	

<sup>1/</sup> Complete terminal conditions shall be as specified in table III.  $\underline{2}/$  Not more than one output should be shorted at a time.

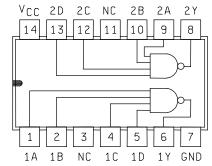
TABLE II. Electrical test requirements.

	Subgroups	(see table III)
MIL-PRF-38535 test requirements	Class S	Class B
	devices	devices
Interim electrical parameters	1	1
Final electrical test parameters	1*, 2, 3, 9, 10, 11	1*, 2, 3, 9
Group A test requirements	1, 2, 3, 9, 10, 11	1, 2, 3, 9,
Group C end-point electrical parameters	1, 2, 3, 5 9, 10, 11	1, 2, 3
Additional electrical subgroup for group C periodic inspections	N/A	10, 11
Group D end-point electrical parameters	1, 2, 3	1, 2, 3

<sup>\*</sup>PDA applies to subgroup 1.

- 4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.
- 4.4 <u>Technology Conformance Inspection (TCI)</u>. Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).
- 4.4.1 <u>Group A inspection.</u> Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:
  - a. Tests shall be as specified in table II herein.
  - b. Subgroups 4, 5, 6, 7, and 8 shall be omitted.
  - 4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of MIL-PRF-38535.
- 4.4.3 <u>Group C inspection.</u> Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:
  - a. End-point electrical parameters shall be as specified in table II herein.
  - b. A subgroup shall be added to the group C inspection requirements for class B devices and shall consist of the tests, conditions and limits specified for subgroups 10 and 11 of group A.
  - c. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
- 4.4.4 <u>Group D inspection.</u> Group D inspection shall be in accordance with table V of MIL-PRF-38535. End-point electrical parameters shall be as specified in table II herein.
  - 4.5 Methods of inspection. Methods of inspection shall be specified and as follows.
- 4.5.1 <u>Voltage and current.</u> All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

## DEVICE TYPE 01 CASES A,B,C,AND D



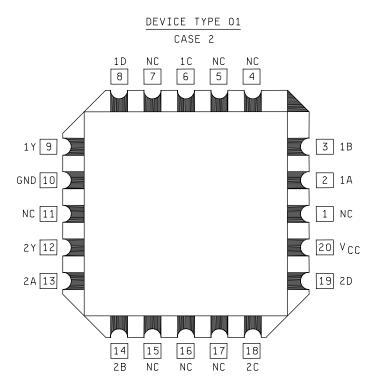
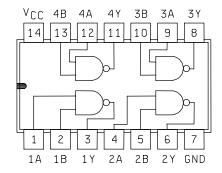


FIGURE 1. Logic diagram and terminal connections (top views).

### DEVICE TYPES 02 AND 03 CASES A,B,C,AND D



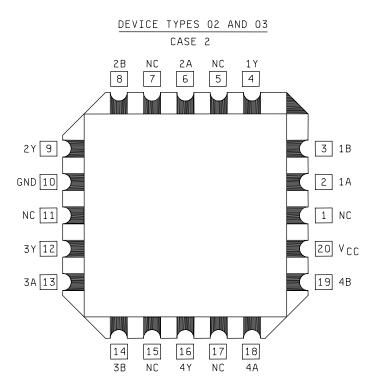


FIGURE 1. <u>Logic diagram and terminal connections (top views)</u> - Continued.

# DEVICE TYPE 04 CASES A,B,C,AND D VCC 4Y 4B 4A 3Y 3B 3 14 13 12 11 10 9 8

1B 2Y 2A 2B GND

3 | 4 | 5 | 6

1 Y 1 A

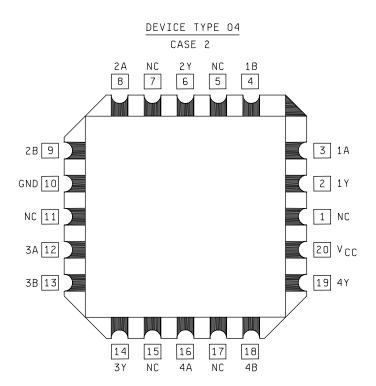


FIGURE 1. Logic diagram and terminal connections (top views) - Continued.

MIL-M-38510/302D

Device type 01

	Truth	n table each	n gate										
	Input												
Α	В	С	D	Output Y									
L	L	L	L	Н									
Н	L	L	L	Н									
L	Н	L	L	Н									
Н	Н	L	L	Н									
L	L	Н	L	Н									
Н	L	Н	L	Н									
L	Н	Н	L	Н									
Н	Н	Н	L	Н									
L	L	L	Н	Н									
Н	L	L	Н	Н									
L	Н	L	Н	Н									
Н	Н	L	Н	Н									
L	L	Η	Η	Н									
Н	L	Н	Н	Н									
L	Η	Н	Н	Н									
Н	Н	Н	Н	L									

Positive logic Y =  $\overline{ABCD}$ 

Device types 02 and 03

Truth table each gate											
In	Output										
Α	В	Υ									
L	L	Н									
Н	L	Н									
L	Н	Н									
Н	Н	L									

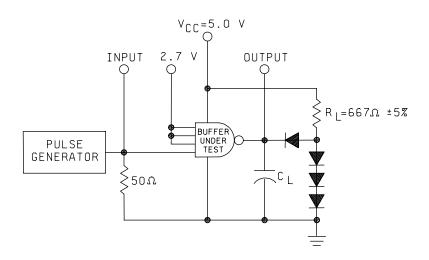
Positive logic  $Y = \overline{AB}$ 

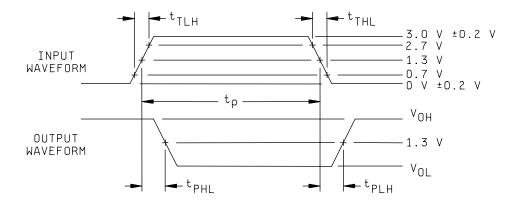
Device type 04

Truth table each gate											
In	Output										
Α	В	Υ									
L	L	Н									
Н	L	L									
L	Η	L									
Н	Н	L									

Positive logic  $Y = \overline{A + B}$ 

FIGURE 2. Truth tables and logic equations.

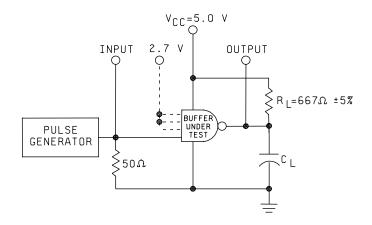


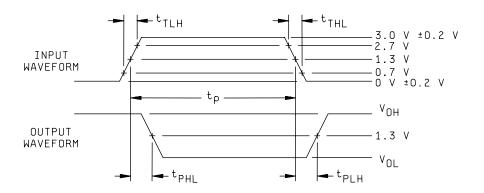


#### NOTES:

- 1. The pulse generator has the following characteristics: PRR  $\leq$  1 MHz,  $t_{TLH} \leq$  15 ns,  $t_{THL} \leq$  6 ns,  $t_{P}$  = 200 ns  $\pm 20$  ns and  $Z_{OUT}~\cong~50\Omega.$
- 2. Including scope probe, wiring, and stray capacitance, without package in test fixture,  $C_L$  = 125 pF  $\pm 10\%$ .
- 3. Voltage measurements are to be made with respect to network ground terminal.
- 4. All diodes are 1N3064 or equivalent.

FIGURE 3. Switching time test circuit and waveforms for device types 01 and 02.

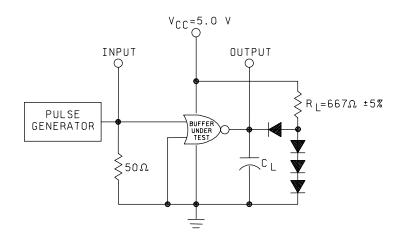


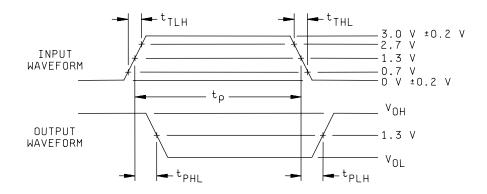


#### NOTES:

- 1. The pulse generator has the following characteristics: PRR  $\leq$  1 MHz,  $t_{TLH} \leq$  15 ns,  $t_{THL} \leq$  6 ns,  $t_{P}$  = 200 ns  $\pm 20$  ns and  $Z_{OUT}~\cong~50\Omega.$
- 2. Including scope probe, wiring, and stray capacitance, without package in test fixture,  $C_L$  = 125 pF  $\pm 10\%$ .
- 3. Voltage measurements are to be made with respect to network ground terminal.
- 4. All diodes are 1N3064 or equivalent.

FIGURE 4. Switching time test circuit and waveforms for device types 03.





#### NOTES:

- 1. The pulse generator has the following characteristics: PRR  $\leq$  1 MHz,  $t_{TLH} \leq$  15 ns,  $t_{THL} \leq$  6 ns,  $t_{P}$  = 200 ns  $\pm$ 20 ns and  $Z_{OUT} ~\cong~ 50\Omega.$
- 2. Including scope probe, wiring, and stray capacitance, without package in test fixture,  $C_L = 125 \text{ pF} \pm 10\%$ .
- 3. All diodes are 1N3064 or equivalent.
- 4. Voltage measurements are to be made with respect to network ground terminal.

FIGURE 5. Switching time test circuit and waveforms for device type 04.

TABLE III. Group A inspection for device type 01. Terminal conditions (pins not designated may be high  $\geq$  2.0 V, low  $\leq$  0.7 V, or open).

											be high										
			Cases	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
		MIL-STD-	A,B,C,D															ł., .l			
Subgroup	Symbol		Case <u>1</u> /	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured	Lin	nits	Unit
		method	2			110			434	0115	2) (			110			.,	terminal			
			Test no.	1A	1B	NC	1C	1D	1Y	GND	2Y	2A	2B	NC	2C	2D	V <sub>cc</sub>		Min	Max	<b></b>
1	V <sub>OL</sub>	3007	1	2.0 V	2.0 V		2.0 V	2.0 V	12 mA	GND		5.5 V	5.5 V		5.5 V	5.5 V	4.5 V	1Y		0.4	V
Tc = 25°C		3007	2	5.5 V	5.5 V		5.5 V	5.5 V		"	12 mA	2.0 V	2.0 V		2.0 V	2.0 V	"	2Y		0.4	
	ОН	3006	3	0.7 V	5.5 V		"	=	-1.2 mA	"		5.5 V	5.5 V		5.5 V	5.5 V	"	1Y	2.5		"
		"	4	5.5 V	0.7 V			=	"	"			"		"	"	-	1Y	-		-
		"	5	"	5.5 V		0.7 V	-		"			"		"	"	"	1Y	"		"
V		"	6	"	"		5.5 V	0.7 V		"			"		"	-	"	1Y	"		"
		"	7	"	"		"	5.5 V		u	-1.2 <sub>"</sub> mA	0.7 V	"		"	"	"	2Y	"		"
		"	8	"	"		"			"		5.5 V	0.7 V		"	"		2Y	"		"
		"	9	"	"		"			"		"	5.5 V		0.7 V	"	-	2Y	"		-
		"	10	"	"		"	"		íí	"	"	5.5 V		5.5 V	0.7 V	"	2Y	"		"
	IC		11	-18 mA						"								1A		-1.5	"
	.0		12		-18 mA					"								1B		"	"
			13				-18 mA			"								1C		"	"
V			14					-18 mA		"							"	1D		"	
V			15					10 11		"		-18 mA					"	2A		"	
			16							"		10 1117 (	-18 mA					2B			"
			17							"			10 111/1		-18 mA			2C			"
			18							"					-101117	-18 mA		2D			"
	os <u>2</u> /	3011	19	GND	GND		GND	GND	GND	66						10 111/1	5.5 V	1Y	-30	-130	mA
	os <u>2</u> /	3011	20	OND	OND		OIVD	OND	GIND	"	GND	GND	GND		GND	GND	J.J V	2Y	-30	-130	mA
		3010	21	2.7 V	GND					"	GIND	UND "	UND "		GIND	GIND		1A	-30	20	
1	IH1	3010						"		"					,					20	μA "
			22	GND	2.7 V		0.71/	"		"			-					1B			
			23		GND		2.7 V			"								1C			
			24	- "	- "		GND	2.7 V		"		0.71/			- "	- "	- "	1D		- "	
			25			"		GND		"	"	2.7 V						2A			
			26								"	GND	2.7 V					2B			
		"	27		"								GND		2.7 V			2C			
			28	"	"			"		"					GND	2.7 V	"	2D		"	
	IH2	"	29	5.5 V	"		"	"		"		"	"		"	GND	"	1A		100	
		"	30	GND	5.5 V			"		"	"		"		"	"	"	1B		"	
		"	31	"	GND		5.5 V	=		"	"		"		"	"	-	1C		"	"
1		"	32	"	"		GND	5.5 V		"			"		"	"	"	1D		"	"
		"	33	"	"			GND		"		5.5 V	"		"	"	-	2A		"	"
		"	34	"	"					"		GND	5.5 V		"	"	-	2B		"	"
		"	35	"	"			"		íí.			GND		5.5 V	"	"	2C		"	"
		"	36	"	"			"		"			GND		GND	5.5 V	"	2D		"	"
	ı∟ <u>3</u> /	3009	37	0.4 V	5.5 V	=	5.5 V	5.5 V		"		5.5 V	5.5 V		5.5 V	"	"	1A	-160	-400	"
		"	38	5.5 V	0.4 V	=	5.5 V	=		tt.		"	"		"	"	"	1B	"	"	"
		"	39	"	5.5 V		0.4 V	"		"	"		"		"	"	"	1C	"	"	"
I		"	40	"	"		5.5 V	0.4 V		tt.			"		"	"	"	1D	"	"	"
		"	41	"	"			5.5 V		"		0.4 V	"		"	"	"	2A	"	"	"
		"	42	"	"					"		5.5 V	0.4 V		"	"	"	2B	"	"	"
		"	43	"	"			"		er.			5.5 V		0.4 V	"	"	2C	"	"	"
		"	44	"	"			"		"			"		5.5 V	0.4 V	"	2D	"	"	"
	CCL	3005	45	"	"		5.5 V	5.5 V		"		"	"		5.5 V	5.5 V	"	V <sub>CC</sub>		6	mA
	CCH	3005	46	GND	GND			GND		"		GND	GND		GND	GND		V <sub>CC</sub>		1	"
2		sts, terminal				roup₁1 e	xcept T <sub>2</sub> =		and V <sub>IC</sub> te	sts are or	nitted								1		
3	Same to	sts, terminal	conditions a	and limite s	e for subc	roun 1 A	vcent T	55°C ar	nd Via test	s are omi	tted										
<del></del>	Janno to	o.o, tomma	22.101110110		.c .c. cabe	CNE	yop. 10 -	30 0 ai	VIC 1001	2 210 0/111											

See footnotes at end of device type 01

3

MIL-M-38510/302D

TABLE III. Group A inspection for device type 01 – Continued.

Terminal conditions (pins not designated may be high > 2.0 V, low < 0.7 V, or open).

					1 011	IIIIIai coi	iditionio (	Pillo Hot	accigna	ou may	oo mgii .	, ,	011 _ 0.7	v, o. op	011/1.						
		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol	883 method	Case <u>1</u> / 2	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lim	nits	Unit
			Test no.	1A	1B	NC	1C	1D	1Y	GND	2Y	2A	2B	NC	2C	2D	Vcc		Min	Max	l
9	t <sub>PHL</sub>	3003	47	IN	2.7 V		2.7 V	2.7 V	OUT	GND							5.0 V	1A to 1Y	2	25	ns
Tc = 25°C		Fig. 3	48							"	OUT	IN	2.7 V		2.7 V	2.7 V	"	2A to 2Y	"	"	"
	PLH	"	49	IN	2.7 V		2.7 V	2.7 V	OUT	"							"	1A to 1Y	"	"	"
			50							"	OUT	IN	2.7 V		2.7 V	2.7 V	"	2A to 2Y	"	"	"
10	t <sub>PHL</sub>		51	IN	2.7 V		2.7 V	2.7 V	OUT	"							"	1A to 1Y	=	30	"
Tc = 125°C		"	52							"	OUT	IN	2.7 V		2.7 V	2.7 V	"	2A to 2Y	"	"	"
	PLH	"	53	IN	2.7 V		2.7 V	2.7 V	OUT	"							"	1A to 1Y	"	"	"
		=	54							"	OUT	IN	2.7 V		2.7 V	2.7 V	"	2A to 2Y	"	"	"
11	Same tes	ts terminal	conditions at	nd limits a	s for suba	roup 10 e	excent To	55° C		·	·	·	·	·				·			

<sup>1/</sup> For case 2, pins not referenced are NC.

<sup>2/</sup> I<sub>OS</sub> limits for CKTS B, C, and D are -15/-100 mA.

 $<sup>\</sup>underline{3}/$  I<sub>IL</sub> limits for CKT B are -30 to -300  $\mu$ A; CKT C, -150 to -380  $\mu$ A; CKT D, -120 to -360  $\mu$ A; CKT F, -5 to -400  $\mu$ A.

TABLE III. Group A inspection for device type 02. Terminal conditions (pins not designated may be high  $\geq$  2.0 V, low  $\leq$  0.7 V, or open).

					rer	minai co	naitions	(pins no	t designa	tea may	/ be high	≥ 2.0 V,	$10W \le 0$ .	7 V, or o	pen).						
		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol		Case <u>1</u> /	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lim	nits	Unit
		metriod	Test no.	1A	1B	1Y	2A	2B	2Y	GND	3Y	3A	3B	4Y	4A	4B	V <sub>cc</sub>	terrinia	Min	Max	
1	Vol	3007	1	2.0 V	2.0 V	12 mA	5.5 V	5.5 V	21	GND	- 51	5.5 V	5.5 V	71	5.5 V	5.5 V	4.5 V	1Y	IVIIII	0.4	V
Tc = 25°C	VOL	3007	2	5.5 V	5.5 V	12 IIIA	2.0 V	2.0 V	12 mA	"		5.5 V	5.5 V		3.5 V	J.J V	4.5 V	2Y		"	"
10 = 25 C			3	3.5 V	3.5 V		5.5 V	5.5 V	12 IIIA	"	12 mA	2.0 V	2.0 V		"		"	3Y		"	"
		"	4	"	"		3.5 V	3.3 V		"	12 IIIA	5.5 V	5.5 V	12 mA	2.0 V	2.0 V	"	4Y		"	"
		3006	5	0.7 V	"	-1.2 mA	"			"		3.5 V	3.5 V	12 IIIA	5.5 V	5.5 V	u	1Y	2.5		
	ОН	3006	6	5.5 V	0.7 V	-1.2 mA	"			"			"		5.5 V	5.5 V	"	1 Y	2.5		"
			7	3.5 V	5.5 V	-1.2 IIIA	0.7 V		-1.2 mA	"			"		,	"	"	2Y	"		"
.,			8	"	3.5 V		5.5 V			"						"	"	2 Y	"		"
V			9	"	"		5.5 V	0.7 V 5.5 V	-1.2 mA	"	10 1	0.71/			,	"	"	3Y	"		"
					"		44	5.5 V		"	-1.2.mA	0.7 V	0.71/			"	"				"
			10 11				44	"		"	-1.2 mA	5.5 V	0.7 V 5.5 V	1.0 1	0.7 V	"	"	3Y 4Y	"		
			12	"			"	"			"			-1.2 mA		0.71/	"		"		"
						"				"			5.5 V	-1.2 mA	5.5 V	0.7 V	"	4Y		4.5	-
	I C		13	-18 mA	40 4												"	1A		-1.5	"
			14		-18 mA		40 4			"							"	1B		"	"
			15				-18 mA	40 4	"	"	"						"	2A		"	"
V			16					-18 mA		"		40 4					"	2B		"	"
			17									-18 mA	40. 4				"	3A			"
			18										-18 mA		40. 4		"	3B			-
			19												-18 mA		"	4A		"	"
		2211	20	0110	0115	0110				"						-18 mA		4B			
	os <u>2</u> /	3011	21	GND	GND	GND				"							5.5 V	1Y	-30	-130	mA
			22				GND	GND	GND	"	0110	0115	0115				"	2Y			
1 .			23								GND	GND	GND	0115	0115	0110	"	3Y	"		
' '			24	0.71/	OND		OND	OND		"		ONID	ONID	GND	GND	GND		4Y			
	IH1	3010	25	2.7 V	GND		GND	GND				GND	GND					1A		20	μA
			26	GND	2.7 V		GND	- "										1B			
			27	"	GND		2.7 V			"			"		- "	- "	"	2A			"
			28	"	"		GND "	2.7 V		"		0 = 17	"			- "	"	2B			"
			29	"	"		"	GND	ű	"	"	2.7 V			- "	- "	"	3A			"
			30	"	"		- :		"	"	"	GND	2.7 V					3B			"
			31	"				- "			"		GND		2.7 V	"		4A			
			32					"					"		GND "	2.7 V		4B			
	IH2		33	5.5 V	"		"	"					"			GND "	"	1A		100	"
			34	GND	5.5 V			"					"				"	1B			
			35		GND "		5.5 V			"			"					2A			"
		"	36	"	"	"	GND	5.5 V	"	"	"	5 5 V	"			- "	- "	2B			"
		[ ]	37	"	"		-	GND	ű	"	"	5.5 V				"	"	3A		"	"
			38	"	"				"	"	"	GND	5.5 V		· ·		- "	3B		"	"
			39	"	"					"	"	-	GND		5.5 V	551	"	4A		"	"
<u> </u>			40				E E \ /						GND		GND	5.5 V	"	4B	400		- "
	IL /	3009	41	0.4 V	5.5 V		5.5 V	5.5 V		"		5.5 V	5.5 V		5.5 V			1A	-160	-400	
	_/	[ ]	42	5.5 V	0.4 V	"	5.5 V	"			1		"			<u> </u>	- "	1B	"	"	"
			43	"	5.5 V	"	0.4 V			"			"				"	2A	"		"
I			44	"	"		5.5 V	0.4 V				0.417	"					2B	"		"
3		"	45		"			5.5 V		"		0.4 V				<u> </u>		3A	"		"
			46				.,		ű	"	"	5.5 V	0.4 V		0.111	<u> </u>		3B			"
			47	"	"					"	"	- "	5.5 V		0.4 V	0.417	" "	4A	"	"	"
			48					"							5.5 V	0.4 V		4B			

16

TABLE III. Group A inspection for device type 02 – Continued. Terminal conditions (pins not designated may be high  $\geq 2.0 \text{ V}$ , low  $\leq 0.7 \text{ V}$ , or open).

								(1-11-11-11-11-11-11-11-11-11-11-11-11-1					10W <u>-</u> 0.	,							
			Cases	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
		MIL-STD-	A,B,C,D																		1
Subgroup	Symbol	883	Case <u>1</u> /	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured	Lin	nits	Unit
		method	2															terminal			1
			Test no.	1A	1B	1Y	2A	2B	2Y	GND	3Y	3A	3B	4Y	4A	4B	$V_{CC}$		Min	Max	ı
1	I <sub>CCL</sub>	3005	49	5.5 V	5.5 V		5.5 V	5.5 V		GND		5.5 V	5.5 V		5.5 V	5.5 V	5.5 V	Vcc		12	mA
Tc = 25°C	I <sub>C C H</sub>	3005	50	GND	GND			GND		GND			GND		GND	GND	5.5 V	Vcc		2	mA
2	Same to	ests, terminal	conditions a	and limits	as for sub	group 1, e	except T <sub>C</sub>	= +125° C	, and V <sub>LC</sub>	tests are	omitted.										
3	Same to	ests, terminal	conditions a	and limits	as for sub	groupg1/v1g	xcept T <sub>C</sub>	= -55° C,	and V <sub>IC</sub> 1	tests are o	mitted <sub>ND</sub>	)									
9	t <sub>PHL</sub>	3003	51	IN	2.7 V	OUT				GND							5.0 V	1A to 1Y	2	25	ns
Tc = 25°C		Fig. 3	52				IN	2.7 V	OUT	"							"	2A to 2Y	"	"	"
		"	53							tt	OUT	IN	2.7 V				"	3A to 3Y	tt	"	ee .
		"	54							ű				OUT	IN	2.7 V	"	4A to 4Y	"	"	"
	PLH	"	55	IN	2.7 V	OUT				"							"	1A to 1Y	66		"
		"	56				IN	2.7 V	OUT	"							"	2A to 2Y	"	"	"
		"	57							"	OUT	IN	2.7 V				"	3A to 3Y	"	"	"
t		"	58							ű				OUT	IN	2.7 V	"	4A to 4Y	"	"	"
10	t <sub>PHL</sub>	"	59	IN	2.7 V	OUT				"							"	1A to 1Y	"	30	"
Tc = 125°C		"	60				IN	2.7 V	OUT	er.							"	2A to 2Y	tt.	"	66
		"	61							tt	OUT	IN	2.7 V				"	3A to 3Y	tt	"	ee .
		"	62							ű				OUT	IN	2.7 V	"	4A to 4Y	"	"	"
	PLH	"	63	IN	2.7 V	OUT				"							"	1A to 1Y	66		"
		"	64				IN	2.7 V	OUT	"							"	2A to 2Y	"	"	"
		"	65							"	OUT	IN	2.7 V				"	3A to 3Y	"	"	"
t		"	66							"				OUT	IN	2.7 V	"	4A to 4Y	"	"	"
11	Same te	ests, terminal	conditions a	and limits	as for sub	aroup 10.	except To	= -55° C		•		•	•			•					

<sup>1/</sup> For case 2, pins not referenced are NC.

<sup>2/</sup> I<sub>OS</sub> limits for CKTS B, C, and D are -15/-100 mA.

 $<sup>\</sup>underline{3}/$  I<sub>IL</sub> limits for CKT B are -30 to -300  $\mu$ A; CKT C, -150 to -380  $\mu$ A; CKT D, -120 to -360  $\mu$ A; CKT F, -5 to -400  $\mu$ A.

TABLE III. Group A inspection for device type 03. Terminal conditions (pins not designated may be high  $\geq$  2.0 V, low  $\leq$  0.7 V, or open).

	1		Cases	1	2	3	4	5	6	7 7	8	<u> </u>	10W <u>3</u> 0.	11	12	13	14				Т
		MIL-STD-	A,B,C,D	'		3	-	3	0	,	0	3	10	11	12	13	14				
Subgroup	Symbol	883 method	Case <u>1</u> / 2	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lir	nits	Unit
		metriou	Test no.	1A	1B	1Y	2A	2B	2Y	GND	3Y	ЗА	3B	4Y	4A	4B	V <sub>CC</sub>	terrinia	Min	Max	-
1	Vol	3007	1	2.0 V	2.0 V	12 mA	5.5 V	5.5 V		GND	01	5.5 V	5.5 V		5.5 V	5.5 V	4.5 V	1Y		0.4	V
Tc = 25°C	VOL	"	2	5.5 V	5.5 V	12 1107	2.0 V	2.0 V	12 mA	"		5.5 V	5.5 V		"	"	"	2Y		"	"
10 = 25 0			3	"	"		5.5 V	5.5 V	12 110	"	12 mA	2.0 V	2.0 V		"	"	"	3Y		"	"
		"	4	"			"	U.U V		"	12 1117 (	5.5 V	5.5 V	12 mA	2.0 V	2.0 V	"	4Y		"	"
	CEX	"	5	0.7 V	"	5.5 V	"	"		"		0.5 V	0.0 V	12 111/1	2.0 V	2.0 V	"	1Y		250	μА
	CEX		6	5.5 V	0.7 V	5.5 V	"	"		"		"	"		"	"	"	1Y		"	"
			7	0.0 V	5.5 V	0.0 V	0.7 V	"	5.5 V	"		"	"		"	"	"	2Y		"	"
		"	8	"	0.0 V		5.5 V	0.7 V	5.5 V	"		"	"		"	"	"	2Y		"	"
'		"	9	"	"		"	5.5 V	0.0 1	íí.	5.5 V	0.7 V	"		"	"	"	3Y		"	"
		"	10	"			"	U.U V		"	5.5 V	5.5 V	0.7 V		"	"	"	3Y		"	"
		"	11	"			"	"		"	0.0 1	"	5.5 V	5.5 V	0.7 V	"	"	4Y		"	"
		"	12	"			"	"		"		"	5.5 V	5.5 V	5.5 V	0.7 V	"	4Y		"	"
	I C		13	-18 mA						íí.			0.0 .	0.0 .	0.0 1	0	"	1A		-1.5	V
	10		14	10 11	-18 mA					"							"	1B		"	"
			15				-18 mA			"							tt	2A		"	"
V			16					-18 mA		"							"	2B		"	"
v			17							"		-18 mA					"	3A		"	"
			18							"			-18 mA				"	3B		"	"
			19							"					-18 mA		"	4A		"	"
			20							"						-18 mA	"	4B		"	"
	IH1	3010	21	2.7 V	GND		GND	GND		íí.		GND	GND		GND	GND	5.5 V	1A		20	μА
		"	22	GND	2.7 V		GND	"		"			"		"	"	"	1B		"	"
		"	23	"	GND		2.7 V	"		íí.			"		"	"	"	2A		"	"
1		"	24	"	"		GND	2.7 V		"			"		"	"	"	2B		"	"
-		"	25	"	"			GND		"		2.7 V	"		"	"	"	3A		"	"
		"	26	"	"			"		íí.	"	GND	2.7 V		"	"	"	3B		"	"
		"	27	"	"			"			"		GND		2.7 V	"	"	4A		"	"
		"	28	"	"	"		"		"			"		GND	2.7 V	"	4B		"	"
	IH2	"	29	5.5 V	"	-	"	"		"		"	-		"	GND	"	1A		100	"
		"	30	GND	5.5 V	-		"	"	"			-		"	"	"	1B		"	"
		"	31	"	GND		5.5 V	"		"	=		-		"	"	"	2A		"	"
I		"	32	"	"		GND	5.5 V		"			"		"	"	"	2B		"	"
		"	33	"	"	"		GND		"		5.5 V			"	"	"	3A		"	"
		"	34	"	"			"			"	GND	5.5 V		"	"	"	3B		"	"
		"	35	"	"			"					GND		5.5 V	"	"	4A		"	"
		"	36	"	"	"		"		"			GND		GND	5.5 V	"	4B		"	"
	1L <u>2</u> /	3009	37	0.4 V	5.5 V	"	5.5 V	5.5 V	"	"		5.5 V	5.5 V		5.5 V	"	"	1A	-160	-400	"
		"	38	5.5 V	0.4 V	"	5.5 V	"	"	"	"	"	"		"	"	"	1B	"	"	"
		"	39	"	5.5 V	"	0.4 V	"		"	"		"		"	"	"	2A	"	"	"
'		"	40	"	"		5.5 V	0.4 V		"			"		"	"	"	2B	"	"	"
		"	41	"	"			5.5 V		ű		0.4 V	"		"	"	"	3A	"	"	- "
		"	42	"	"			"		"	"	5.5 V	0.4 V		"	"	"	3B	"	"	
		"	43	- "	"			"			"		5.5 V		0.4 V		"	4A			
		п	44			"		"		"			- 1		5.5 V	0.4 V	"	4B	"		"

See footnotes at end of device type.

TABLE III. Group A inspection for device type 03 – Continued. Terminal conditions (pins not designated may be high  $\geq 2.0 \text{ V}$ , low  $\leq 0.7 \text{ V}$ , or open).

					1611	minai coi	iditions (	pins not	uesigna	leu may	be nign :	≥ 2.0 V, I	10W ≤ 0.7	ν, οι ορ							
		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol		Case <u>1</u> /	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured	Lim	its	Unit
		method	2															terminal			
			Test no.	1A	1B	1Y	2A	2B	2Y	GND	3Y	3A	3B	4Y	4A	4B	V <sub>CC</sub>		Min	Max	
1	I <sub>C C L</sub>	3005	45	5.5 V	5.5 V		5.5 V	5.5 V		GND		5.5 V	5.5 V		5.5 V	5.5 V	5.5 V	Vcc		12	mA
Tc = 25°C	I <sub>C C H</sub>	3005	46	GND	GND		GND	GND		GND		GND	GND		GND	GND	5.5 V	Vcc		2	mA
2	Same te	ests, terminal	conditions a	and limits a	as for sub	group 1, e	xcept T <sub>C</sub>	= +125° C	, and V <sub>IC</sub>	tests are	omitted.										
3	Same te	ests, terminal	conditions a	and limits a	as for sub	group 1, e	xcept T <sub>C</sub>	= -55° C,	and V <sub>IC</sub>	tests are	omitted.										
9	t <sub>PHL</sub>	3003	47	IN	2.7 V	OUT				GND							5.0 V	1A to 1Y	3	39	ns
Tc = 25°C		(Fig. 4)	48				IN	2.7 V	OUT	"							"	2A to 2Y	"	"	"
		"	49							66	OUT	IN	2.7 V				"	3A to 3Y	66	66	"
		"	50							"				OUT	IN	2.7 V	"	4A to 4Y	**	**	66
	PLH	"	51	IN	27 V	OUT				66							"	1A to 1Y	"	43	"
		"	52				IN	2.7 V	OUT	66							"	2A to 2Y	"	"	"
		"	53							66	OUT	IN	2.7 V				"	3A to 3Y	"	"	"
t		"	54							"				OUT	IN	2.7 V	"	4A to 4Y	"	"	"
10	t <sub>PHL</sub>	=	55	IN	2.7 V	OUT				"							"	1A to 1Y	"	51	"
Tc =+125°C		"	56				IN	2.7 V	OUT	66							"	2A to 2Y	"	"	"
		"	57							66	OUT	IN	2.7 V				"	3A to 3Y	"	66	"
		"	58							66				OUT	IN	2.7 V	"	4A to 4Y	"	66	"
	PLH	=	59	IN	27 V	OUT				66							"	1A to 1Y	"	56	"
		"	60				IN	2.7 V	OUT	66							"	2A to 2Y	"	66	"
		"	61							66	OUT	IN	2.7 V				"	3A to 3Y	"	"	"
t		"	62							66				OUT	IN	2.7 V	"	4A to 4Y	"	**	"
11	Same te	ests, terminal	conditions a	and limits	as for sub	group 10,	except T <sub>c</sub>	= -55° C	;.												

 $<sup>\</sup>underline{1}/$  For case 2, pins not referenced are NC.

18

 $<sup>\</sup>underline{2}/$  I<sub>IL</sub> limits for CKT B are -30 to -300  $\mu$ A; CKT C, -150 to -380  $\mu$ A; CKT D, -120 to -360  $\mu$ A; CKT F, -5 to -400  $\mu$ A.

TABLE III. Group A inspection for device type 04. Terminal conditions (pins not designated may be high  $\geq$  2.0 V, low  $\leq$  0.7 V, or open).

									t designa									1			
		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol		Case <u>1/</u> 2	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lim	nits	Unit
		memod	Test no.	1Y	1A	1B	2Y	2A	2B	GND	3A	3B	3Y	4A	4B	4Y	V <sub>cc</sub>	terminai	Min	Max	l
1	1/	3007	1	12 mA	2.0 V	GND	21	ZA	ZD	GND	SA	SD	31	4A	4D	41	4.5 V	1Y	IVIIII	0.4	V
	VoL	3007	2	12 mA	GND	2.0 V				GND							4.5 V	1 Y		"	V
$Tc = 25^{\circ}C$				12 MA	GND	2.0 V	40 4	0.01/	OND	"							-			"	"
			3				12 mA	2.0 V	GND	"								2Y		"	"
			4				12 mA	GND	2.0 V	"	0.01/	ONE	40 4					2Y			"
			5							"	2.0 V	GND	12 mA					3Y		"	"
			6							-	GND	2.0 V	12 mA	0.01/	ONE	40. 4		3Y			
			7											2.0 V	GND	12 mA		4Y			
			8		0 = 17	0 = 17								GND	2.0 V	12 mA	- "	4Y		- "	"
	OH	3006	9	-1.2 mA	0.7 V	0.7 V				"								1Y	2.5		"
			10				-1.2 mA	0.7 V	0.7 V	"								2Y			
		"	11							"	0.7 V	0.7 V	-1.2 mA					3Y			
V		"	12		40 1					"				0.7 V	0.7 V	-1.2 mA		4Y	- "	4.5	
	IC		13		-18 mA												- "	1A		-1.5	"
			14			-18 mA				"								1B			"
.,			15					-18 mA										2A		"	
V			16						-18 mA	"								2B		"	"
			17							"	-18 mA						- "	3A		"	
			18							"		-18 mA						3B		"	"
			19							"				-18 mA				4A		"	
			20							"					-18 mA			4B		u	
	os	3011	21	GND	GND	GND											5.5 V	1Y	-30	-130	mA
		i.	22				GND	GND	GND		0115	0115	0115				- "	2Y	- "	- "	"
			23							- :	GND	GND	GND					3Y			
1			24											GND	GND	GND		4Y			
	IH1	3010	25		2.7 V	GND		GND	GND	"	GND	GND						1A		20	μΑ
			26		GND	2.7 V		GND	"	"	"	"		"				1B		"	
			27		"	GND		2.7 V	"	"	"			"				2A		"	"
I			28		"	"		GND	2.7 V	"	"	"		"				2B		"	"
			29			"			GND		2.7 V	"						3A		"	"
			30		"	"		"		"	GND	2.7 V		"				3B			
		i i	31		- "	"		- "				GND "		2.7 V				4A			
			32				"	**	"				"	GND	2.7 V			4B		"	
	IH2		33		5.5 V	"		"	- "	"	"	"			GND			1A		100	u
			34		GND	5.5 V												1B			
			35		"	GND "		5.5 V		"	"	"						2A		"	"
I			36			"		GND	5.5 V	"		"		"	- "			2B			"
			37						GND	"	5.5 V				- "			3A			
			38		"	"		- "		"	GND	5.5 V			- "			3B		"	"
			39		"	"		- "	- "		"	GND		5.5 V			- "	4A		"	
			40				"			"		GND	"	GND	5.5 V			4B	405		"
	1L <u>3</u> /	3009	41		0.4 V	5.5 V		5.5 V	5.5 V	"	5.5 V	5.5 V		5.5 V	- "		- "	1A	-160	-400	
		,	42		5.5 V	0.4 V		5.5 V	"	"	"	"		4	"		- "	1B	"	"	
		"	43		"	5.5 V		0.4 V	"		"	"		"	"		- "	2A		"	
			44			"		5.5 V	0.4 V		"						- "	2B	"	"	"
			45		"			"	5.5 V	-	0.4 V	"					- "	3A		- "	<del></del>
			46			"		"	"	"	5.5 V	0.4 V					- "	3B		- "	<del>"</del>
			47			"			"		- "	5.5 V		0.4 V			- "	4A		"	
		"	48	l	"	"		"	"		"	"		5.5 V	0.4 V			4B	"	"	"

See footnotes at end of device type.

TABLE III. <u>Group A inspection for device type 04</u> – Continued. Terminal conditions (pins not designated may be high  $\geq 2.0 \text{ V}$ , low  $\leq 0.7 \text{ V}$ , or open).

							HUILIONS	(hing no	uesigne	ited may	De High	≥ 2.0 V,		v, 01 0	oen).						
		MIL-STD-	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14				ı
Subgroup	Symbol	883 method	Case <u>1</u> /	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Lin	nits	Unit
		method	Test no.	1Y	1A	1B	2Y	2A	2B	GND	3A	3B	3Y	4A	4B	4Y	V <sub>CC</sub>	terriiriai	Min	Max	
	CCL	3005	49	- ' '	5.5 V	5.5 V	21	5.5 V	5.5 V	GND	5.5 V	5.5 V	- 51	5.5 V	5.5 V	1	5.5 V	Vcc	IVIIII	13.8	mA
	ССН	3005	50		GND	GND		GND	GND	GND	GND	GND		GND	GND		"	Vcc		3.6	mA
2		ests, terminal	conditions a	and limits a	as for sub		except T <sub>C</sub>	= +125° C						ı	I						-
3.		ests, terminal																			
9	t <sub>PHI</sub>	3003	51	OUT	IN	GND				GND							5.0 V	1A to 1Y	2	25	ns
Tc = 25°C	TIL	(Fig. 5)	52	OUT	GND	IN				"							"	1B to 1Y	"	"	"
		(1.9)	53				OUT	IN	GND	"							"	2A to 2Y	"	"	"
		"	54				OUT	GND	IN	"							"	2B to 2Y	"	**	"
		"	55							"	IN	GND	OUT				"	3A to 3Y	"	**	66
		"	56							"	GND	IN	OUT				"	3B to 3Y	"	"	"
		"	57							"				IN	GND	OUT	"	4A to 4Y	"	"	ıı .
		"	58							££				GND	IN	OUT	"	4B to 4Y	66	"	££
	PLH	"	59	OUT	IN	GND				"							"	1A to 1Y	"	"	"
			60	OUT	GND	IN				í,							"	1B to 1Y	66	"	66
		"	61				OUT	IN	GND	í,							"	2A to 2Y	66	"	66
t			62				OUT	GND	IN	"							"	2B to 2Y	"	"	"
			63							"	IN	GND	OUT				"	3A to 3Y	"	"	"
		"	64							"	GND	IN	OUT				"	3B to 3Y		"	"
		"	65							"				IN	GND	OUT	"	4A to 4Y		"	"
		"	66							66				GND	IN	OUT	"	4B to 4Y	66	"	66
10	t <sub>PHL</sub>	"	67	OUT	IN	GND				"							"	1A to 1Y	"	30	"
Tc = 125°C		"	68	OUT	GND	IN				"							"	1B to 1Y	66	"	66
		"	69				OUT	IN	GND	££							"	2A to 2Y	66	"	66
		"	70				OUT	GND	IN	"							"	2B to 2Y	66	"	66
		"	71							66	IN	GND	OUT				"	3A to 3Y	"	"	66
		"	72							"	GND	IN	OUT				"	3B to 3Y	=	"	"
		"	73							"				IN	GND	OUT	"	4A to 4Y	"	"	"
			74							"				GND	IN	OUT	"	4B to 4Y	"	"	66
	PLH	"	75	OUT	IN	GND				"							"	1A to 1Y	"	"	"
		"	76	OUT	GND	IN				"							"	1B to 1Y	"	"	66
		"	77				OUT	IN	GND	"							"	2A to 2Y	"	"	66
t		"	78				OUT	GND	IN	"							"	2B to 2Y	"	"	66
		"	79							66	IN	GND	OUT				"	3A to 3Y	"	"	66
		"	80							"	GND	IN	OUT				"	3B to 3Y	"	"	"
		"	81							"				IN	GND	OUT	"	4A to 4Y	"	"	"
		"	82							"				GND	IN	OUT	"	4B to 4Y	"	"	66
11	Same to	ests, terminal	conditions	and limits	as for sub	group 10,	except To	= -55° C	;							·					

<sup>1/</sup> For case 2, pins not referenced are NC.

 $<sup>\</sup>underline{2}$ / I<sub>OS</sub> limits for CKTS B and D are -15 to -100 mA.

 $<sup>\</sup>underline{3}/$   $I_{IL}$  limits for CKT B are -30 to -300  $\mu A;$  CKT D, -120 to -360  $\mu A;$  CKT F, -5 to -400  $\mu A.$ 

#### 5. PACKAGING

5.1 <u>Packaging requirements.</u> For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When actual packaging of materiel is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department of Defense Agency, or within the Military Department's System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

#### 6. NOTES

- 6.1 <u>Intended use.</u> Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.
  - 6.2 Acquisition requirements. Acquisition documents should specify the following:
    - a. Title, number, and date of the specification.
    - b. Complete part number (see 1.2).
    - c. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
    - d. Requirements for certificate of compliance, if applicable.
    - e. Requirements for notification of change of product or process to contracting activity in addition to notification to the qualifying activity, if applicable.
    - f. Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
    - g. Requirements for product assurance options.
    - h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements shall not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
    - j. Requirements for "JAN" marking.
- 6.3 <u>Superseding information</u>. The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractor's parts lists.
- 6.4 <u>Qualification</u>. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, 3990 E. Broad Street, Columbus, Ohio 43123-1199.

6.5 <u>Abbreviations, symbols, and definitions.</u> The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535, MIL-HDBK-1331, and as follows:

GND	Ground zero voltage potential
I <sub>IN</sub>	Current flowing into an input terminal
V <sub>IC</sub>	Input clamp voltage
V <sub>IN</sub>	Voltage level at an input terminal

- 6.6 <u>Logistic support.</u> Lead materials and finishes (see 3.4) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming shall not affect the part number.
- 6.7 <u>Substitutability.</u> The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information shall not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

Military device	Generic-industry
type	type
01	54LS40
02	54LS37
03	54LS38
04	54LS28

6.8 <u>Manufacturers' designation.</u> Manufacturers' circuits which form a part of this specification are designated with an "X" as shown in table IV herein.

TABLE IV. Manufacturers' designation.

			Circ	cuits		
Device	Α	В	С	D	E	F
type	Texas Instruments	Signetics Corp.	National Semiconductor Corporation	Raytheon Company	Motorola Inc	Fairchild Semiconductor
01	X	Х	X	X	X	X
02	Χ	Χ	Χ	X	X	X
03	X	Χ	X	X	X	X
04	X	X		X	X	X

6.9 <u>Changes from previous issue.</u> Asterisks are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of the changes.

Custodians: Preparing activity: Army - CR DLA - CC

Navy - EC Air Force - 11 (Project 5962-1948)

DLA - CC

Review activities:

Army - HD, MI, SM Navy - AS, CG, MC, SH, TD Air Force - 03, 19, 99

#### STANDARDIZATION DOCUMENT IMPROVEMENT PROPOSAL

#### INSTRUCTIONS

- 1. The preparing activity must complete blocks 1, 2, 3, and 8. In block 1, both the document number and revision letter should be given.
- 2. The submitter of this form must complete blocks 4, 5, 6, and 7, and send to preparing activity.
- 3. The preparing activity must provide a reply within 30 days from receipt of the form.

NOTE: This form may not be used to request copies of documents, nor to request waivers, or clarification of requirements on current contracts. Comments submitted on this form do not constitute or imply authorization to waive any portion of the referenced document(s) or to amend contractual requirements.

contractual requirements.	onstitute of imply authorization to waive any	conton of the referenced document(s) of to amend
I RECOMMEND A CHANGE:	DOCUMENT NUMBER     MIL-M-38510/302D	2. DOCUMENT DATE (YYYYMMDD) 2003-01-10
3. DOCUMENT TITLE		
	AR LOW-POWER SCHOTTKY TTL BL	IFFERS MONOLITHIC SILICON
MICROCIRCUITS, DIGITAL, BIPOLA  4. NATURE OF CHANGE (Identify paragraph)	AR LOW-POWER SCHOTTKY TTL, BL h number and include proposed rewrite, if po	
5. REASON FOR RECOMMENDATION		
O. OUDMITTED		
SUBMITTER     A. NAME (Last, First Middle Initial)	b. ORGANIZATION	J
c. ADDRESS (Include Zip Code)	d. TELEPHONE (In (1) Commercial (2) DSN (If applicable)	
8. PREPARING ACTIVITY		
NAME     Defense Supply Center, Columbus	b. TELEPHONE (In (1) Commercial 6	
c. ADDRESS (Include Zip Code) DSCC-VA P. O. Box 3990 Columbus, Ohio 43216-5000	Defense Standar 8725 John J. Kin Fort Belvoir, Virgi	ECEIVE A REPLY WITHIN 45 DAYS, CONTACT: dization Program Office (DLSC-LM) gman Road, Suite 2533 nia 22060-6221 767-6888 DSN 427-6888