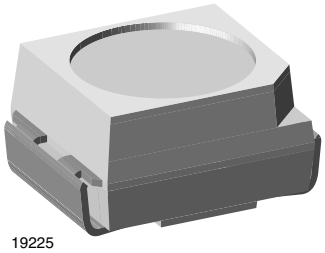
Low Current SMD LED PLCC-2



19225

DESCRIPTION

These new devices have been designed to meet the increasing demand for low current SMD LEDs.

The package of the VLMT3100 is the PLCC-2 (equivalent to a size B tantalum capacitor).

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD PLCC-2
- Product series: Low current
- Angle of half intensity: ± 60°

FEATURES

- SMD LED with exceptional brightness
- Compatible with automatic placement equipment
- EIA and ICE standard package
- Compatible with infrared, vapor phase and wave solder processes according to CECC
- Available in 8 mm tape
- Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Very low power consumption
- Luminous intensity ratio in one packaging unit $I_{Vmax.}/I_{Vmin.} \leq 1.6$
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free

e3

RoHS COMPLIANT

HALOGEN FREE GREEN (5-2008)

APPLICATIONS

- Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight for audio and video equipment
- Indicator and backlight for battery driven equipment
- Small indicator for outdoor applications
- Indicator and backlight in office equipment
- Flat backlight for LCDs, switches, and symbols
- General use

PARTS TABLE

PART	COLOR	LUMINOUS INTENSITY (mcd)			at I_F (mA)	WAVELENGTH (nm)			at I_F (mA)	FORWARD VOLTAGE (V)			at I_F (mA)	TECHNOLOGY
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLMT3100-GS08	Red	0.28	1.1	-	2	612	618	625	2	-	2.2	2.9	2	GaAsP on GaP

For technical questions, contact: LED@vishay.com

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ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)
VLMT3100

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage (1)		V_R	6	V
DC forward current	$T_{amb} \leq 90$ °C	I_F	7	mA
Surge forward current	$t_p \leq 10$ µs	I_{FSM}	0.34	A
Power dissipation		P_V	20	mW
Junction temperature		T_j	100	°C
Operating temperature range		T_{amb}	-40 to +100	°C
Storage temperature range		T_{stg}	-40 to +100	°C
Soldering temperature	t ≤ 5 s	T_{sd}	260	°C
Thermal resistance junction to ambient	Mounted on PC board (pad size > 16 mm²)	R_{thJA}	500	K/W

Note

(1) Driving the LED in reverse direction is suitable for short term application

OPTICAL AND ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)
VLMT3100, RED

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity (1)	I_F = 2 mA	I_V	0.28	1.1	-	mcd
Dominant wavelength	I_F = 2 mA	λ_d	612	618	625	nm
Peak wavelength	I_F = 2 mA	λ_p	-	635	-	nm
Angle of half intensity	I_F = 2 mA	φ	-	± 60	-	°
Forward voltage	I_F = 2 mA	V_F	-	2.2	2.9	V
Reverse voltage	I_R = 10 µA	V_R	6	15	-	V
Junction capacitance	V_R = 0 V, f = 1 MHz	C_j	-	50	-	pF

Note

(1) In one packing unit $I_{Vmax.}/I_{Vmin.} \leq 1.6$

LUMINOUS INTENSITY CLASSIFICATION

GROUP	LUMINOUS INTENSITY (mcd)	
	MIN.	MAX.
C1	0.28	0.36
C2	0.36	0.45
D1	0.45	0.56
D2	0.56	0.71
E1	0.71	0.90
E2	0.9	1.12
F1	1.12	1.4
F2	1.4	1.8
G1	1.8	2.24
G2	2.24	2.8

Note

- Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.
  The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups will not be orderable.
  In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.
  In order to ensure availability, singe wavelength groups will not be orderable.

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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

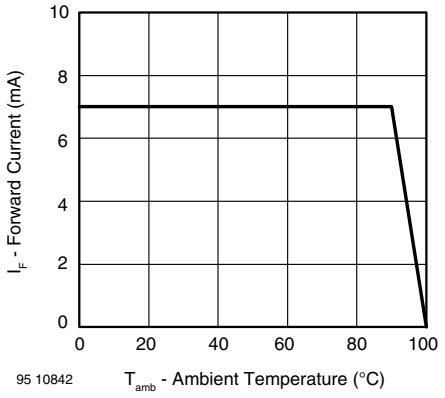


Fig. 1 - Forward Current vs. Ambient Temperature

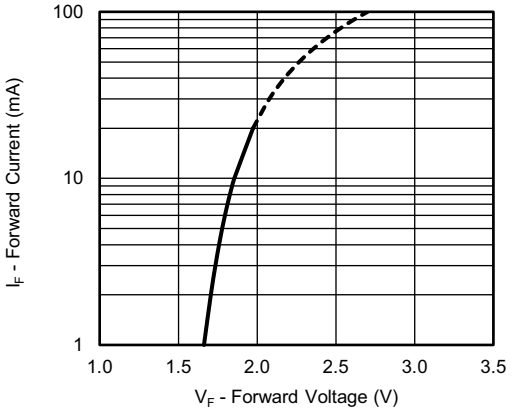


Fig. 4 - Forward Current vs. Forward Voltage

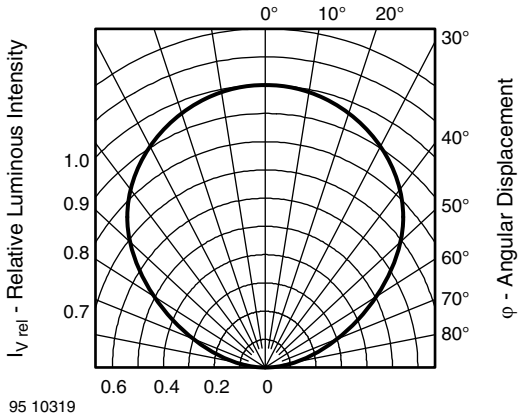


Fig. 2 - Relative Luminous Intensity vs. Angular Displacement

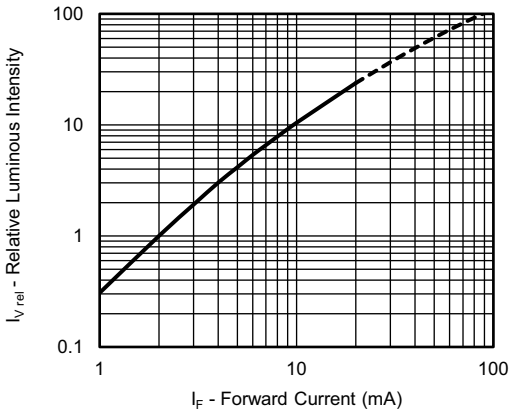


Fig. 5 - Relative Luminous Intensity vs. Forward Current

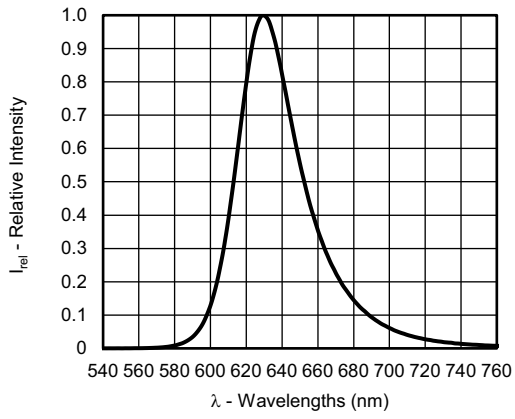


Fig. 3 - Relative Intensity vs. Wavelength

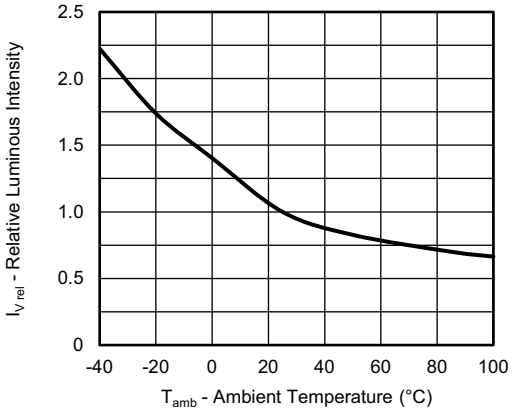


Fig. 6 - Relative Luminous Intensity vs. Ambient Temperature

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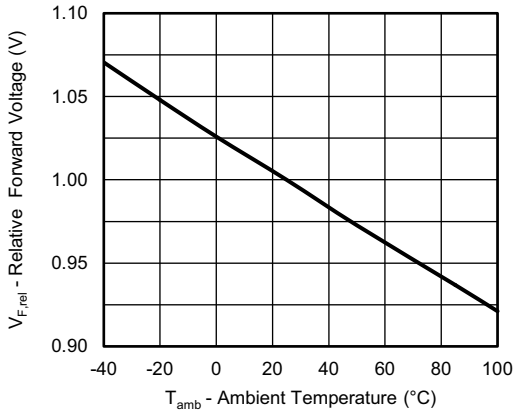
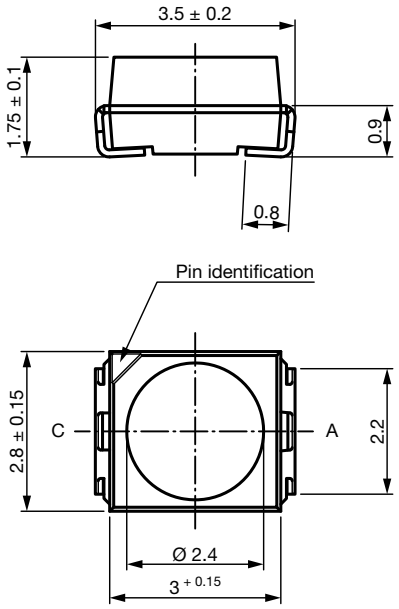
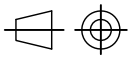


Fig. 7 - Relative Forward Voltage vs. Ambient Temperature

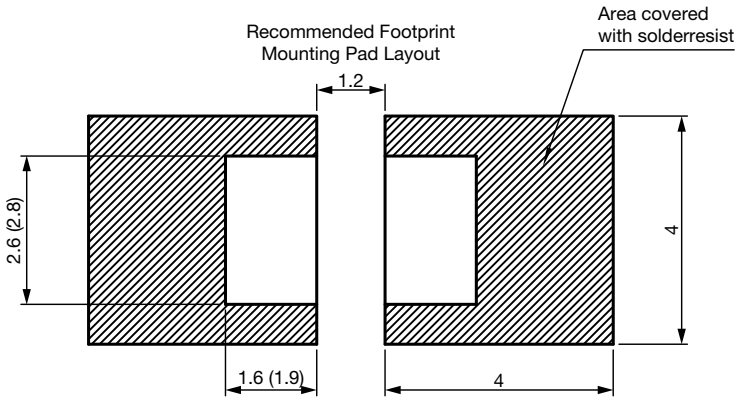
PACKAGE DIMENSIONS in millimeters





technical drawings
according to DIN
specifications

Drawing-No.: 6.541-5067.01-4
Issue: 7; 12.03.14

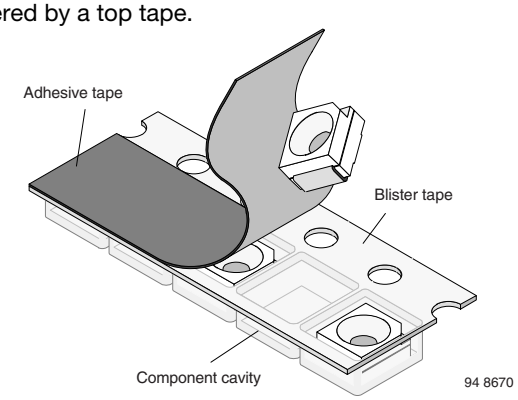


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METHOD OF TAPING / POLARITY AND TAPE AND REEL

SMD LED (VLM.3-SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3...

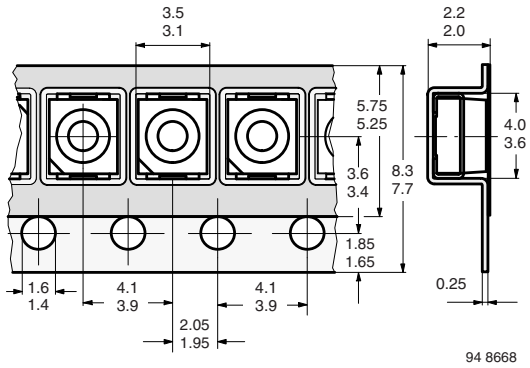


Fig. 8 - Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

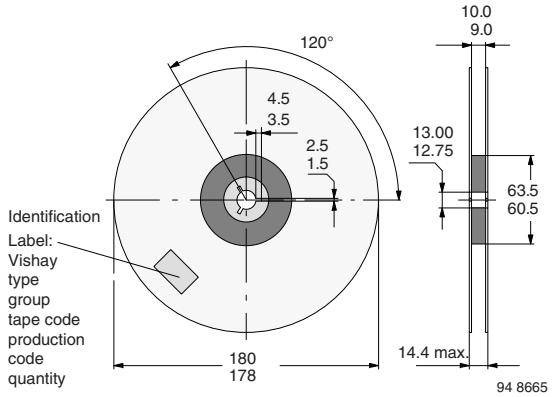


Fig. 9 - Reel Dimensions - GS08

SOLDERING PROFILE

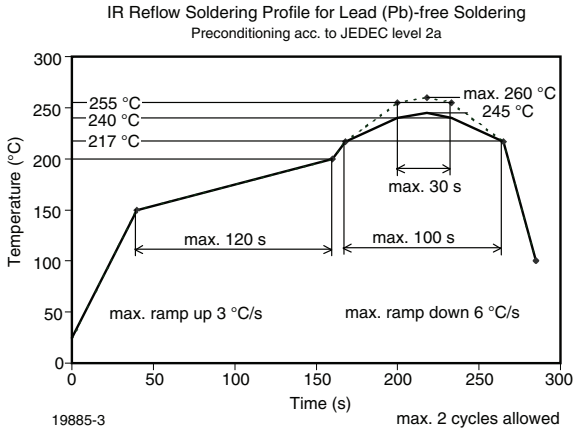


Fig. 10 - Vishay Lead (Pb)-free Reflow Soldering Profile (according to J-STD-020)

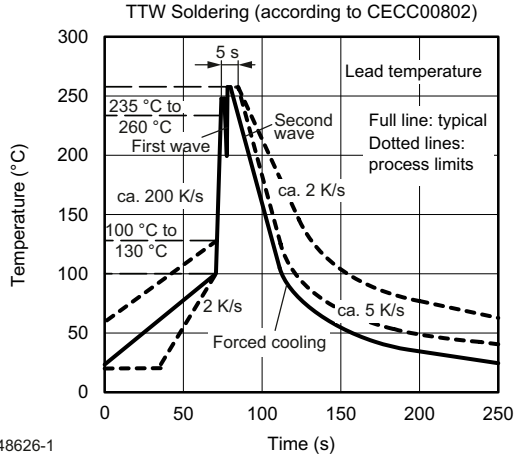
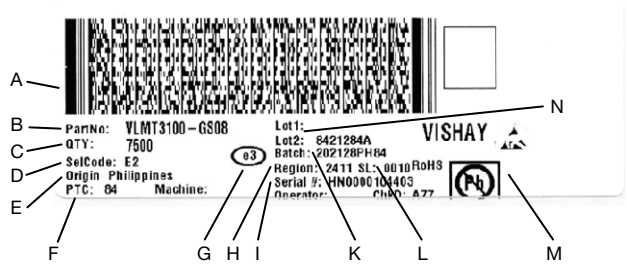


Fig. 11 - Double Wave Soldering of Opto Devices (all Packages)

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BAR CODE PRODUCT LABEL (example)



A. 2D barcode

B. Part No: Vishay part number

C. QTY: quantity

D. SelCode: selection bin code

E. Country of origin

F. PTC: production plant code

G. Termination finish

H. Region code

I. Serial#: serial number

K. Batch Number: year, week, country code, plant code

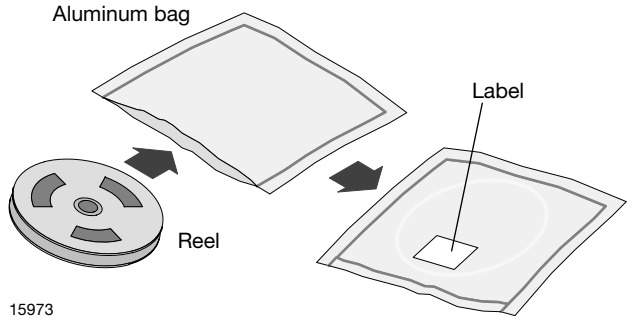
L. SL: storage location

M. Environmental Symbols: RoHS, lead (Pb)-free, halogen free

N. Lot numbers

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

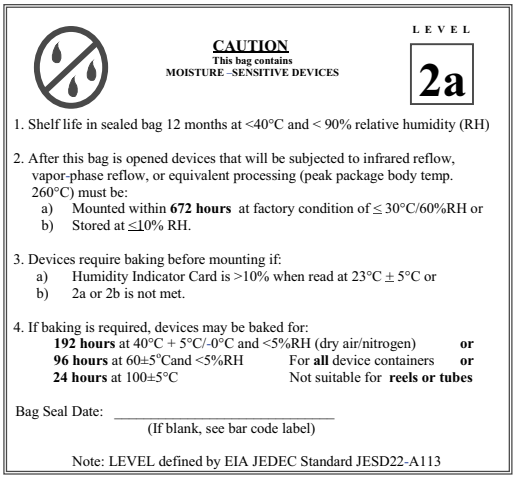
- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or
96 h at 60 °C + 5 °C and < 5 % RH for all device containers or
24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABEL

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

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