

# RQM2201DNS

# Silicon N Channel MOS FET Power Switching

REJ03G1492-0200 Rev.2.00 Apr 16, 2007

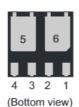
#### **Features**

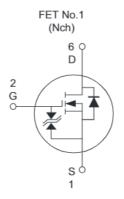
- Small, thin and leadless type package  $(3 \cdot 3 \text{ mm}, t = 0.8 \text{ mm max.})$
- · Two FET chips are mounted in one package
- · High density mounting
- High speed switching. (Ciss = 200 pF typ)
- V<sub>DSS</sub> ≥ 60 V and capable of 2.5 V gate drive

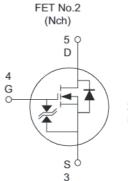
#### Outline

RENESAS Package code: PWSN0006ZA-A (Package name: WSON0303-6 <HWSON-6>)









1, 3: Source 2, 4: Gate 5, 6: Drain

Notes:

- 1. Marking is "M2201".
- The following maximum ratings and electric characteristics are applied to both FET1 and FET2.

#### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	V <sub>GSS</sub>	±12	V
Drain current	I <sub>D</sub>	2	A
Drain peak current	I <sub>D(pulse)</sub> Note1	8	A
Body - drain diode reverse drain current	I <sub>DR</sub>	2	A
Channel dissipation	Pch Note2	1	W
Channel dissipation	Pch Note3	1.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW ≤ 10 ∞s, Duty cycle ≤ 1%

- 2. 1 Drive operation: When using the glass epoxy board (FR-4 40 · 40 · 1 mm)
- 3. 2 Drive operation: When using the glass epoxy board (FR-4 40  $\cdot$  40  $\cdot$  1 mm)

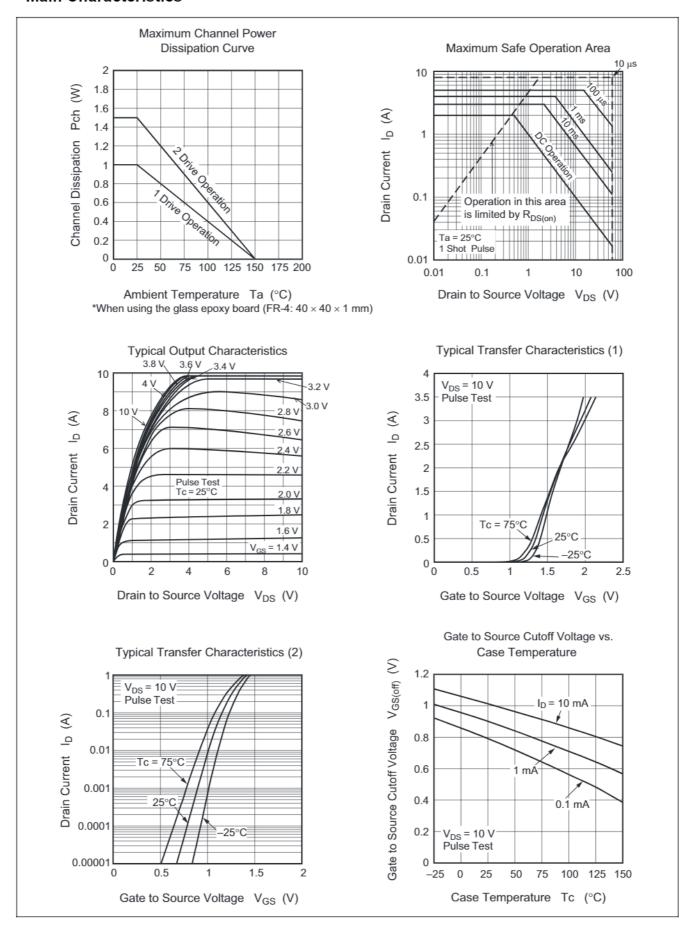
## **Electrical Characteristics**

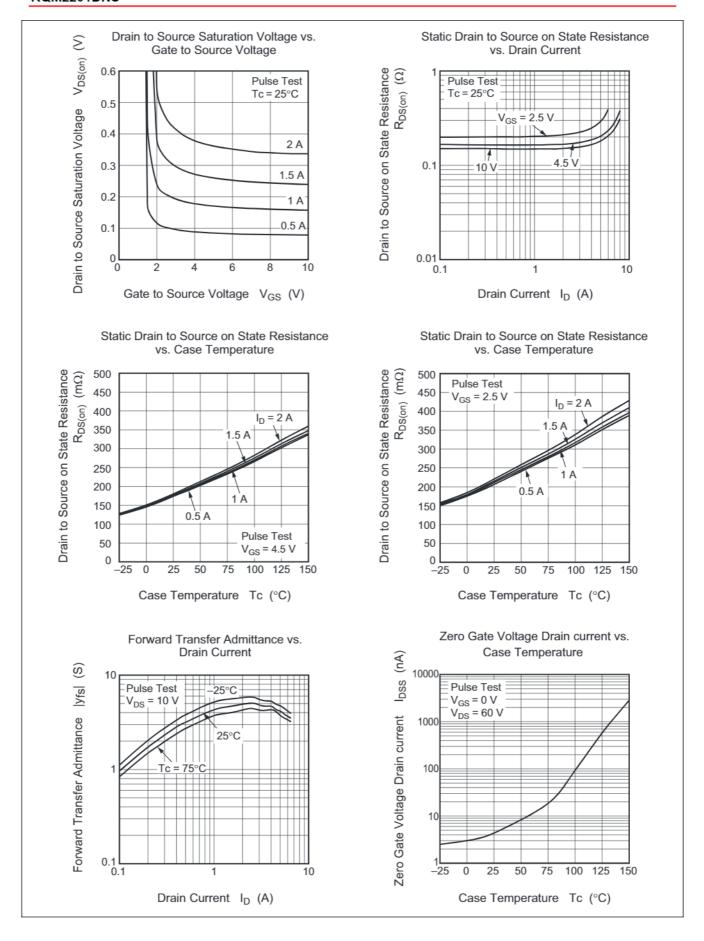
 $(Ta = 25^{\circ}C)$ 

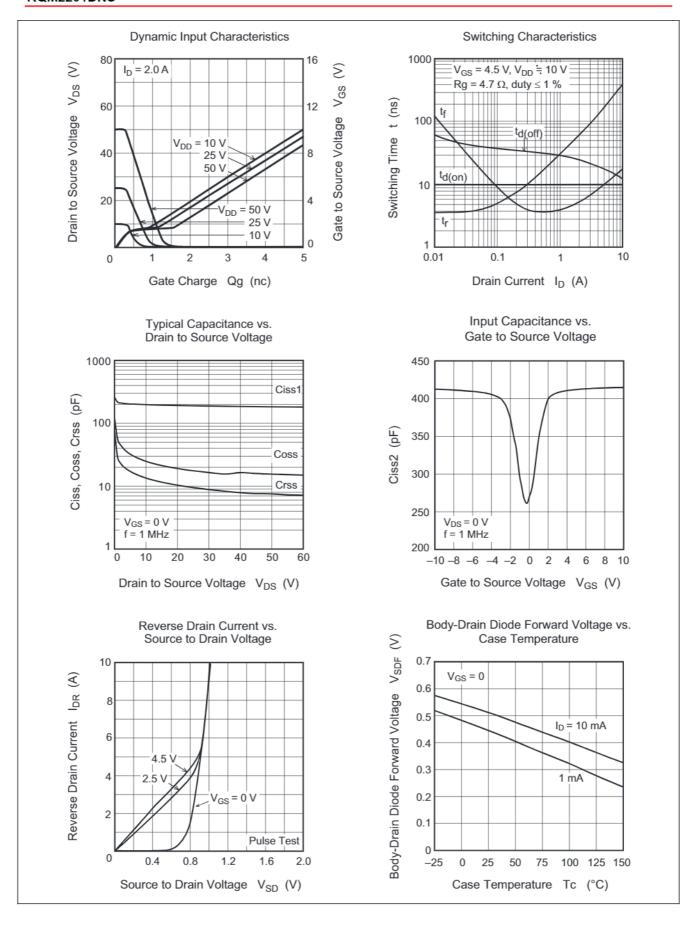
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	60	_	_	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	+12	_	_	V	I <sub>G</sub> = +100 ∝A, V <sub>DS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	-12	_	_	V	I <sub>G</sub> = −100 ∝A, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	_	_	+10	∞A	V <sub>GS</sub> = +10 V, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	_	_	-10	αA	$V_{GS} = -10 \text{ V}, V_{DS} = 0$
Drain to source leak current	I <sub>DSS</sub>	_	_	1	αA	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	0.4	_	1.4	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Drain to source on state resistance	R <sub>DS(on)</sub>	_	173	225	mΩ	I <sub>D</sub> = 1 A, V <sub>GS</sub> = 4.5 V Note4
Drain to source on state resistance	R <sub>DS(on)</sub>	_	207	290	mΩ	I <sub>D</sub> = 1 A, V <sub>GS</sub> = 2.5 V Note4
Forward transfer admittance	y <sub>fs</sub>	2.3	3.5	_	S	I <sub>D</sub> = 1 A, V <sub>DS</sub> = 10 V Note4
Input capacitance	Ciss	_	200	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	25	_	pF	V <sub>GS</sub> = 0
Reverse transfer capacitance	Crss	_	13	_	pF	f = 1 MHz
Turn - on delay time	t <sub>d(on)</sub>	_	7	_	ns	I <sub>D</sub> = 1 A
Rise time	t <sub>r</sub>	_	28	_	ns	V <sub>GS</sub> = 10 V
Turn - off delay time	t <sub>d(off)</sub>	_	30	_	ns	$R_L = 10 \Omega$
Fall time	t <sub>f</sub>	_	4	_	ns	Rg = 4.7 Ω
Total gate charge	Qg	_	2.4	_	nC	V <sub>DD</sub> = 10 V
Gate to Source charge	Qgs	_	0.4	_	nC	V <sub>GS</sub> = 4.5 V
Gate to drain charge	Qgd	_	0.4	_	nC	I <sub>D</sub> = 2 A
Body - drain diode forward voltage	V <sub>DF</sub>	_	0.8	_	V	I <sub>F</sub> = 2 A, V <sub>GS</sub> = 0 <sup>Note4</sup>

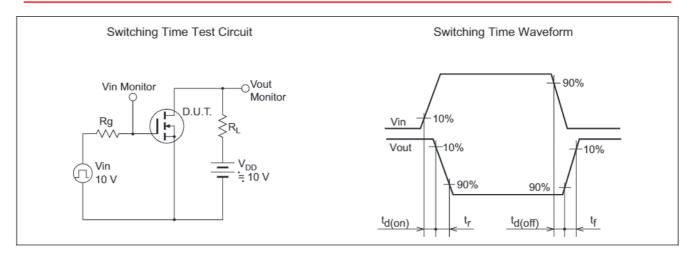
Notes: 4. Pulse test

#### **Main Characteristics**

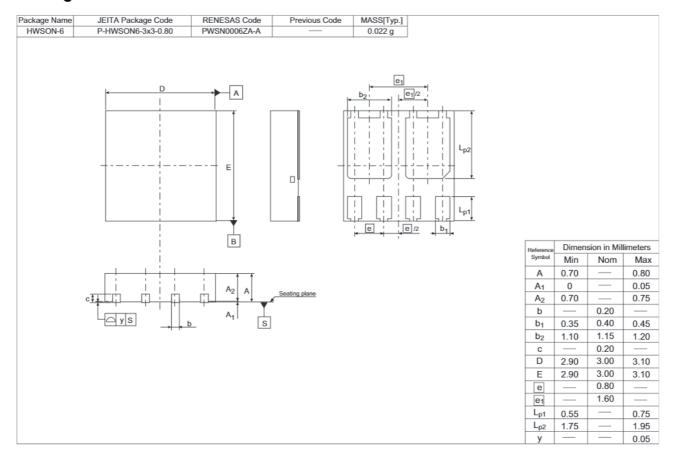








#### **Package Dimensions**



## **Ordering Information**

Part No. Quantity		Shipping Container		
RQM2201DNSTL-E	2000 pcs.	φ178 mm reel, 8 mm Emboss taping		
RQM2201DNSTR-E	2000 pcs.	φ178 mm reel, 8 mm Emboss taping		

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