

HSB88AS

Silicon Schottky Barrier Diode for High Speed Switching

REJ03G0586-0100
(Previous: ADE-208-964)
Rev.1.00
Mar 31, 2005

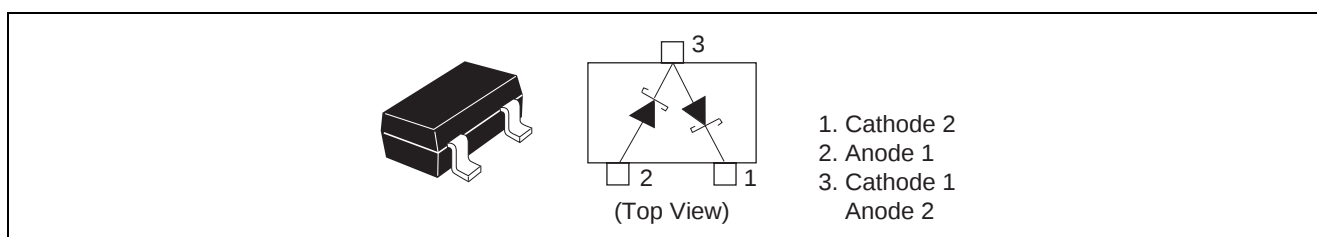
Features

- Low reverse current, Low capacitance.
- CMPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HSB88AS	C1	CMPAK	PTSP0003ZB-A (CMPAK)

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	10	V
Average rectified current	I_O * ¹	15	mA
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

Note: 1. Per one device.

Electrical Characteristics *¹

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	0.350	—	0.420	V	$I_F = 1$ mA
	V_{F2}	0.500	—	0.580		$I_F = 10$ mA
Reverse current	I_{R1}	—	—	0.2	μ A	$V_R = 2$ V
	I_{R2}	—	—	10		$V_R = 10$ V
Capacitance	C	—	—	0.80	pF	$V_R = 0$ V, $f = 1$ MHz
Capacitance deviation	ΔC	—	—	0.10	pF	$V_R = 0$ V, $f = 1$ MHz
Forward voltage deviation	ΔV_F	—	—	10	mV	$I_F = 10$ mA
ESD-Capability ^{*2}	—	30	—	—	V	C = 200 pF, R = 0 Ω , Both forward and reverse direction 1 pulse.

Notes: 1. Per one device.

2. Failure criterion ; $I_R > 0.4 \mu$ A at $V_R = 2$ V

Main Characteristic

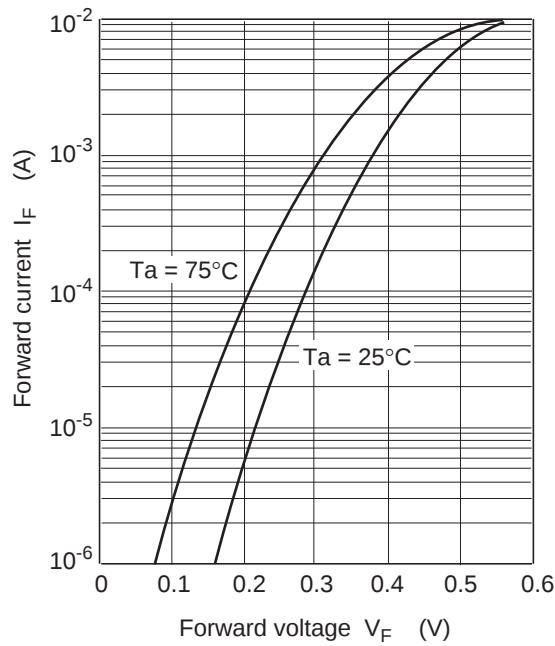


Fig.1 Forward current vs. Forward voltage

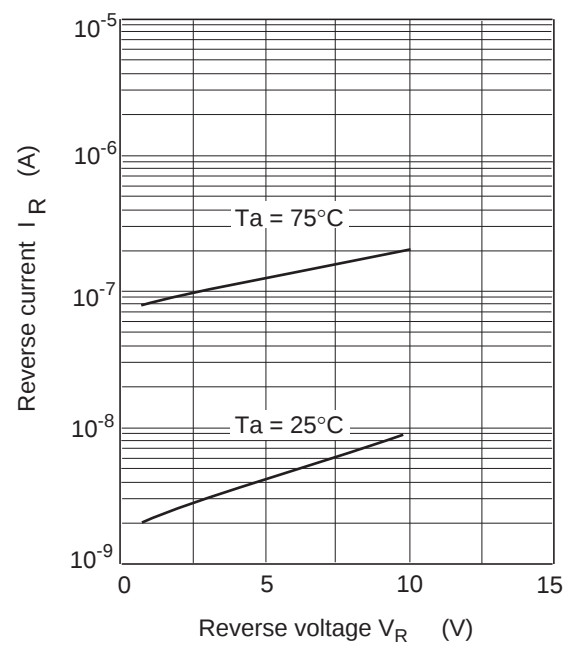


Fig.2 Reverse current vs. Reverse voltage

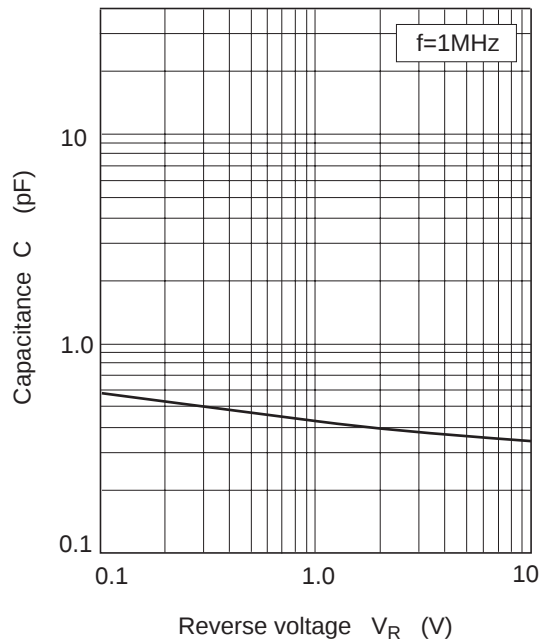
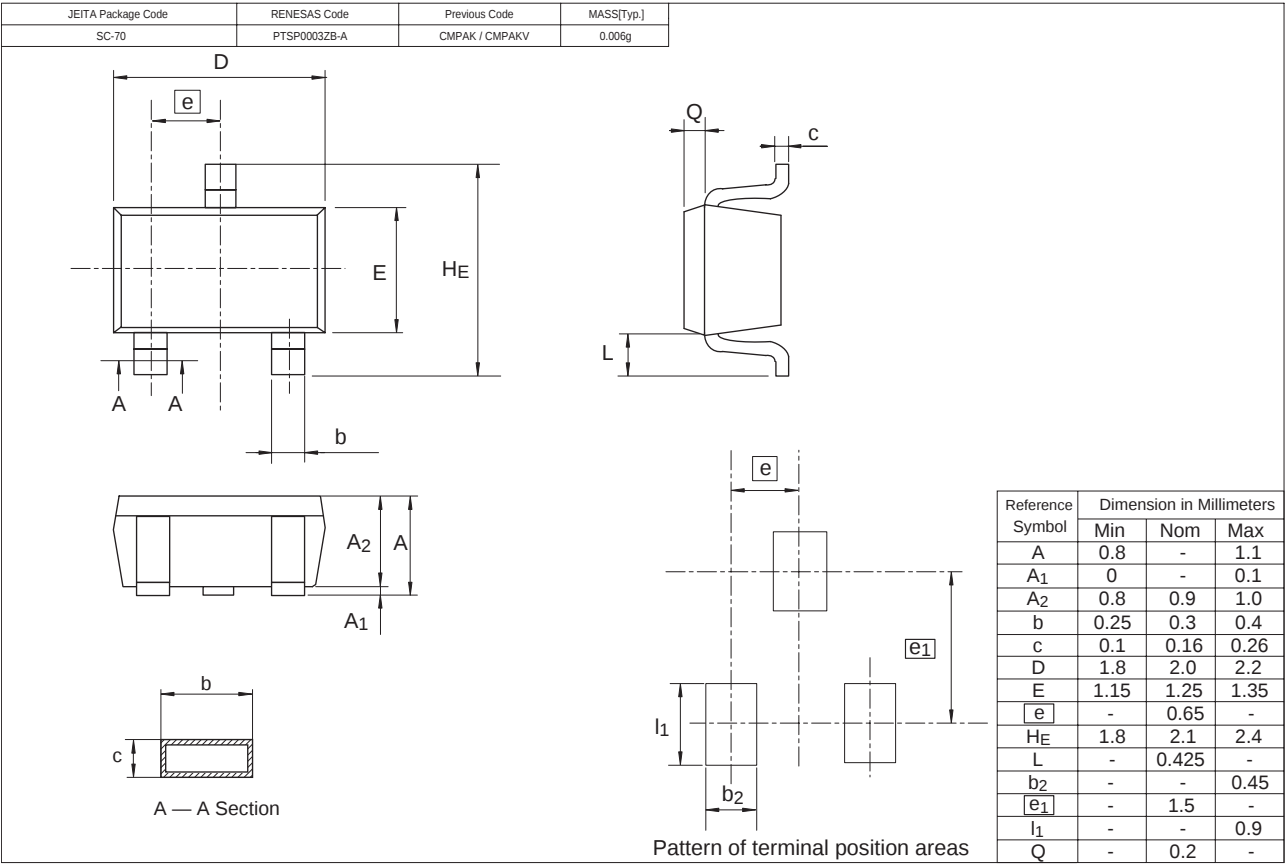


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions



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Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.

7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.

10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.

Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001