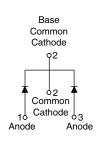


Hyperfast Rectifier, 30 A FRED Pt®





PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 15 A							
V_{R}	200 V							
V _F at I _F	0.78 V							
t _{rr} typ.	30 ns							
T _J max.	175 °C							
Package	D ² PAK (TO-263AB)							
Circuit configuration	Common cathode							

FEATURES

- · Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature



- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- · AEC-Q101 qualified, class 1 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Vishay Semiconductors 200 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS				
Peak repetitive reverse voltage		V_{RRM}		200	V				
Average rectified forward ourrent	per diode	1	T _C = 159 °C	15					
Average rectified forward current -	per device	I _F (AV)		30	Α				
Non-repetitive peak surge current		I _{FSM}	T _C = 25 °C	200					
Operating junction and storage temp	peratures	T _J , T _{Stg}		-55 to +175	°C				

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	200	-	-	V				
Forward voltage	V _F	I _F = 15 A	-	0.92	1.05	V				
		I _F = 15 A, T _J = 125 °C	-	0.78	0.85	V				
Deverage legisers of month	I _R	$V_R = V_R$ rated	-	-	10					
Reverse leakage current		T _J = 125 °C, V _R = V _R rated	-	5	300	μΑ				
Junction capacitance	C _T	V _R = 200 V	-	57	-	pF				
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH				



DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS				
	t _{rr}	$I_F = 1 A, dI_F/dt = 1$	-	-	30					
Reverse recovery time		T _J = 25 °C		-	26	-	ns - A			
		T _J = 125 °C	$I_F = 15 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_B = 160 \text{ V}$	-	40	-				
Dook receivent current	I _{RRM}	T _J = 25 °C		-	2.8	=				
Peak recovery current		T _J = 125 °C] ''	-	6.0	-				
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	37	-	nC			
		T _J = 125 °C		-	120	-	IIC			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}	-55	-	175	°C				
Thermal resistance, junction to case per diode	R _{thJC}	-	-	1.1	°C/W				
Wai-lat		-	2.0	-	g				
Weight		-	0.07	-	oz.				
Mounting torque		6.0		12	kgf ⋅ cm				
Mounting torque		(5.0)	-	(10)	(lbf · in)				
Marking device		Case style D ² PAK (TO-263AB)		30CTH02SH					

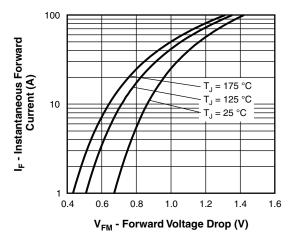


Fig. 1 - Maximum Forward Voltage Drop Characteristics

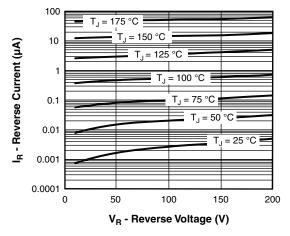


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

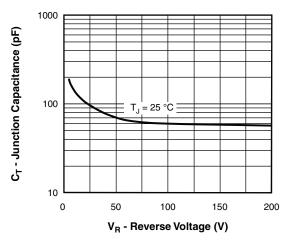


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

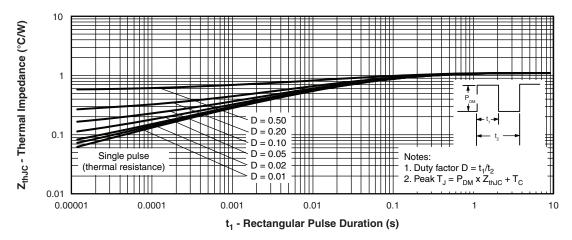


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

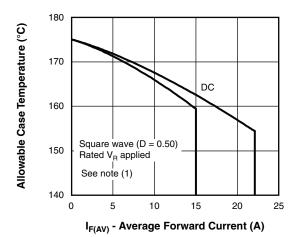


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

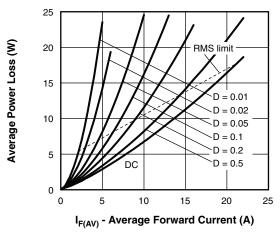
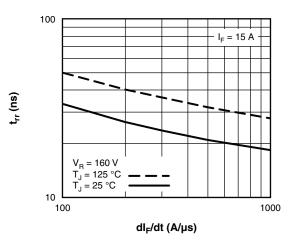


Fig. 6 - Forward Power Loss Characteristics





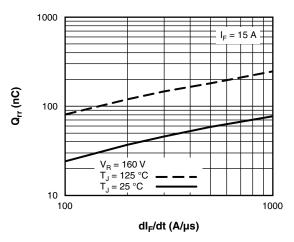
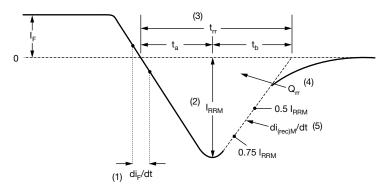


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_{rr}$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} x I_{RRM}}{2}$$

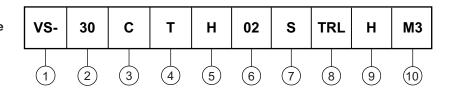
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 A)

C = common cathode

T = TO-220, D²PAK

- H = hyperfast rectifier

Voltage rating (02 = 200 V)

7 - • S = D²PAK

None = tube (50 pieces)

• TRL = tape and reel (left oriented, for D²PAK package)

• TRR = tape and reel (right oriented, for D²PAK package)

9 - H = AEC-Q101 qualified

- M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

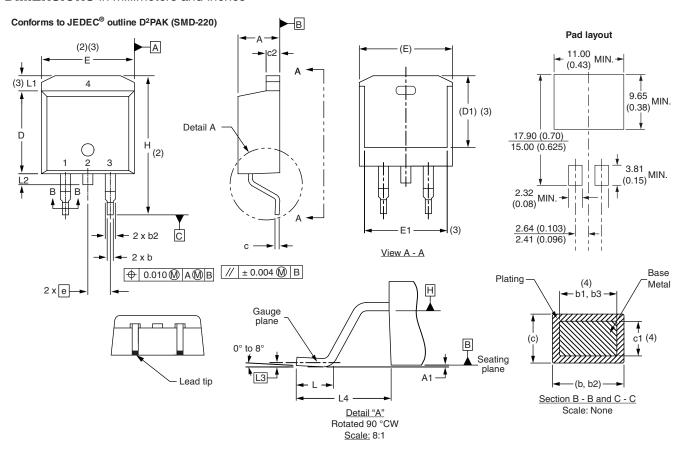
ORDERING INFORMATION (Example)									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION							
VS-30CTH02SHM3	50	Antistatic plastic tubes							
VS-30CTH02STRLHM3	800	13" diameter plastic tape and reel							
VS-30CTH02STRRHM3	800	13" diameter plastic tape and reel							
VS-30CTH02-1HM3	50	Antistatic plastic tubes							

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95046						
Part marking information	www.vishay.com/doc?95444						
Packaging information	www.vishay.com/doc?95032						



D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES		
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



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Vishay

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