

# T16M35T800HC

### **TRIACS** SILLICON BIDIRECTIONAL THYRISTORS

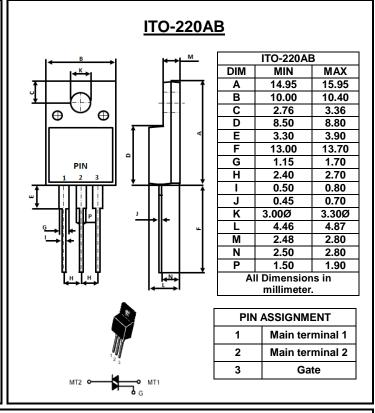
## **TRIACS** 16 AMPERES RMS 800 VOLTS

#### **FEATURES**

- Passivated die for reliability and uniformity
- Three-quadrant triggering
- Blocking voltage to 800V
- Low level triggering and holding characteristics
- · Isolated mounting base package

#### **MECHANICAL DATA**

· Case: Molded plastic Weight: 1.625 grams



#### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

#### **ABSOLUTE RATINGS**

PARAMETER	SYMBOL	VALUE	UNIT
Peak repetitive off-state voltage ( $T_J$ = -40 to 150°C, sine wave, 50 to 60 Hz; gate open) Peak repetitive off-state voltage ( $T_J$ = -40 to 125°C, sine wave, 50 to 60 Hz; gate open) (Note 1)	V <sub>DRM</sub> V <sub>RRM</sub>	800 800	٧
On-stage RMS current ( Full cycles sine wave, 60 Hz, $T_C = 80^{\circ}C$ )	I <sub>T(RMS)</sub>	16	А
Peak non-repetitive surge current (one full cycle 60 $H_Z$ , $T_J = 25^{\circ}C$ )	I <sub>TSM</sub>	150	А
Circuit fusing consideration ( t = 8.3ms)	I <sup>2</sup> T	93	A <sup>2</sup> S
Peak gate power	P <sub>GM</sub>	5	W
Operating junction temperature range	TJ	-40 to +150	°C
Storage temperature range	T <sub>STG</sub>	-40 to +150	°C
Dielectric strength from terminals to case, AC with t =1 minute, RH<30%	Vdis	2500	V
Note:		REV. 3, AUG2020	, KTXC42

(1)  $V_{DRM}$  and  $V_{RRM}$  for all types can be applied on a continuous basis.

Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

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### THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	VALUE	UNIT
Thermal resistance from junction to heatsink	RthJ <sub>C</sub> RthJ <sub>A</sub>	4.3 7.5	°C/W
Maximum lead temperature for soldering purposes (1/8" form case for 10 seconds)	TL	260	°C

#### OFF CHARACTERISTICS

PARAMETER	SYMBOL	MAX	UNIT
Peak repetitive forward or reverse blocking current @ $T_J = 25^{\circ}C$ ( $V_{AK} = \text{rated } V_{DRM} \text{ and } V_{RRM}$ , gate open) @ $T_J = 150^{\circ}C$	I <sub>DRM</sub>	5	uA
	I <sub>RRM</sub>	2	mA

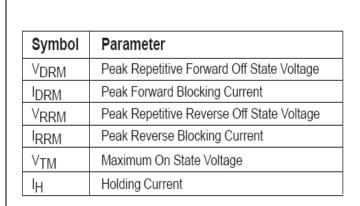
#### ON CHARACTERISTICS

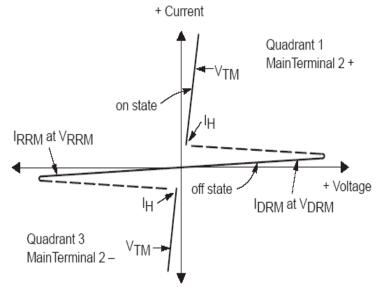
PARAMETER	SYMBOL	TYP.	MAX	UNIT
Peak forward on-state voltage ( $I_{TM}=\pm16A~$ peak @ $T_P\leq 2.0$ ms, duty cycle $\leq$ 2%)	V <sub>TM</sub>	1.3	1.5	V
Gate trigger current ( $V_{AK}$ = 12V, RL=100 $\Omega$ )	I <sub>GT</sub> 1 I <sub>GT</sub> 2 I <sub>GT</sub> 3		35	mA
Gate trigger voltage ( $V_{AK}$ = 12V, RL=100 $\Omega$ )	V <sub>GT</sub> 1 V <sub>GT</sub> 2 V <sub>GT</sub> 3	0.9	1	V
Holding current ( $V_{AK} = 12V$ , RL=100 $\Omega$ )	I <sub>H</sub> 1 I <sub>H</sub> 3		35 50	mA
Latching current ( $V_{AK}$ = 12V, RL=100 $\Omega$ )	I <sub>L</sub> 1 I <sub>L</sub> 2 I <sub>L</sub> 3		50 60 50	mA

### DYNAMIC CHARACTERISTICS

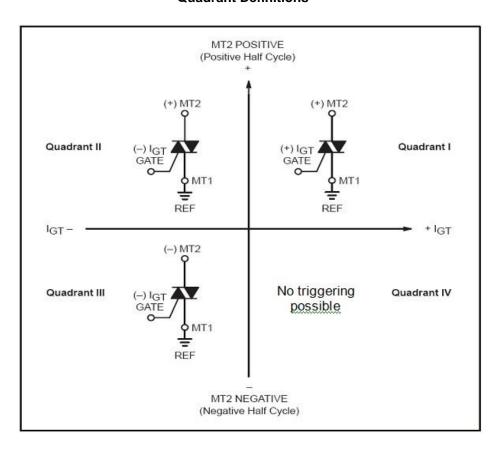
PARAMETER	SYMBOL	MIN.	UNIT
Critical rate of rise of off-stage voltage ( $V_{AK} = 67\%$ rated $V_{DRM}$ , exponential waveform @ TJ = 150°C, gate open)	dv/dt	500	V/us





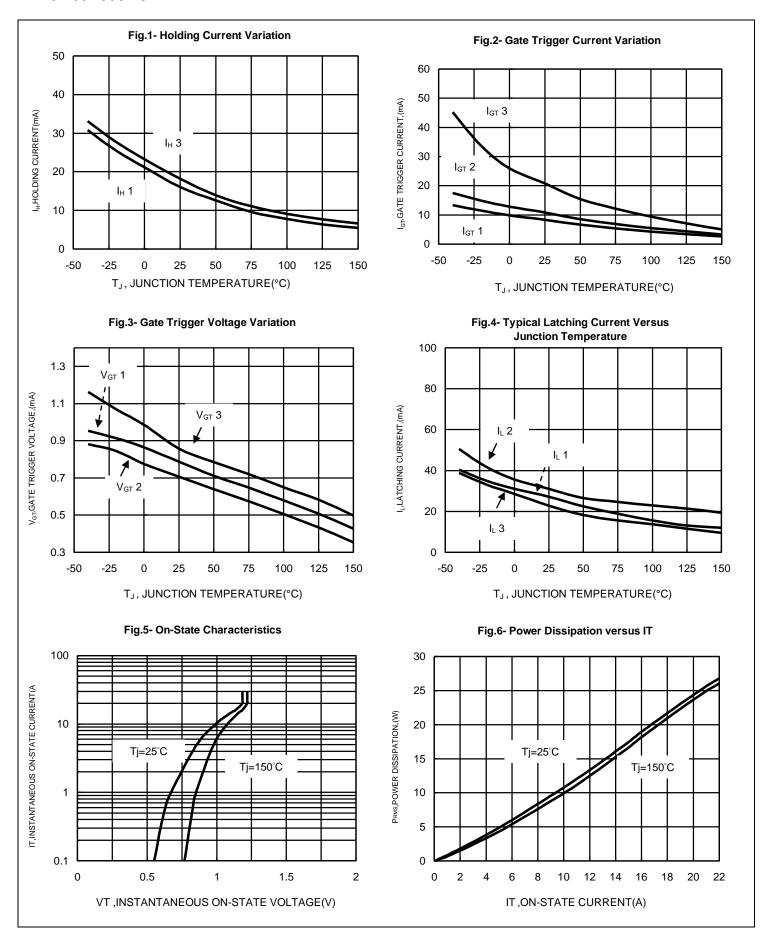


### **Quadrant Definitions**

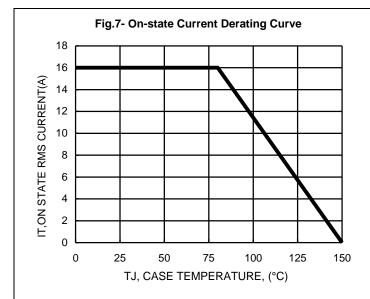


All polarities are referenced to MT1
With in -phase signal (using standard AC lines) quadrants I and III are used











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