LI0402B800R-10

UNCONTROLLED DOCUMENT

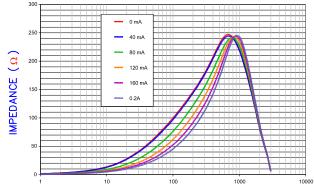
PHYSICAL DIMENSIONS:

A 1.01 [.040] ± 0.18 [.007] B 0.50 [.020] ± 0.20 [.008]

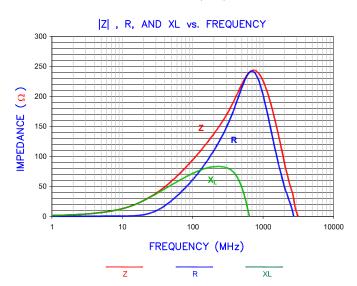
C 0.50 [.020] ± 0.20 [.008]

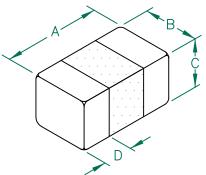
D 0.30 [.012] \pm MAX.

Z vs. FREQUENCY IMPEDANCE UNDER DC BIAS











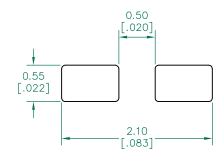
ELECTRICAL CHARACTERISTICS:										
Z @ 100M (Ω)	1Hz	DCR (Ω)	Rated Current							
Nominal	80									
Minimum	60									
Maximum	100	0.80	200 mA							

NOTES: UNLESS OTHERWISE SPECIFIED

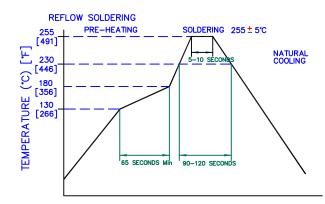
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 10,000 PCS/REEL, PAPER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING

RECOMMENDED SOLDERING CONDITIONS



(For wave soldering, add 0.762 [.030] to this dimension.)





		DIMENSIONS ARE IN mm [INCHE	This print is the property of Lair								
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					written consent of Laird Tech. Al rights to design or invention are	'	_				
					reserved.						
	D	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE			PROJECT/PART NUMBER:	T	REV	PART TO	PE;	DRAWN BY:	
	_				LI0402B800R-10	Т	D	co-	FIRE	JRK	
	С	CORRECT Z SPECS	09/11/08	JRK	LIU402B000K-10					•	
	В	UPDATE COMPANY LOGO	07/08/08	JRK	DATE: 05/10/06 S	CAL	E: N	TS	SHEET:	•	
	Α	ORIGINAL DRAFT	05/10/06	JRK	CAD #	OOL			2	of 2	
	REV	DESCRIPTION	DATE	INT			•	-	_	· -	

AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 5644