

PCB LAYOUT

The diagram shows a rectangular PCB layout with a break in the middle. Dimensions and tolerances are indicated:

- A circular feature on the left is dimensioned as $\phi 1.40 \pm 0.10$ with a tolerance of 0.00 .
- Two circular features in the center are dimensioned as 3.81 ± 0.03 .
- A circular feature on the right is dimensioned as 1.30 .

UL/CUL 技术参数 UL/CUL Technical Data				
等 级	Use Group	B	C	D
额定电压	Rated Voltage [V]	300	-	300
额定电流	Rated Current [A]	8	-	10
接线范围 单/多芯线	Wire Range Solid/Stranded [AWG]	-		
耐电压	Withstanding Voltage	1000V/1Min		
IEC 技术参数 IEC Technical Data				
过电压类别	Overvoltage Category	III	III	II
污染等级	Contamination Class	3	2	2
额定电压	Rated Voltage [V]	-	100	320
额定电流	Rated Current [A]	-	7	7
额定冲击电压 Impulse withstand voltage	[kV]	-	2.5	2.5
导线截面积 硬质/柔性导线	Conductor cross section Solid/flexible [mm²]	-		
材 料 Material				
绝缘材料	Insulation Material	PA66		
阻燃等级	Inflammability	UL94V-0		
导体材料	Conductor Material	铜合金 Copper alloy		
导体表面镀层	Contactive Coating	镀锡 Tin Plated		
一般参数 Other Data				
间距	Pitch [mm]	3.81		
极数	Pole [P]	2-24		
剥线长度	Stripping Length [mm]	-		
扭矩	Screw/Torque [N.m]	-		
工作温度	Operating temperatur [°C]	-40~+105		
引脚尺寸	Pin Dimensions [mm]	3.7		
PCB板孔径	PCB Hole Diameter [mm]	1.4		

DES.	FZM	DATE	21.03.15	 慈溪市科发电子有限公司 CIXI KEFA ELECTRONIC CO., LTD	单位/UNITS	mm	尺寸/SIZE	A4	比例/SCALE	1:1	第三视角/THIRD ANGLE PROJECTION	版本/REV	A
CHK.	SY	DATE	21.03.15										名称/TITLE KF2EDGRM-3.81
APP.	CLF	DATE	21.03.15										