TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

# **TK15A50D**

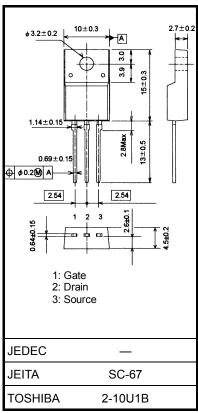
### **Switching Regulator Applications**

Unit: mm

- Low drain-source ON-resistance: RDS (ON) =  $0.24 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 7.0 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 500 \text{ V)}$
- Enhancement mode:  $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	500	V
Gate-source voltage		$V_{GSS}$	±30	V
Drain current	DC (Note 1)	I <sub>D</sub>	15	Α
	Pulse (Note 1)	I <sub>DP</sub>	60	A
Drain power dissipation	on (Tc = 25°C)	P <sub>D</sub>	50	W
Single pulse avalanch	ne energy (Note 2)	E <sub>AS</sub>	542	mJ
Avalanche current		I <sub>AR</sub>	15	Α
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	5.0	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

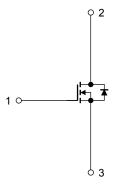
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.5	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 4.1 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 15 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2008-07

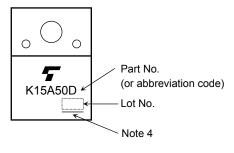
# Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rent	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	_	_	10	μΑ
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	500	_	_	V
Gate threshold ve	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON	-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.5 A	_	0.24	0.3	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 7.5 A	2.0	7.0	_	S
Input capacitance		C <sub>iss</sub>		_	2300		
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	10		pF
Output capacitan	Output capacitance			_	250	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 0 \text{ V}$ $V_{GS} = 0 \text{ V}$ $V_{DD} \approx 200 \text{ V}$ $V_{DD} \approx 10 \text{ J}$ $V_{DD} \approx 200 \text{ V}$ $V_{DD} \approx 10 \text{ J}$	_	50	_	
	Turn-on time	t <sub>on</sub>		_	100	_	
	Fall time	t <sub>f</sub>		_	25		ns ns
	Turn-off time	t <sub>off</sub>		_	140	_	
Total gate charge		Qg		_	40	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	_	25	_	nC
Gate-drain charge		Q <sub>gd</sub>			15	_	

## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

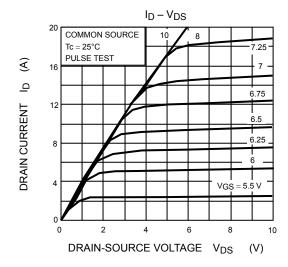
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	15	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	60	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V,	_	1600	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	20	_	μС

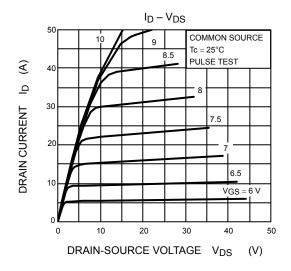
### Marking

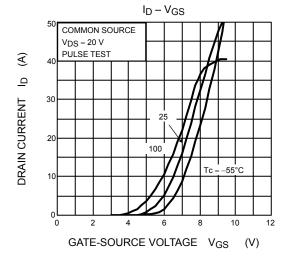


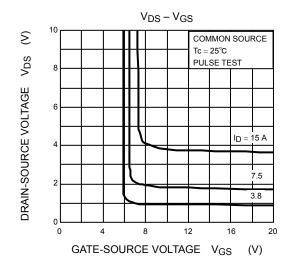
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

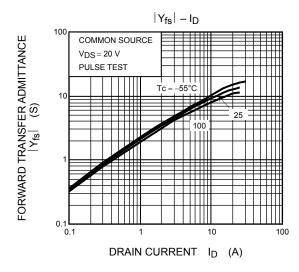
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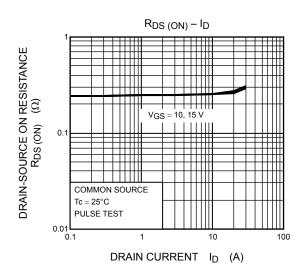


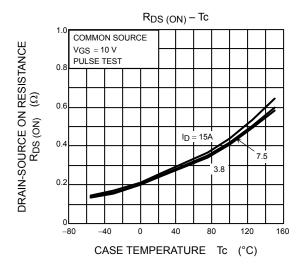


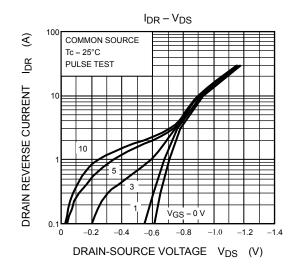


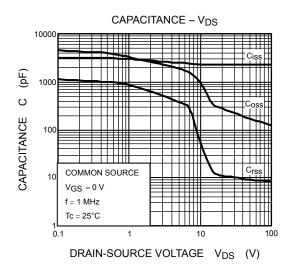


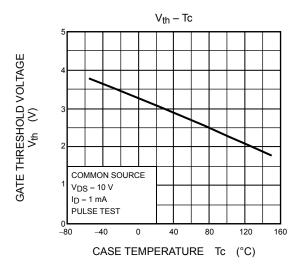


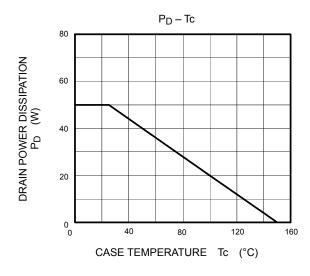


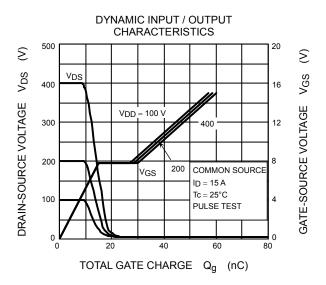


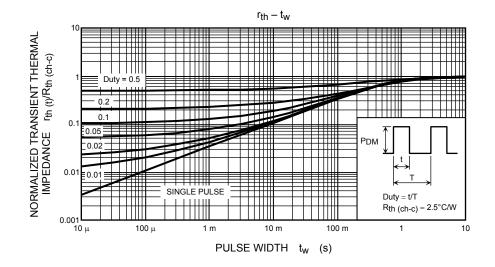


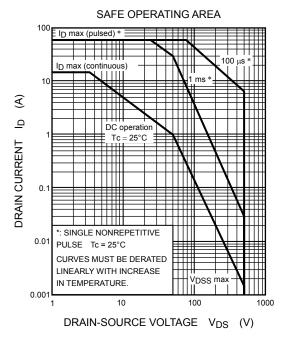


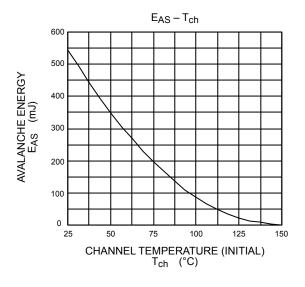


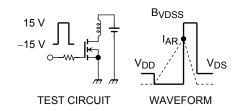












$$R_G = 25~\Omega$$
 
$$V_{DD} = 90~V,~L = 4.1~mH$$

$$EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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