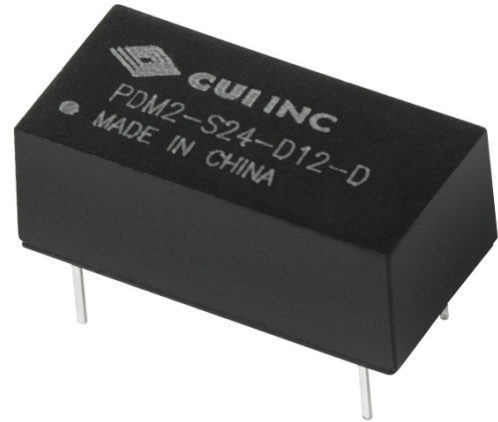


**SERIES:** PDM2-D | **DESCRIPTION:** DC-DC CONVERTER

**FEATURES**

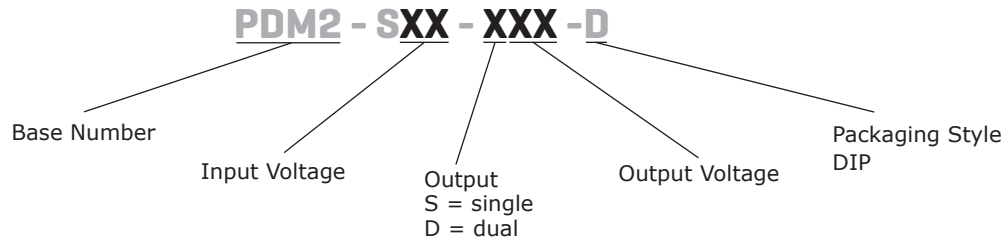
- 2 W isolated output
- smaller package
- single/dual unregulated output
- 1,500 Vdc isolation
- short circuit protection
- extended temperature range (-40~105°C)
- antistatic protection up to 8kV
- high efficiency at light load
- efficiency up to 85%



| MODEL                         | input voltage |                | output voltage<br>(Vdc) | output current |             | output power<br>max<br>(W) | ripple and noise <sup>2</sup><br>typ<br>(mVp-p) | efficiency<br>typ<br>(%) |
|-------------------------------|---------------|----------------|-------------------------|----------------|-------------|----------------------------|-------------------------------------------------|--------------------------|
|                               | typ<br>(Vdc)  | range<br>(Vdc) |                         | min<br>(mA)    | max<br>(mA) |                            |                                                 |                          |
| PDM2-S3-S5-D*                 | 3.3           | 2.97~3.63      | 5                       | 40             | 400         | 2                          | 75                                              | 78                       |
| PDM2-S5-S5-D <sup>1,*</sup>   | 5             | 4.5~5.5        | 5                       | 40             | 400         | 2                          | 75                                              | 80                       |
| PDM2-S5-S12-D <sup>1,*</sup>  | 5             | 4.5~5.5        | 12                      | 17             | 167         | 2                          | 75                                              | 84                       |
| PDM2-S5-S15-D <sup>1,*</sup>  | 5             | 4.5~5.5        | 15                      | 13             | 133         | 2                          | 75                                              | 84                       |
| PDM2-S5-D5-D <sup>1,*</sup>   | 5             | 4.5~5.5        | ±5                      | ±20            | ±200        | 2                          | 75                                              | 80                       |
| PDM2-S5-D7-D*                 | 5             | 4.5~5.5        | ±7                      | ±15            | ±142        | 2                          | 75                                              | 80                       |
| PDM2-S5-D9-D <sup>1,*</sup>   | 5             | 4.5~5.5        | ±9                      | ±11            | ±111        | 2                          | 75                                              | 84                       |
| PDM2-S5-D12-D <sup>1,*</sup>  | 5             | 4.5~5.5        | ±12                     | ±8             | ±83         | 2                          | 75                                              | 84                       |
| PDM2-S5-D15-D <sup>1,*</sup>  | 5             | 4.5~5.5        | ±15                     | ±7             | ±67         | 2                          | 75                                              | 84                       |
| PDM2-S12-S5-D <sup>1,*</sup>  | 12            | 10.8~13.2      | 5                       | 40             | 400         | 2                          | 75                                              | 80                       |
| PDM2-S12-S12-D <sup>1,*</sup> | 12            | 10.8~13.2      | 12                      | 17             | 167         | 2                          | 75                                              | 82                       |
| PDM2-S12-S24-D <sup>1,*</sup> | 12            | 10.8~13.2      | 24                      | 8              | 83          | 2                          | 75                                              | 84                       |
| PDM2-S12-D5-D <sup>1,*</sup>  | 12            | 10.8~13.2      | ±5                      | ±20            | ±200        | 2                          | 75                                              | 80                       |
| PDM2-S12-D9-D <sup>1,*</sup>  | 12            | 10.8~13.2      | ±9                      | ±11            | ±111        | 2                          | 75                                              | 84                       |
| PDM2-S12-D12-D <sup>1,*</sup> | 12            | 10.8~13.2      | ±12                     | ±8             | ±83         | 2                          | 75                                              | 83                       |
| PDM2-S12-D15-D <sup>1,*</sup> | 12            | 10.8~13.2      | ±15                     | ±7             | ±67         | 2                          | 75                                              | 84                       |
| PDM2-S24-S5-D <sup>1,*</sup>  | 24            | 21.6~26.4      | 5                       | 40             | 400         | 2                          | 75                                              | 79                       |
| PDM2-S24-S9-D <sup>1,*</sup>  | 24            | 21.6~26.4      | 9                       | 22             | 222         | 2                          | 75                                              | 85                       |
| PDM2-S24-S12-D <sup>1,*</sup> | 24            | 21.6~26.4      | 12                      | 17             | 167         | 2                          | 75                                              | 83                       |
| PDM2-S24-D5-D <sup>1,*</sup>  | 24            | 21.6~26.4      | ±5                      | ±20            | ±200        | 2                          | 75                                              | 79                       |
| PDM2-S24-D12-D <sup>1,*</sup> | 24            | 21.6~26.4      | ±12                     | ±8             | ±83         | 2                          | 75                                              | 83                       |
| PDM2-S24-D15-D <sup>1,*</sup> | 24            | 21.6~26.4      | ±15                     | ±7             | ±67         | 2                          | 75                                              | 84                       |

Notes: 1. UL approved  
 2. Ripple and noise are measured at 20 MHz BW by "parallel cable" method with 1 µF ceramic and 10 µF electrolytic capacitors on the output.  
 3. \* Discontinued model.

## PART NUMBER KEY



## INPUT

| parameter               | conditions/description  | min  | typ | max  | units |
|-------------------------|-------------------------|------|-----|------|-------|
| operating input voltage | 3.3 Vdc input models    | 2.97 | 3.3 | 3.63 | Vdc   |
|                         | 5 Vdc input models      | 4.5  | 5   | 5.5  | Vdc   |
|                         | 12 Vdc input models     | 10.8 | 12  | 13.2 | Vdc   |
|                         | 24 Vdc input models     | 21.6 | 24  | 26.4 | Vdc   |
| surge voltage           | for maximum of 1 second |      |     |      |       |
|                         | 3.3 Vdc input models    | -0.7 |     | 5    | Vdc   |
|                         | 5 Vdc input models      | -0.7 |     | 9    | Vdc   |
|                         | 12 Vdc input models     | -0.7 |     | 18   | Vdc   |
|                         | 24 Vdc input models     | -0.7 |     | 30   | Vdc   |
| filter                  | capacitance filter      |      |     |      |       |

## OUTPUT

| parameter               | conditions/description           | min | typ | max   | units |
|-------------------------|----------------------------------|-----|-----|-------|-------|
| line regulation         | for Vin change of 1%             |     |     | ±1.2  | %     |
| load regulation         | measured from 10%~100% load      |     |     |       |       |
|                         | 5 Vdc output models              |     | 12  |       | %     |
|                         | 9 Vdc output models              |     | 9   |       | %     |
|                         | 12 Vdc output models             |     | 8   |       | %     |
|                         | 15 Vdc output models             |     | 7   |       | %     |
|                         | 24 Vdc output models             |     | 6   |       | %     |
| voltage accuracy        | see tolerance envelope curve     |     |     |       |       |
| switching frequency     | 100% load, nominal input voltage |     | 100 |       | kHz   |
| temperature coefficient | at 100% load                     |     |     | ±0.03 | %/°C  |

## PROTECTIONS

| parameter                             | conditions/description | min | typ | max | units |
|---------------------------------------|------------------------|-----|-----|-----|-------|
| short circuit protection <sup>1</sup> |                        |     |     | 1   | s     |

Notes: 1. The supply voltage must be discontinued at the end of the short circuit duration

## SAFETY AND COMPLIANCE

| parameter                     | conditions/description                                                                                                 | min       | typ | max | units |
|-------------------------------|------------------------------------------------------------------------------------------------------------------------|-----------|-----|-----|-------|
| isolation voltage             | input to output for 1 minute, 1 mA max. leakage current                                                                | 1,500     |     |     | Vdc   |
| isolation resistance          | input to output at 500 Vdc                                                                                             | 1,000     |     |     | MΩ    |
| safety approvals <sup>2</sup> | certified to 60950-1: UL                                                                                               |           |     |     |       |
| conducted emissions           | CISPR22/EN55022, class B, external circuit required                                                                    |           |     |     |       |
| radiated emissions            | CISPR22/EN55022, class B, external circuit required                                                                    |           |     |     |       |
| ESD                           | IEC/EN61000-4-2, class B, contact ± 8kV for single outputs<br>IEC/EN61000-4-2, class B, contact ± 6kV for dual outputs |           |     |     |       |
| MTBF                          | as per MIL-HDBK-217F at 25°C                                                                                           | 3,500,000 |     |     | hours |
| RoHS                          | 2011/65/EU                                                                                                             |           |     |     |       |

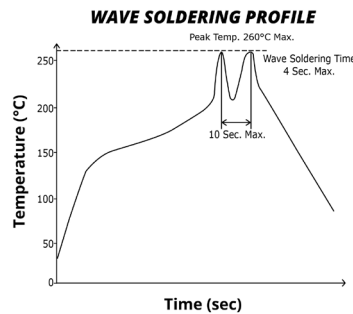
Notes: 2. See specific models listed on page 1

## ENVIRONMENTAL

| parameter             | conditions/description | min | typ | max | units |
|-----------------------|------------------------|-----|-----|-----|-------|
| operating temperature | see derating curve     | -40 |     | 105 | °C    |
| storage temperature   |                        | -55 |     | 125 | °C    |
| storage humidity      | non-condensing         |     |     | 95  | %     |
| temperature rise      | Ta=25°C, 100% load     |     | 25  |     | °C    |

## SOLDERABILITY

| parameter      | conditions/description          | min | typ | max | units |
|----------------|---------------------------------|-----|-----|-----|-------|
| hand soldering | 1.5 mm from case for 10 seconds |     |     | 300 | °C    |
| wave soldering | see wave soldering profile      |     |     | 260 | °C    |



## MECHANICAL

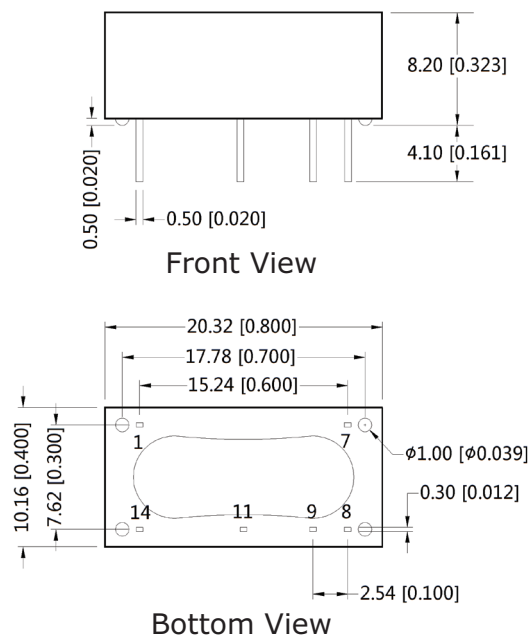
| parameter     | conditions/description                           | min | typ | max | units |
|---------------|--------------------------------------------------|-----|-----|-----|-------|
| dimensions    | 20.32 x 10.16 x 8.2 (0.800 x 0.400 x 0.323 inch) |     |     |     | mm    |
| case material | epoxy resin (UL94-V0)                            |     |     |     |       |
| weight        |                                                  |     | 2.8 |     | g     |

## MECHANICAL DRAWING

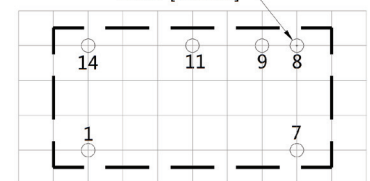
units: mm[inch]

tolerance:  $\pm 0.25[\pm 0.010]$ pin section tolerance:  $\pm 0.10[\pm 0.004]$ 

| PIN CONNECTIONS |               |             |
|-----------------|---------------|-------------|
| PIN             | Single Output | Dual Output |
| 1               | GND           | GND         |
| 7               | NC            | NC          |
| 8               | 0V            | 0V          |
| 9               | +Vo           | +Vo         |
| 11              | NO PIN        | -Vo         |
| 14              | Vin           | Vin         |



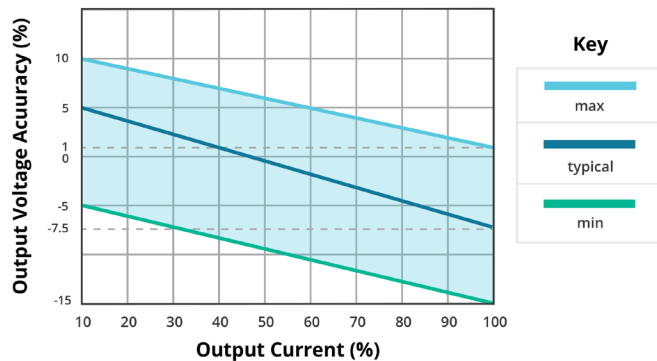
Note : Grid 2.54\*2.54mm

 $\phi 1.00 [\phi 0.039]$ 

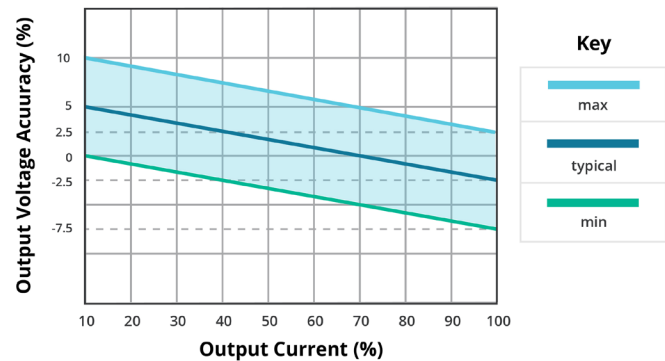
Top View  
PCB Layout

## DERATING CURVES

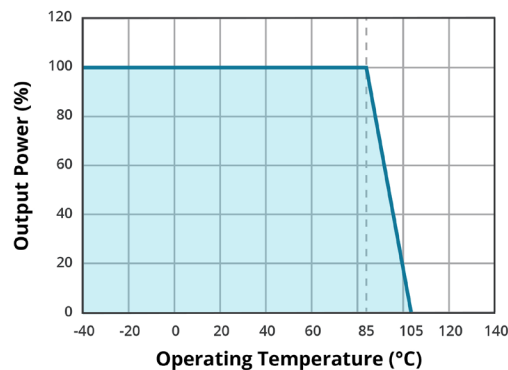
**OUTPUT REGULATION CURVE**  
3.3 Vdc output model  
(nominal input)



**OUTPUT REGULATION CURVE**  
all other output models  
(nominal input)

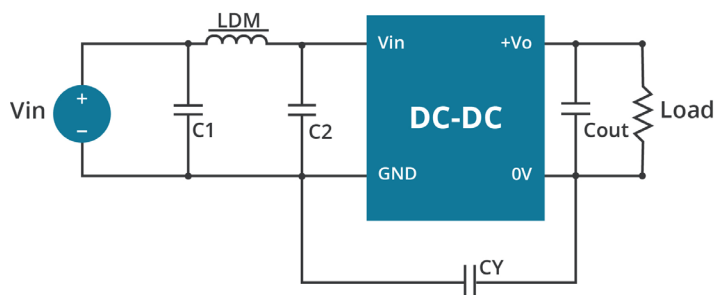


**TEMPERATURE DERATING CURVE**



## EMC RECOMMENDED CIRCUIT

**Figure 1**



**Table 1**

| Recommended external circuit components |           |         |       |
|-----------------------------------------|-----------|---------|-------|
| Vin (Vdc)                               | C1, C2    | CY      | LDM   |
| 3.3                                     | 4.7μF/50V | --      | 6.8μH |
| 5                                       | 4.7μF/50V | --      | 6.8μH |
| 12                                      | 4.7μF/50V | --      | 6.8μH |
| 24                                      | 4.7μF/50V | 1nF/2KV | 6.8μH |

Note: 1. See Table 2 for Cout values

## APPLICATION NOTES

### 1. Output load requirement

To ensure this module can operate efficiently and reliably, the minimum output load may not be less than 10% of the full load during operation. If the actual output power is low, connect a resistor at the output end in parallel to increase the load.

### 2. Overload Protection

Under normal operating conditions, the output circuit of this product has no protection against overload. The simplest method to add this is to add a circuit breaker to the circuit.

### 3. Recommended circuit

If you want to further decrease the input/output ripple, you can increase the capacitance accordingly or choose capacitors with low ESR (see Figure 2 & Table 2). However, the capacitance of the output filter capacitor must be appropriate. If the capacitance is too high, a startup problem might arise. For every channel of the output, to ensure safe and reliable operation, the maximum capacitance must be less than the maximum capacitive load (see Table 3).

Figure 2

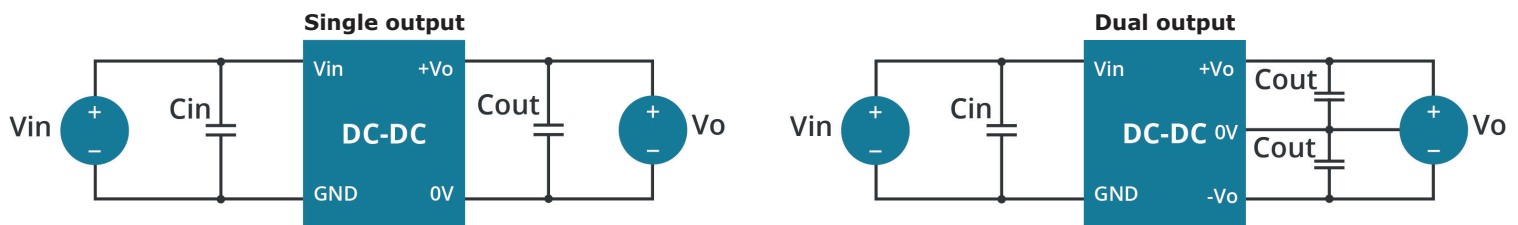


Table 2

| $V_{in}$<br>(Vdc) | $C_{in}$<br>( $\mu F$ ) | Single $V_o$<br>(Vdc) | $C_{out}$<br>( $\mu F$ ) | Dual $V_o$<br>(Vdc) | $C_{out}$<br>( $\mu F$ ) |
|-------------------|-------------------------|-----------------------|--------------------------|---------------------|--------------------------|
| 3.3               | 4.7                     | 5                     | 10                       | $\pm 5$             | 4.7                      |
| 5                 | 4.7                     | 9                     | 4.7                      | $\pm 9$             | 2.2                      |
| 12                | 2.2                     | 12                    | 2.2                      | $\pm 12$            | 1                        |
| 24                | 1                       | 15/24                 | 1                        | $\pm 15$            | 0.47                     |

Note: It's not recommended to connect any external capacitors in applications with less than 0.5 watt output.

Table 3

| Single $V_{out}$<br>(Vdc) | Max. Capacitive Load<br>( $\mu F$ ) | Dual $V_{out}$<br>(Vdc) | Max. Capacitive Load <sup>1</sup><br>( $\mu F$ ) |
|---------------------------|-------------------------------------|-------------------------|--------------------------------------------------|
| 5                         | 220                                 | 5                       | 100                                              |
| 9                         | 220                                 | 7                       | 100                                              |
| 12                        | 220                                 | 9                       | 100                                              |
| 15                        | 220                                 | 12                      | 100                                              |
| 24                        | 220                                 | 15                      | 100                                              |

Note: 1. For each output.

Note: 1. Operation under minimum load will not damage the converter; however, they may not meet all specifications listed.  
 2. Max. capacitive load tested at input voltage range and full load.  
 3. All specifications measured at:  $T_a=25^\circ C$ , humidity<75%, nominal input voltage and rated output load, unless otherwise specified.

## REVISION HISTORY

| rev. | description                                  | date       |
|------|----------------------------------------------|------------|
| 1.0  | initial release                              | 08/30/2013 |
| 1.01 | added UL approval to some models             | 02/23/2015 |
| 1.02 | updated tolerance envelope curves            | 02/01/2016 |
| 1.03 | safeties updated in features and safety line | 01/18/2021 |
| 1.04 | derating curves and circuit figures updated  | 07/12/2021 |
| 1.05 | CE removed                                   | 10/11/2022 |
| 1.06 | discontinued models                          | 06/29/2023 |
| 1.07 | discontinued model PDM2-S12-D5-D             | 09/26/2023 |

The revision history provided is for informational purposes only and is believed to be accurate.



**CUI INC**  
a bel group

**Headquarters**  
20050 SW 112th Ave.  
Tualatin, OR 97062  
**800.275.4899**

Fax 503.612.2383  
**cui.com**  
techsupport@cui.com

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