

MSKSEMI 美森科

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



PLED

AO3406

Product specification

Features

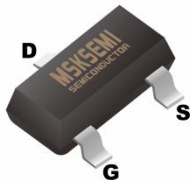
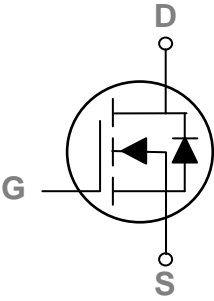

- 30V,3.5A , $R_{DS(ON)}=35m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- MB / VGA / Vcore
- Load Switch
- Hand-Held Instrument

BVDSS	RDSON	ID
30V	35mΩ	3.5A

Reference News

PACKAGE OUTLINE	PIN Configuration	Marking
 SOT-23-3L		

Absolute Maximum Ratings $T_c=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current - Continuous ($T_A=25^{\circ}C$)	3.5	A
	Drain Current - Continuous ($T_A=70^{\circ}C$)	1.68	A
I_{DM}	Drain Current - Pulsed ¹	14	A
P_D	Power Dissipation ($T_A=25^{\circ}C$)	278	mW
	Power Dissipation - Derate above $25^{\circ}C$	2.22	mW/ $^{\circ}C$
T_{STG}	Storage Temperature Range	-50 to 150	$^{\circ}C$
T_J	Operating Junction Temperature Range	-50 to 150	$^{\circ}C$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	450	$^{\circ}C/W$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =1mA	---	0.018	---	V/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =30V , V _{GS} =0V , T _J =25°C	---	---	1	uA
		V _{DS} =24V , V _{GS} =0V , T _J =125°C	---	---	10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V , V _{DS} =0V	---	---	±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =3.5A	---	35	50	mΩ
		V _{GS} =4.5V , I _D =2.5A	---	45	70	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1	1.5	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-3.2	---	mV/°C
g _{fs}	Forward Transconductance	V _{DS} =10V , I _D =2A	---	2.3	---	S

Dynamic and switching Characteristics

Q _g	Total Gate Charge ^{2, 3}	V _{DS} =24V , V _{GS} =10V , I _D =1A	---	3.1	---	nC
Q _{gs}	Gate-Source Charge ^{2, 3}		---	0.1	---	
Q _{gd}	Gate-Drain Charge ^{2, 3}		---	1.7	---	
T _{d(on)}	Turn-On Delay Time ^{2, 3}	V _{DD} =24V , V _{GS} =10V , R _G =3.3Ω I _D =1A	---	2.2	---	ns
T _r	Rise Time ^{2, 3}		---	6.9	---	
T _{d(off)}	Turn-Off Delay Time ^{2, 3}		---	15.2	---	
T _f	Fall Time ^{2, 3}		---	4.5	---	
C _{iss}	Input Capacitance	V _{DS} =25V , V _{GS} =0V , F=1MHz	---	245	---	pF
C _{oss}	Output Capacitance		---	40	---	
C _{rss}	Reverse Transfer Capacitance		---	78	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	3.5	A
I _{SM}	Pulsed Source Current		---	---	7.0	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A , T _J =25°C	---	---	1.3	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
3. Essentially independent of operating temperature.

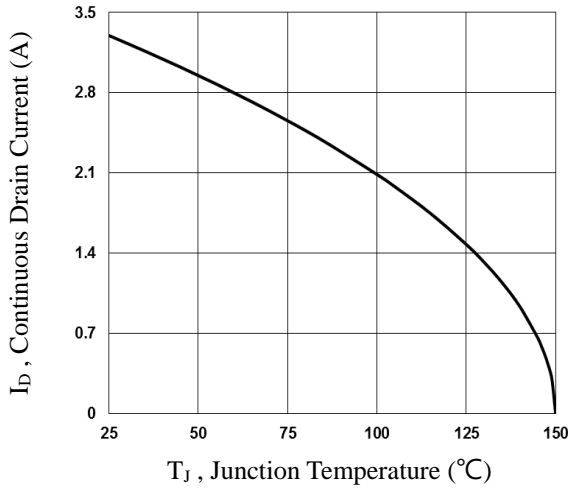


Fig.1 Continuous Drain Current vs. T_J

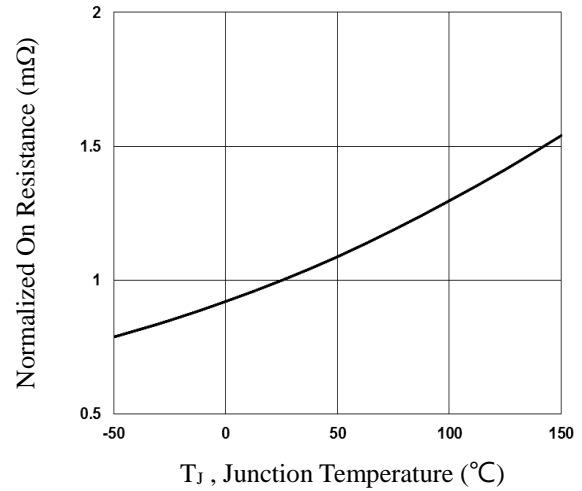


Fig.2 Normalized R_{DS(on)} vs. T_J

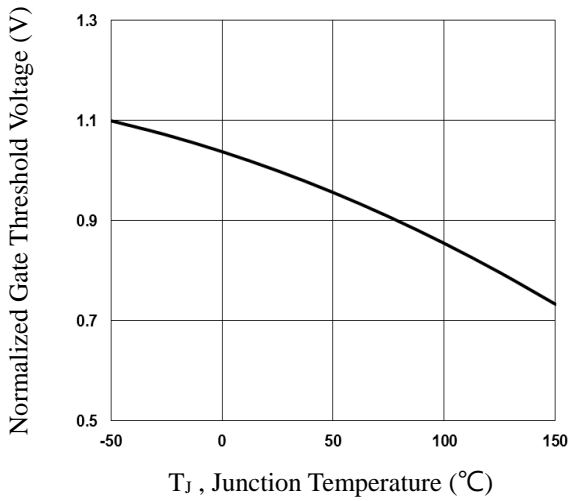


Fig.3 Normalized V_{th} vs. T_J

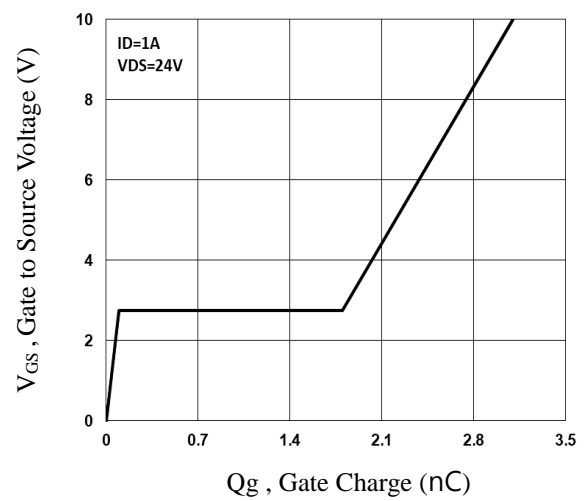


Fig.4 Gate Charge Waveform

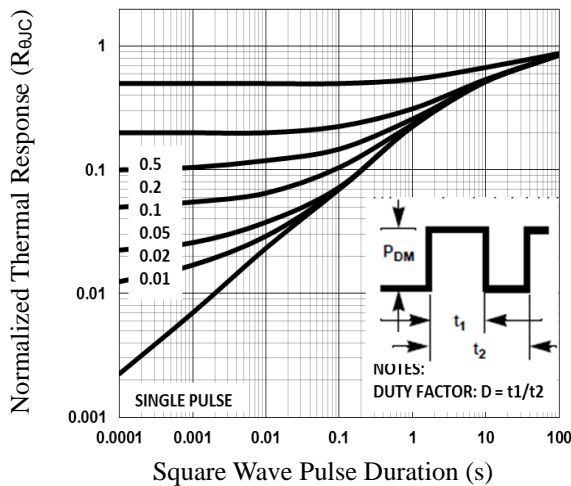


Fig.5 Normalized Transient Impedance

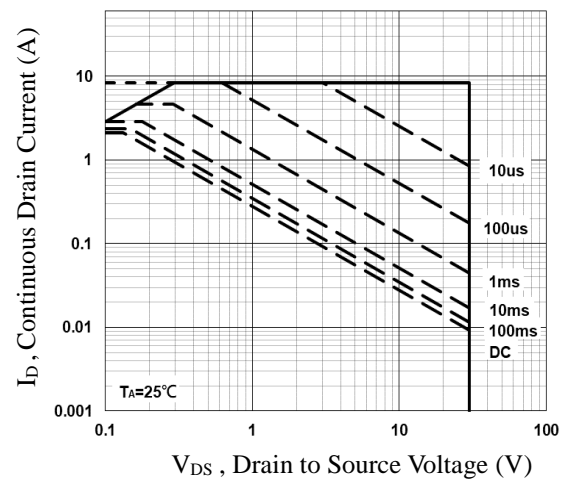


Fig.6 Maximum Safe Operation Area

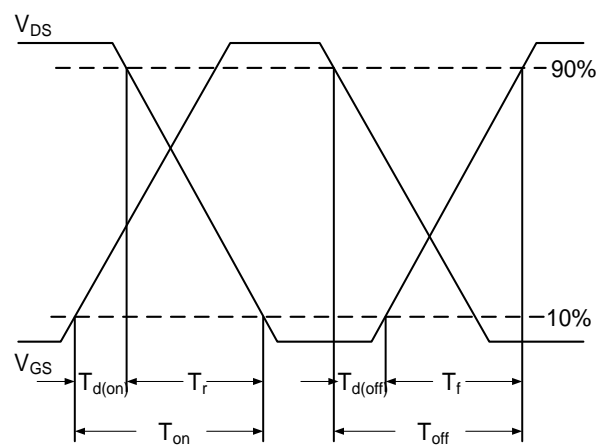


Fig.7 Switching Time Waveform

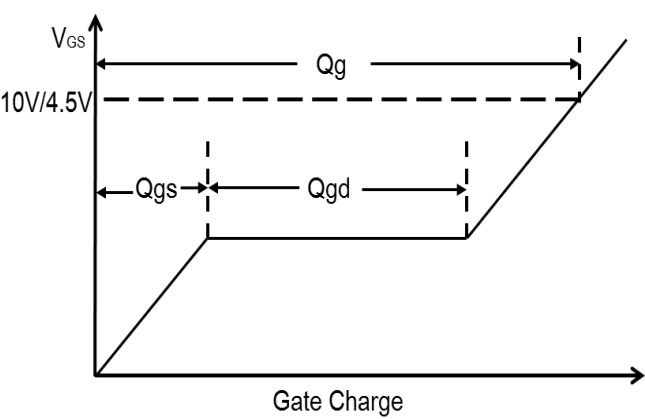
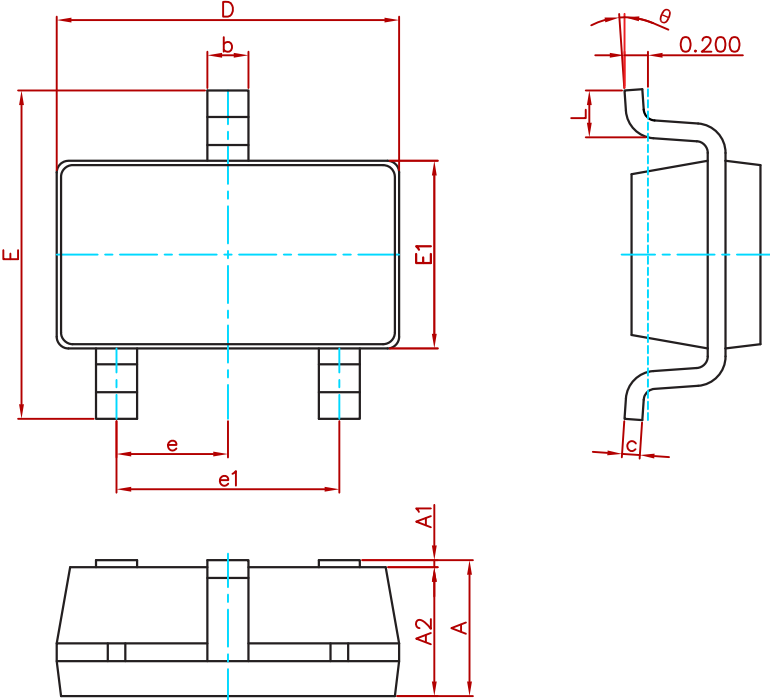


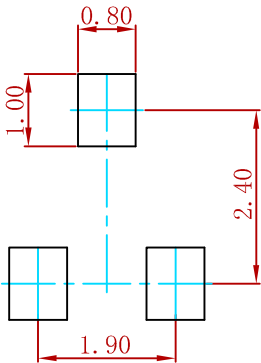
Fig.8 Gate Charge Waveform

PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

Suuggested Pad Layout



Note:
1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

REELS SPECIFICATION

P/N	PKG	QTY
AO3406	SOT-23-3L	3000

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