MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

 PLED

MS8205A

Product specification





Description

The MS8205A uses advanced trench technology to provide excellent R_{DS(ON)}, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

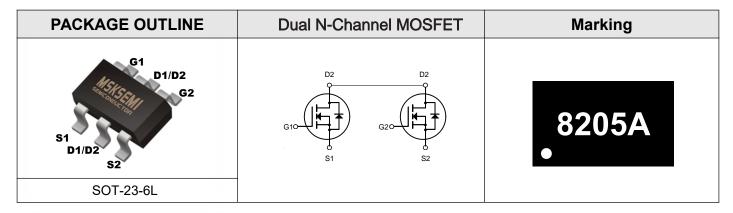
General Features

- V_{DS}=20V,I_D = 6A
- RDS(ON)<25m Ω @ VGS=4.5V

Application

- Battery protection
- Load switch
- Power management

Reference News



Absolute Maximum Ratings (TC=25℃unless otherwise noted)

Symbol	Parameter	Rating	Units
V _D S	Drain-Source Voltage	20	V
Vgs	Gate-Source Voltage	<u>+</u> 10	V
lo@Ta=25℃	Drain Current, V _{GS} @ 4.5V ³	6	А
Ідм	Pulsed Drain Current ¹	25	А
PD@TA=25℃	Total Power Dissipation	1.25	W
Тѕтс	Storage Temperature Range	-55 to 150	${\mathbb C}$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$
Rthj-a	Maximum Thermal Resistance, Junction- ambient ³	100	°C/W



Electrical Characteristics (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics				l l		
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	20	21	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 19.5V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	Igss	V _{GS} =±10V,V _{DS} =0V	_	-	±100	nA
On Characteristics (Note 3)			•	,	-	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	0.5	0.7	1.2	V
D : 0	_	V _{GS} =4.5V, I _D =4A	-	22	25	mΩ
Drain-Source On-State Resistance	RDS(ON)	V _{GS} =2.5V, I _D =3A	_	26	31	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =4A	-	10	-	S
Dynamic Characteristics (Note4)				'	'	
Input Capacitance	C _{lss}		-	600	-	PF
Output Capacitance	Coss	V _{DS} =8V,V _{GS} =0	_	330	-	PF
Reverse Transfer Capacitance	Crss	V, F=1.0MHz	-	140	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	18	-	nS
Turn-on Rise Time	tr	V_{DD} =10 V , I_{D} =1 A	_	5	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =4 V , R_{GEN} =10	_	43	-	nS
Turn-Off Fall Time	t _f	Ω	_	20	-	nS
Total Gate Charge	Qg		_	11	-	nC
Gate-Source Charge	Q _{gs}	$V_{DS}=10V,I_{D}=4$	_	2.3	-	nC
Gate-Drain Charge	Q _{gd}	A, V _{GS} =4.5V	-	2.5	-	nC
Drain-Source Diode Characteristics				l	J	
Diode Forward Voltage (Note 3)	VsD	V _{GS} =0V,I _S =2A	_	0.8	1.2	V
Diode Forward Current (Note 2)	ls		_	-	2	Α

Notes:

- $\textbf{1.} \ \ \textbf{Repetitive} \ \ \textbf{Rating: Pulse width limited by maximum junction temperature}.$
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics

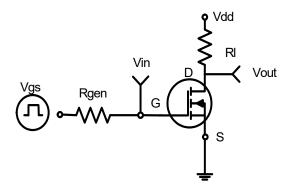


Figure 1:Switching Test Circuit

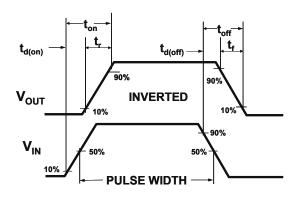


Figure 2:Switching Waveforms

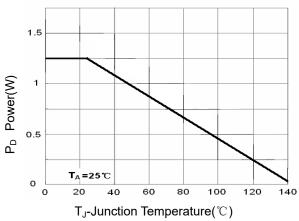


Figure 3 Power Dissipation

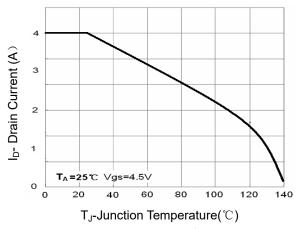


Figure 4 Drain Current

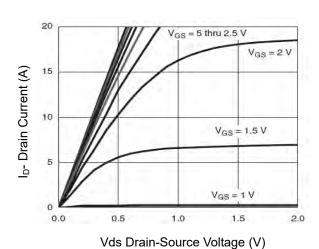


Figure 5 Output Characteristics

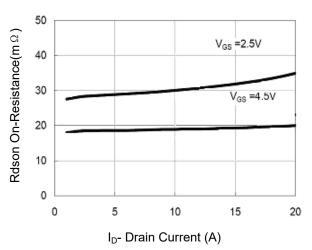


Figure 6 Drain-Source On-Resistance



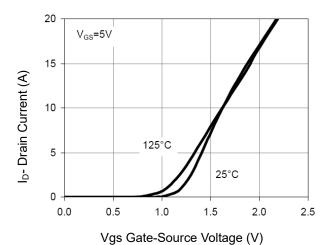


Figure 7 Transfer Characteristics

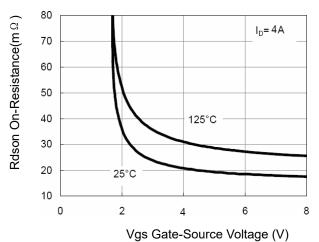
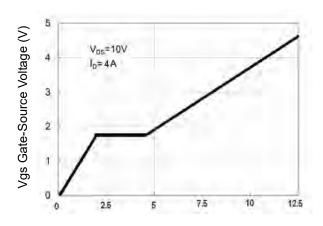
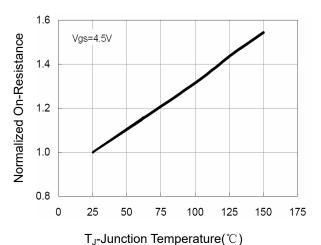


Figure 9 Rdson vs Vgs



Qg Gate Charge (nC) Figure 11 Gate Charge



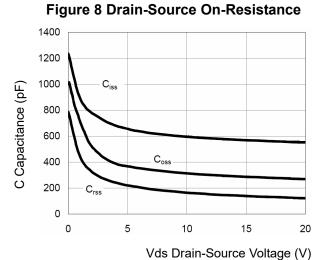
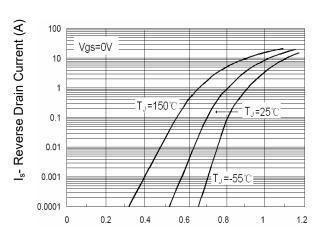


Figure 10 Capacitance vs Vds



Vsd Source-Drain Voltage (V)

Figure 12 Source- Drain Diode Forward

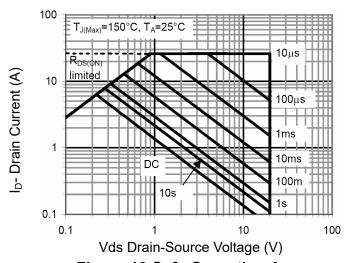


Figure 13 Safe Operation Area

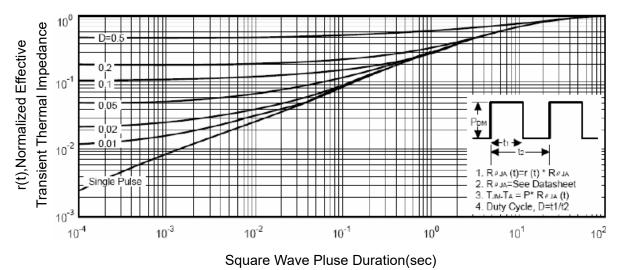
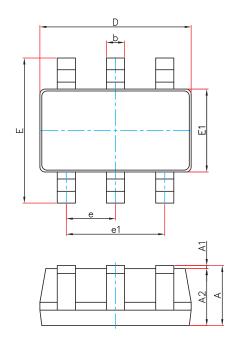
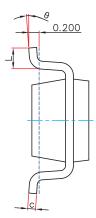


Figure 14 Normalized Maximum Transient Thermal Impedance



SOT-23-6L Package Outline Dimensions

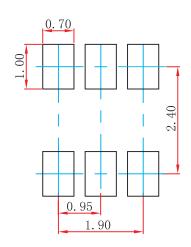




Symbol	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950(BSC)	0.037((BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

9012 P

SOT-23-6L Suggested Pad Layout



Note:

- 1.Controlling dimension: in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MS8205A	SOT-23-6L	3000



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