

#### **Features**

- 1.2kV Schottky Rectifier
- Zero Reverse Recovery Current
- **High-Frequency Operation**
- Temperature-Independent Switching
- Extremely Fast Switching

#### **Benefits**

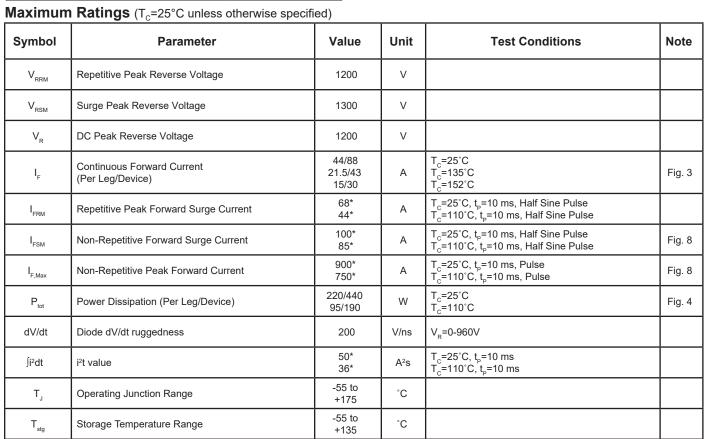
- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

#### **Applications**

- Switch Mode Power Supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free Wheeling Diodes in Inverter stages
- AC/DC converters







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8.8

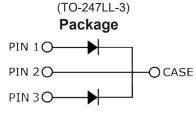
M3 Screw

6-32 Screw

Nm

lbf-in





TO-247 Mounting Torque

<sup>\*</sup> Per Leg, \*\* Per Device



#### **Electrical Characteristics (Per Leg)**

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V <sub>F</sub>	Forward Voltage	1.6 2.3	1.8 3	V	I <sub>F</sub> = 15 A T <sub>J</sub> =25°C I <sub>F</sub> = 15 A T <sub>J</sub> =175°C	Fig. 1
I <sub>R</sub>	Reverse Current	35 120	200 300	μΑ	V <sub>R</sub> = 1200 V T <sub>J</sub> =25°C V <sub>R</sub> = 1200 V T <sub>J</sub> =175°C	Fig. 2
Q <sub>c</sub>	Total Capacitive Charge	77.5		nC	$V_R = 800 \text{ V, } I_F = 15\text{A}$ $di/dt = 200 \text{ A/}\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	Fig. 5
С	Total Capacitance	1200 70 50		pF	V <sub>R</sub> = 0 V, T <sub>J</sub> = 25°C, f = 1 MHz V <sub>R</sub> = 400 V, T <sub>J</sub> = 25°C, f = 1 MHz V <sub>R</sub> = 800 V, T <sub>J</sub> = 25°C, f = 1 MHz	Fig. 6
E <sub>c</sub>	Capacitance Stored Energy	22.1	μJ V <sub>R</sub> = 800 V		V <sub>R</sub> = 800 V	Fig. 7

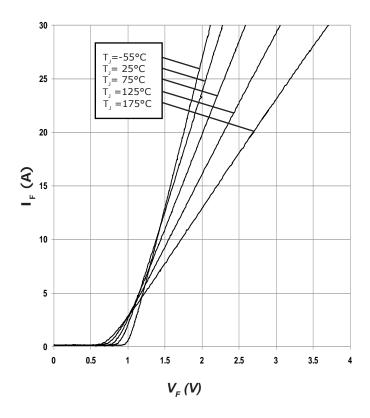
Note: This is a majority carrier diode, so there is no reverse recovery charge.

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Unit	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	0.34** 0.68*	°C/W	Fig. 9

<sup>\*\*</sup> Per Device, \* Per Leg

# **Typical Performance (Per Leg)**



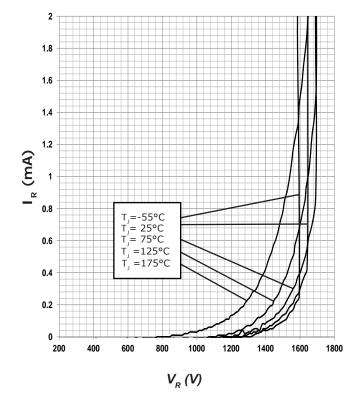
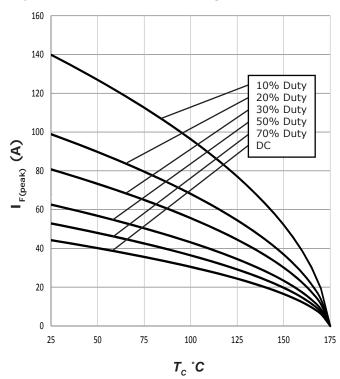


Figure 1. Forward Characteristics

Figure 2. Reverse Characteristics



### **Typical Performance (Per Leg)**



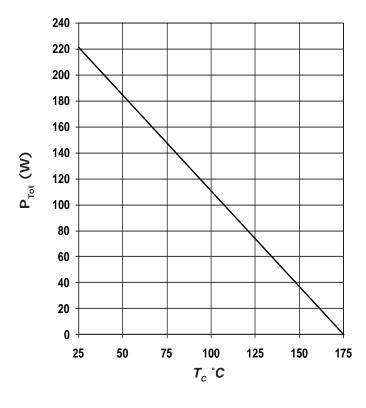


Figure 3. Current Derating

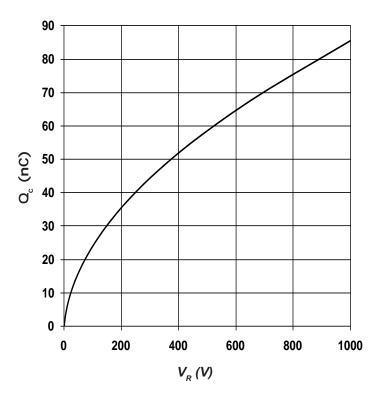


Figure 4. Power Derating

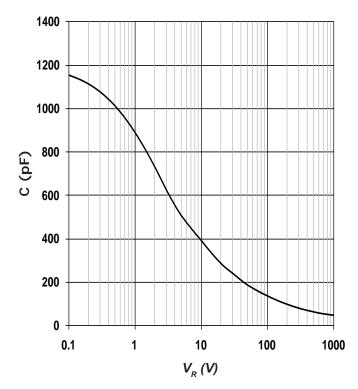
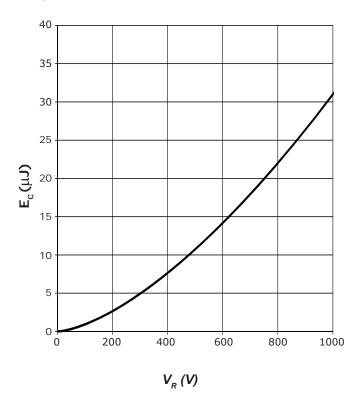


Figure 5. Recovery Charge vs. Reverse Voltage

Figure 6. Capacitance vs. Reverse Voltage

### **Typical Performance**



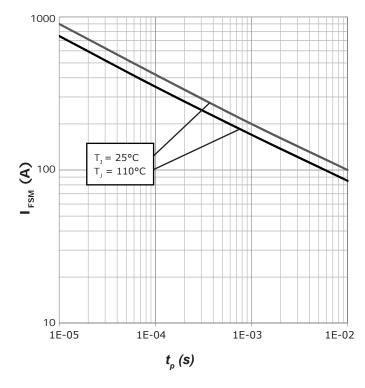


Figure 7. Typical Capacitance Stored Energy, per leg

Figure 8. Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform), per leg

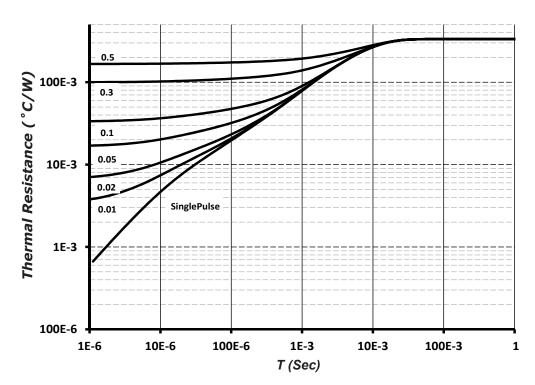


Figure 9. Device Transient Thermal Impedance



#### **Diode Model**

$$\begin{array}{c|c} - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & &$$

$$=Vf_{T}$$
  $V_{T} + If * R_{T}$ 

$$V_T = 0.97 + (T_j^* - 2.12*10^{-3})$$

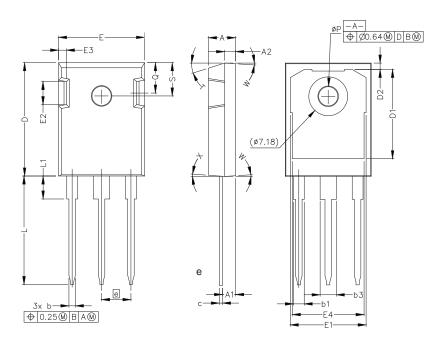
$$R_T = 0.031 + (T_j * 3.92*10^{-4})$$

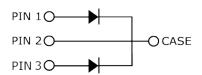
Note: T<sub>j</sub> = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C



## **Package Dimensions**

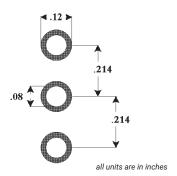
Package TO-247(TO-247LL-3)

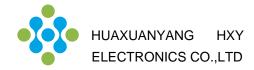




POS	Inc	hes	Millimeters			
PUS	Min	Max	Min	Max		
А	.190	.205	4.83	5.21		
A1	.090	.100	2.29	2.54		
A2	.075	.085	1.91	2.16		
b	.042	.052	1.07	1.33		
b1	.075	.095	1.91	2.41		
b3	.113	.133	2.87	3.38		
С	.022	.027	0.55	0.68		
D	.819	.831	20.80	21.10		
D1	.640	.695	16.25	17.65		
D2	.037	.049	0.95	1.25		
Е	.620	.635	15.75	16.13		
E1	.516	.557	13.10	14.15		
E2	.145	.201	3.68	5.10		
E3	.039	.075	1.00	1.90		
E4	.487	.529	12.38	13.43		
е	.214	BSC	5.44 BSC			
L	.780	.800	19.81	20.32		
L1	.161	.173	4.10	4.40		
N		:	3			
ØP	.138	.144	3.51	3.65		
Q	.216	.236	5.49	6.00		
S	.238	.248	6.04	6.30		
Т	17.5° REF					
W	3.5° REF					
Х	4° REF					

## **Recommended Solder Pad Layout**





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