

Features

- · 3rd generation SiC MOSFET technology
- · Optimized package with separate driver source pin
- High blocking voltage with low on-resistance
- · High-speed switching with low capacitances
- · Fast intrinsic diode with low reverse recovery (Qrr)
- · Halogen free, RoHS compliant

Benefts

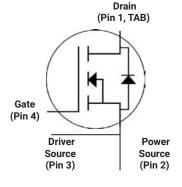
- · Reduce switching losses and minimize gate ringing
- · Higher system effciency
- · Reduce cooling requirements
- · Increase power density
- · Increase system switching frequency

Applications

- · Renewable energy
- · EV battery chargers
- High voltage DC/DC converters
- · Switch Mode Power Supplies

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Ordering Part Number	Package	Qty(PCS)	
HNTH4L045N065SC1	TO-247-4L (TO-247-4)	30	RoHS Pb

Maximum Ratings (Tc = 25 °C unless otherwise specifed)

Parameter	Symbol	Value	Unit	
Drain-source voltage	Vds	650	V	
Continuous drain current Tc = 25°C Tc = 100°C	lo	49 53	A	
Pulsed drain current (Tc = 25°C, t_p limited by T_{jmax})	ID pulse	123	А	
Avalanche energy, single pulse (L=10mH)	Eas	1000	mJ	
Gate-Source voltage	Vgs	-5/+20	V	
Gate-Source voltage (dynamic,Absolute maximum values)	VGSmax	-10/+25	V	
Power dissipation (Tc = 25°C)	Ptot	242	W	
Operating junction and storage temperature	Tj,Tstg	-55+175	°C	

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	RthJC	0.62	°C/W
Thermal resistance, junction – ambient. Max	RthJA	40	0/11



Parameter	Symbol	Value			Unit	Test Condition	
Falalletei	Symbol	min.	n. typ. max.		Unit	Test Condition	
Static Characteristic							
Drain-source breakdown voltage	BVDSS	650	-	-	V	Vgs=0V, Id=250uA	
Gate threshold voltage	VGS(th)	2	-	4	V	Vds=Vgs,Id=7mA	
Zero gate voltage drain current	IDSS	-	1 10	100	μA	Vbs=650V,Vgs=0V Tj=25°C Tj=175°C	
Gate-source leakage current	lgss	-		250	nA	Vgs=20V,Vds=0V	
		-	45	-		Vgs=18V, Id=17.6A,	
Drain-source on-state resistance	RDS(on)	-	33 50	49 -	m	VGS=20V, ID=17.6A, Tj=25°C Tj=175°C	
Transconductance	g fs	-	5.6	-	S	VDS=20V,ID=17.6A	
Dynamic Characteristic							
Input Capacitance	Ciss	-	1823	-		$V_{DS} = 650V$ $V_{GS} = 0V$ $T_J = 25^{\circ}C$ $V_{AC} = 25mV$ $f = 1MHz$	
Output Capacitance	Coss	-	190	-	pF		
Reverse Transfer Capacitance	Crss	-	19	-			
Gate Total Charge	QG	-	96	-		VDS = 400V VGS = -5/20V ID = 17.6A	
Gate-Source charge	Qgs	-	25	-	nC		
Gate-Drain charge	Q _{gd}	-	26	-			
Turn-On Switching Energy	Eon	-	188	-	1	V _{DD} = 400V V _{GS} = -5/+20V I _D = 17.6A R _G = 10	
Turn-Off Switching Energy-	Eoff	-	19		μJ		
Turn-on delay time	t _{d(on)}	-	20	-			
Rise time	tr	-	26	-	D C		
Turn-off delay time	td(off)	-	48	-	ns	L = 100uH	
Fall time	tf	-	15	-			
Gate resistance	Rg	-	1.7	-		Vac = 25mV, f=1MHz	

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)



Body Diode Characteristic

Parameter	Symbol		Value			Test Condition	
i alametei	Symbol	min.	typ.	max.	Unit	Test condition	
Body Diode Forward Voltage	Vsd		3.2		V -	Vgs=0V,Isd=8.8A, Tj=25°C	
Body Diode Polward Voltage	VSD		2.6			Vgs=0V,Isd=8.8A, Tj=175°C	
Body Diode Reverse Recovery Time	trr	-	40	-	ns	Vr = 400V, Id = 17.6A	
Body Diode Reverse Recovery Charge	Qrr	-	156	-	nC	di/dt = 1000A/µS	

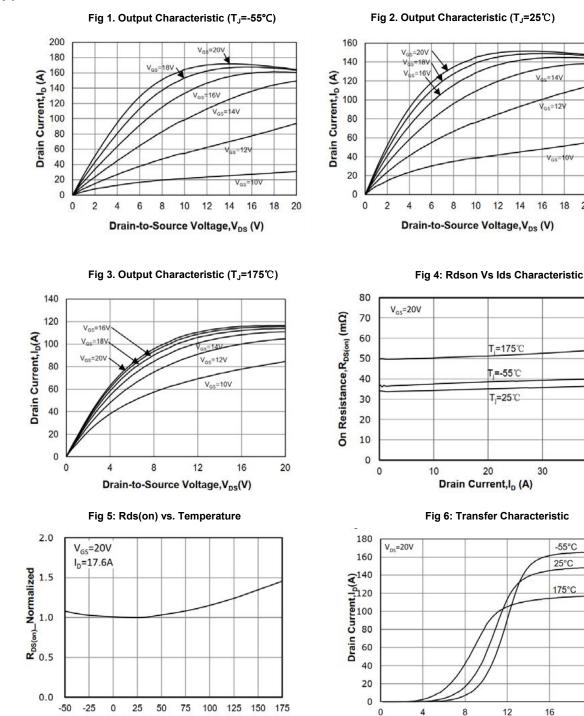


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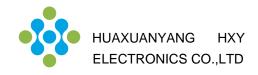
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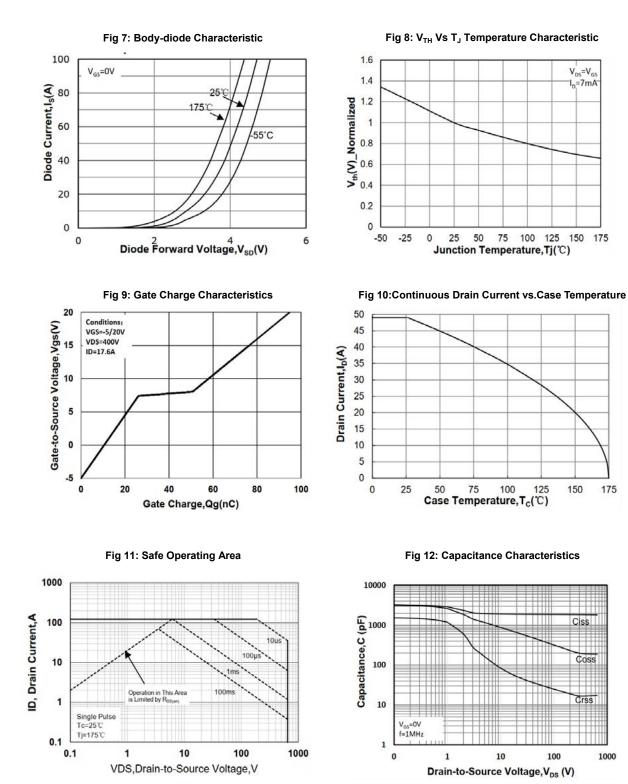
Gate-to-Source Voltage, V_{GS} (V)



Typical Performance Characteristics

Junction Temperature, Tj (°C)

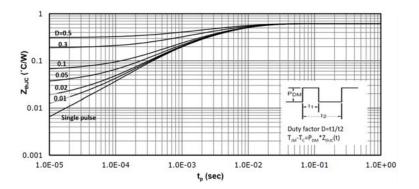




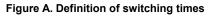


HNTH4L045N065SC1 SiC Power MOSFET N-Channel Enhancement Mode

Fig 13: Transient Thermal Impedance



Test Circuit & Waveform



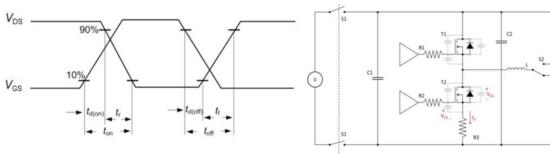
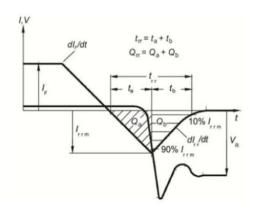


Figure B. Dynamic test circuit

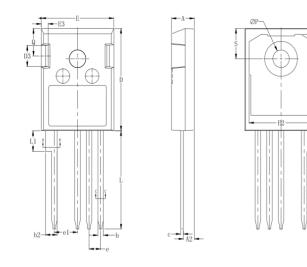
Figure C. Definition of body diodeswitching characteristics





Package Dimensions

Package TO-247-4L(TO-247-4)



Itoma	Value	s(mm)	
Items	MIN	MAX	
A	4.8	5.2	
A2	2.2	2.6	
b	1.05	1.4	
b2	2.4	2.75	
С	0.5	0.75	
D	20	21.5	
D2	15.5	17.2	
D3	4	5	
E	15.5	16.1	
E2	13	15	
E3	1	2	
е	2.54 BSC.		
e1	5.08 BSC.		
L	19	21	
L1	4	4.45	
ФР	3.5	3.7	
Q	5.4	5.9	
S	5.9 6.4		



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