■ Features

- High rated current for circuit design.
- Design by special lead wire to prevent open circuit failure.
- Low cost with rugged reliability and performance fixed inductor.
- Operating temperature:-40℃ ~ +125℃.

■ Applications

- TVs and Audio equipment.
- Notebook,Inkjet printer,Copying machine,Display monitor,Cellular phone.
- Switching Power Supply.
- Excellent as DC/DC converter boost or buck inductor.

■ Product Identification

YDPKP □□□□ – □□□ □

(1) (2) (3) (4)

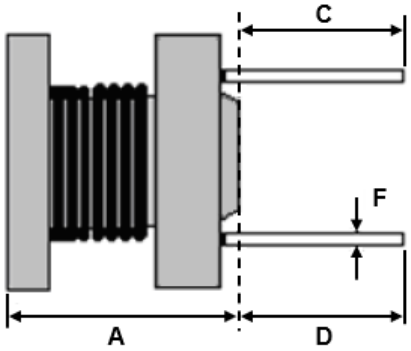
(1)： Type

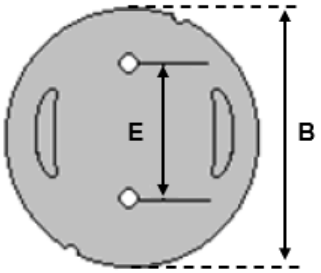
(2)： Dimensions

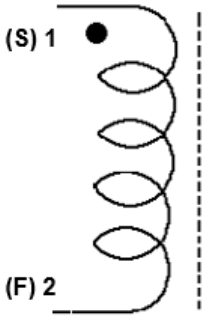
(3)： Inductance value

(4)： Inductance Tolerance： N=±30%,M=±20%， K=±10%， J=±5%

■ Shapes and Dimensions (Unit：mm)







TYPE	A Max.	B Max.	C	D	E	F
YDPKP0895	10.0	8.3	5.0±1.0	5.0±1.0	5.0±0.5	0.8±0.1

■ Electrical specification

Part Number	Inductance (uH)	Test Frequency	Max.DCR (mΩ)	Isat (A)
YDPKP0895-2R2M	2.2±20%	1KHz/0.25V	10	8.0

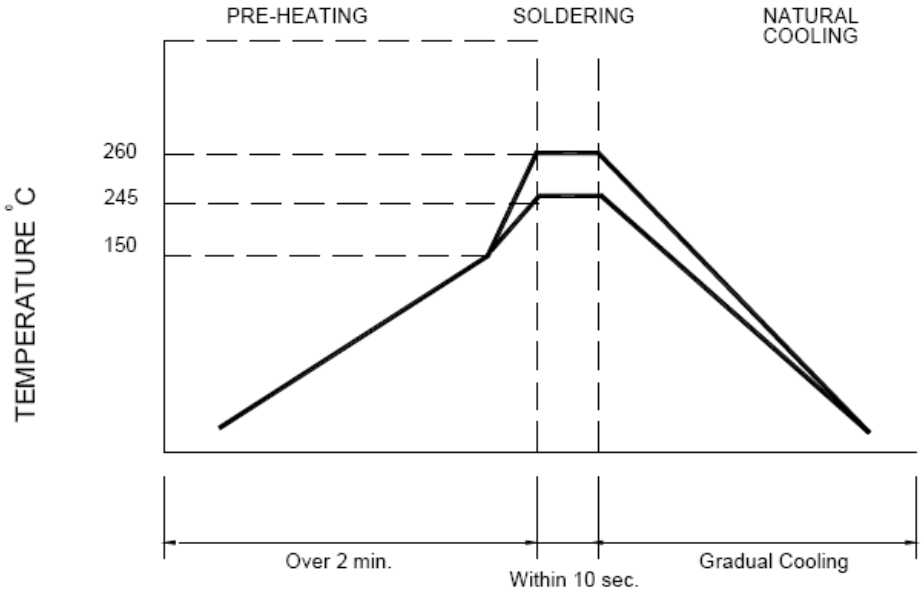
※ Design as Customer's Requested Specifications.

■ Reliability test

NO.	Items	Test Methods	Requirements
1	Lead terminalstrength	A static pulling force of 5N in a direction parallel tothe lead terminals for 60±5 seconds.	No terminal breakage orloosening.
2	Resistance tosoldering heattest	Fix the samples on a 1.6mm thickness PCB,then dip thesample leads into a soldering bath of 270±5℃ up tothe PCB for 5±1 seconds.	No significant abnormality inappearance.Deviation relative toinitial value:L:Within ±10%
3	Solder abilitytest	Immerse the terminal in flux for 5 seconds. Then dipthe terminal into a soldering bath of 245±5℃ for 2±0.5 seconds.	At least 90% of terminalelectrode is covered by newsolder.
4	Humidity test	Temperature:40℃±2℃ Humidity :90%～95%RH Duration:96±4 Hours	No significant abnormality inappearance.Deviation relative toinitial value:L:Within ±10%
5	Hightemperaturestroage test	Emperature:85℃±2℃ Duration :96±4 Hours	No significant abnormality inappearance.Deviation relative toinitial value:L:Within ±10%
6	Lowtemperaturestorage test	Temperature :-25℃±2℃ Time:96±4 Hours	No significant abnormality inappearance.Deviation relative toinitial value:L:Within ±10%
7	Thermal shocktest	First -25±5℃ for 30±3 minutes，last 85±5 ℃ 30±3minutes as 1 cycles. Go through 10 cycles.	No significant abnormality inappearance.Deviation relative toinitial value:L:Within ±10%

■ Soldering Conditions

Wave Soldering:



Note:

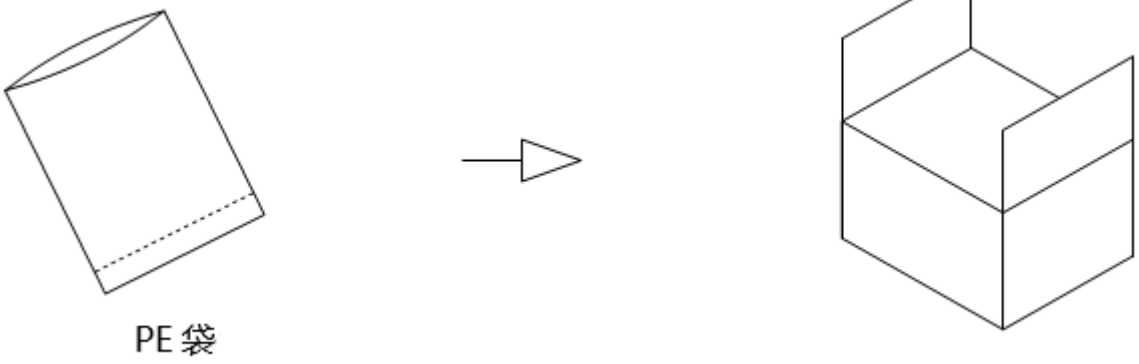
Never contact the ceramic with the iron tip

1.0mm tip diameter(max)

■ Material list

NO	ITEM	DESCRIPTION	SUPPLIER	RATING	UL FILE
1	Core	DR2W 7.8×9.5	HUICI		
		OR EQUIVALENT			
2	Wire	QA-1 Φ0.70mm N	JINYAN	155℃	E238500
		OR EQUIVALENT			
3	PIN	TCW Φ0.80mm	MINGTONG		
REMARK:					

■ Package specification



Type	Quantity(pcs)			Remark
	Bag	Inside box	Outer box	
YDPKP0895	250	2500	5000	