



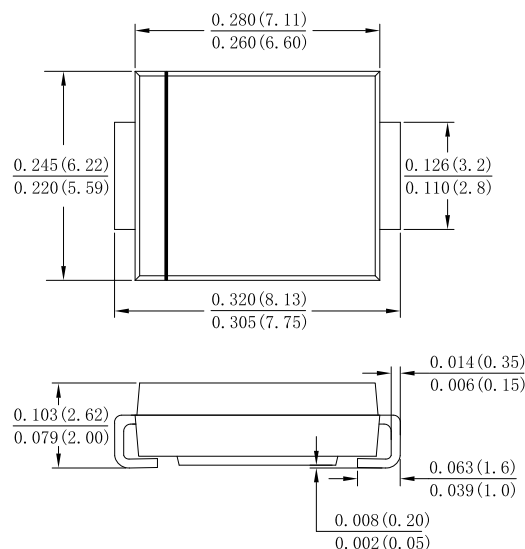
Features

- Glass Passivated Die Construction
- Low forward voltage drop
- High current capability
- High reliability
- Metal silicon junction,majority carrier conduction
- Plastic Case Material has UL Flammability
- Classification Rating 94V-0

Mechanical Data

- Case: Molded plastic SMC
- Terminals: Plated leads solderable per MIL-STD-750,Method 2026 guaranteed
- Polarity: as marked on case
- Mounting Position: Any
- Making: Type Number

Case: SMC(DO-214AB)



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25 °C ambient temperature unless otherwise specified

Single phase,half wave,60Hz,resistive or inductive load

For capacitive load derate current by 20%

| Parameter | Symbol | MURS460U | Unit |
|--|-----------------|-------------|----------------------|
| Peak Repetitive Reverse Voltage | V_{RRM} | 600 | V |
| Working Peak Reverse Voltage | V_{RWM} | | |
| DC blocking voltage | V_{DC} | | |
| RMS Rectified Voltage | $V_{R(RMS)}$ | 420 | V |
| Average Rectified Output Current | $I_F(AV)$ | 4.0 | A |
| Non-Repetitive Peak Forward Surge @ $T_j=25\text{ }^{\circ}\text{C}$ Current 8.3ms Single half sine-wave@ $T_j=125\text{ }^{\circ}\text{C}$ Superimposed On Rated Load (JEDEC Method) | I_{FSM} | 200 160 | A |
| Non-Repetitive Peak Forward Surge @ $T_j=25\text{ }^{\circ}\text{C}$ Current 1.0ms Single half sine-wave @ $T_j=125\text{ }^{\circ}\text{C}$ Superimposed On Rated Load (JEDEC Method) | I_{FSM} | 400 320 | A |
| 10000 times of the wave surge current (time width 1ms, time interval 3s) | I_{FSM} | 150 | A |
| I^2t Rating for Fusing (t < 8.3ms) | I^2t | 166 | A ² s |
| Forward Voltage Drop $T_A=25\text{ }^{\circ}\text{C}$ @ $I_F=4\text{A}$ | V_{FM} | 1.3 | V |
| Peak Reverse Current $T_j=25\text{ }^{\circ}\text{C}$ At Rated DC Blocking Voltage $T_j=125\text{ }^{\circ}\text{C}$ | I_R | 5 100 | μA |
| Typical Junction Capacitance (Note 1) | C_J | 50 | pF |
| Typical Thermal Resistance Junctionto Ambient | $R_{\theta JA}$ | 41 | $^{\circ}\text{C/W}$ |
| Maximum Reverse Recovery Time(Note 3) | T_{rr} | 50 | ns |
| Operating and Storage Temperature Range | T_J, T_{STG} | -55 to +150 | $^{\circ}\text{C}$ |

Note: 1.Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C

2.Reverse Recovery Test Conditions: $I_F=0.5\text{A}$, $I_R=1\text{A}$, $I_{rr}=0.25\text{A}$



Fig. 1 Forward Current Derating Curve

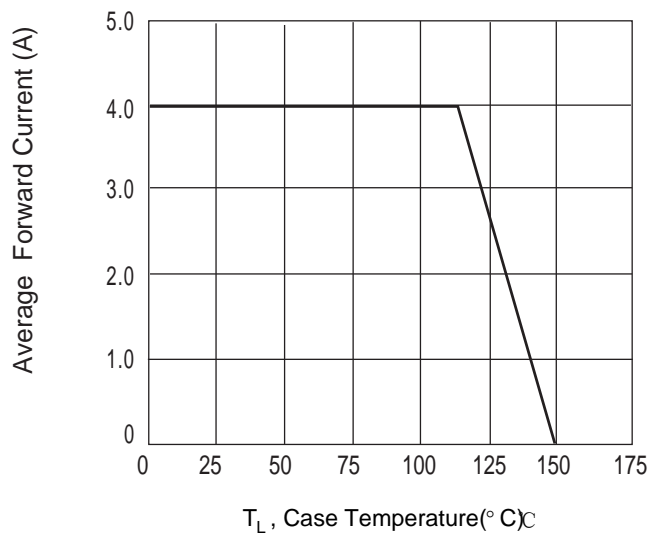


Fig. 2 Typ. Forward Characteristics

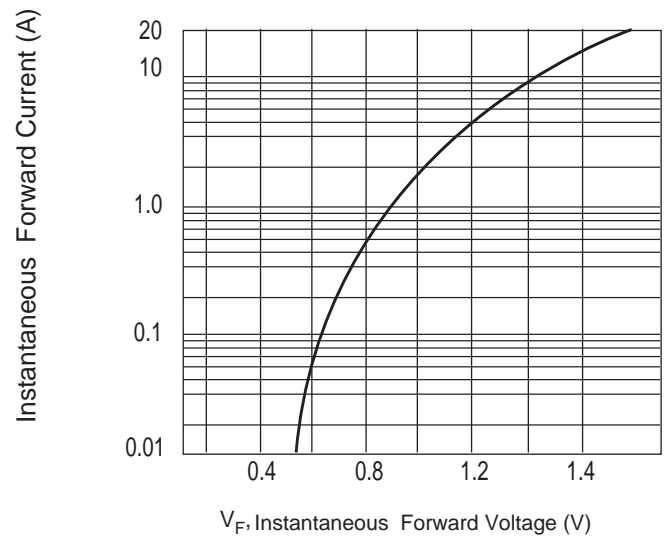


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

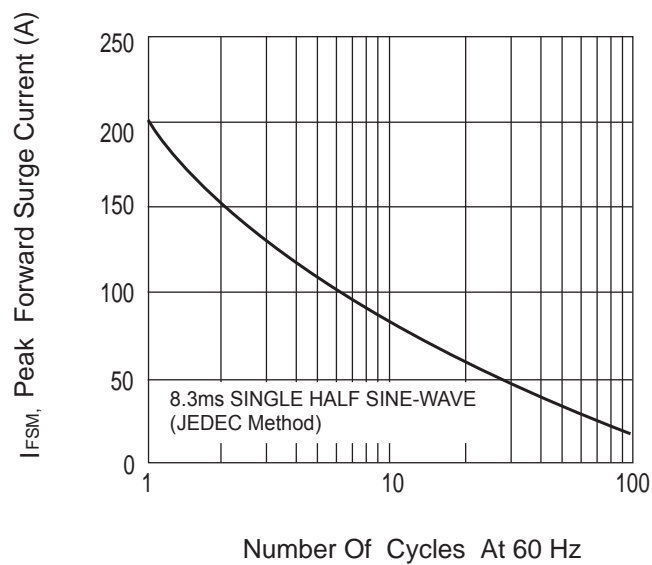


Fig.4 Typical Reverse Characteristics

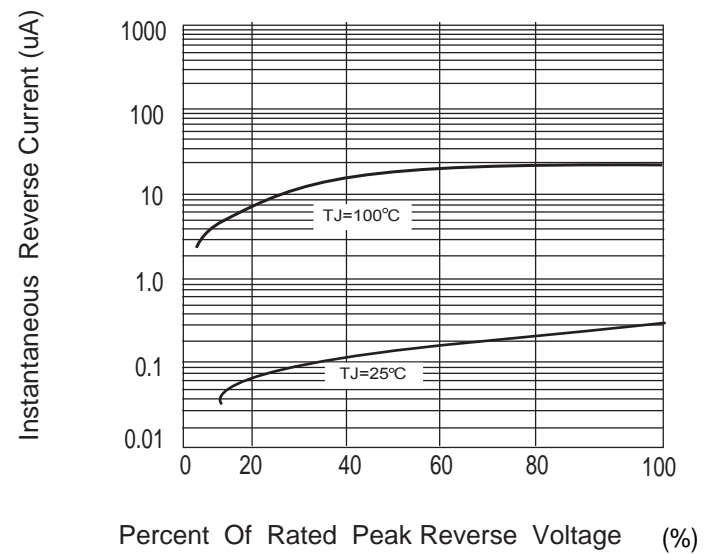
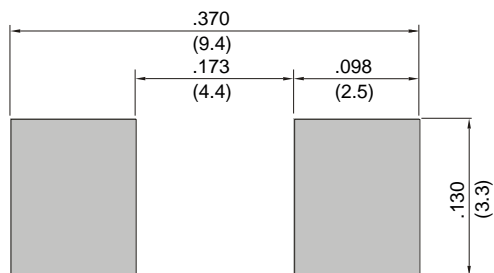


Fig.5 Mounting PAD Layout





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