

## General Description

The WSD2068DN23 is the highest performance trench N-Channel MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WSD2068DN23 meet the RoHS and Green Product requirement with full function reliability approved.

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

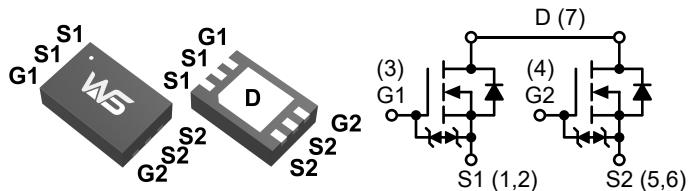
## Product Summary

<b>BV<sub>DSS</sub></b>	<b>R<sub>DSON</sub></b>	<b>I<sub>D</sub></b>
20V	15.5mΩ	7.5A

## Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.
- DC-DC Power System
- ESD:2KV

## DFN2X3-6S Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	20	V
V <sub>GS</sub>	Gate-Source Voltage	±12	V
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 4.5V <sup>1</sup>	7.5	A
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 4.5V <sup>1</sup>	6.5	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	30	A
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>3</sup>	1.5	W
P <sub>D</sub> @T <sub>A</sub> =70°C	Total Power Dissipation <sup>3</sup>	1.0	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup> (Steady State)	---	120	°C/W
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup> (t<10S)	---	83	°C/W

## Electrical Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C, $I_D=1mA$	---	0.022	---	V/°C
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=4.5V, I_D=5.5A$	---	12	15.5	mΩ
		$V_{GS}=2.5V, I_D=5.5A$	---	16	20	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	0.3	0.7	1.0	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-2.32	---	mV/°C
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=16V, V_{GS}=0V, T_J=25°C$	---	---	1	μA
		$V_{DS}=16V, V_{GS}=0V, T_J=55°C$	---	---	5	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	---	---	$\pm 10$	μA
$g_{fs}$	Forward Transconductance	$V_{DS}=5V, I_D=10A$	---	20	---	S
$R_g$	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	11	---	Ω
$Q_g$	Total Gate Charge (4.5V)	$V_{DS}=10V, V_{GS}=4.5V, I_D=5A$	---	15	20	nC
$Q_{gs}$	Gate-Source Charge		---	2.2	---	
$Q_{gd}$	Gate-Drain Charge		---	4.2	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=10V, V_{GS}=10V, R_G=6Ω, I_D=5A, RL=2Ω$	---	148	---	ns
$T_r$	Rise Time		---	277	---	
$T_{d(off)}$	Turn-Off Delay Time		---	1616	---	
$T_f$	Fall Time		---	751	---	
$C_{iss}$	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	---	1219	1350	pF
$C_{oss}$	Output Capacitance		---	150	---	
$C_{rss}$	Reverse Transfer Capacitance		---	123	---	

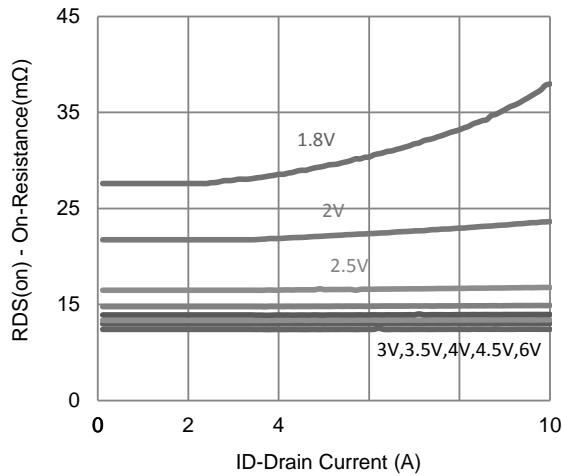
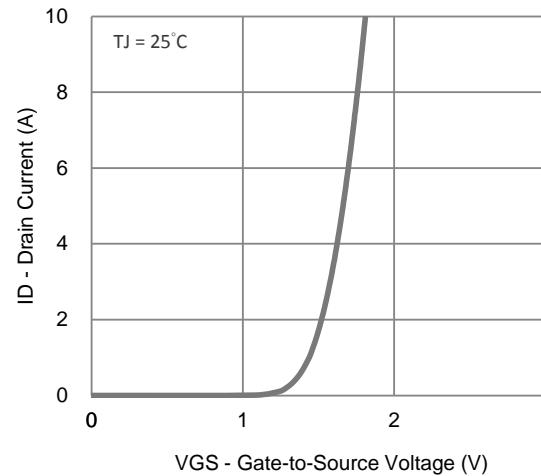
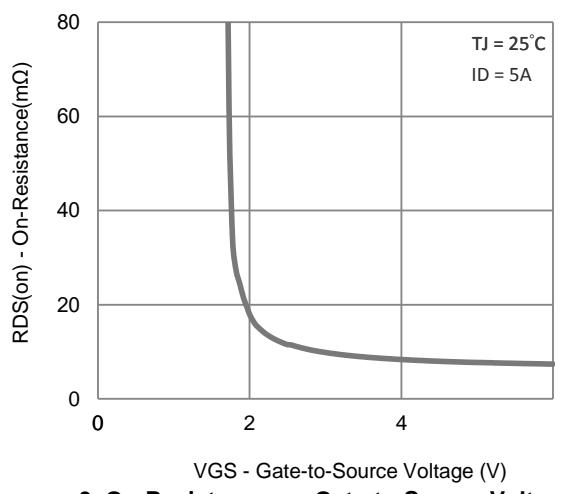
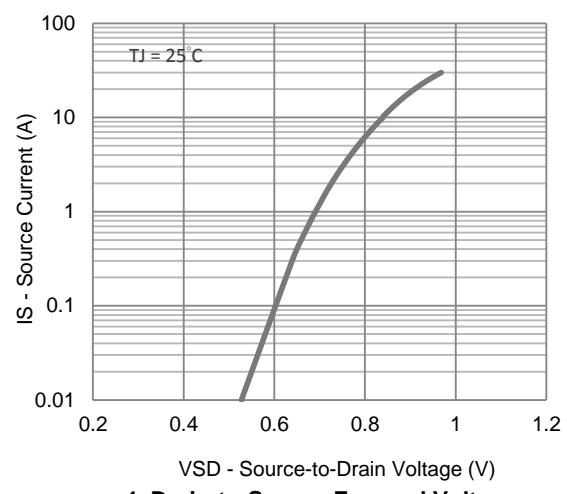
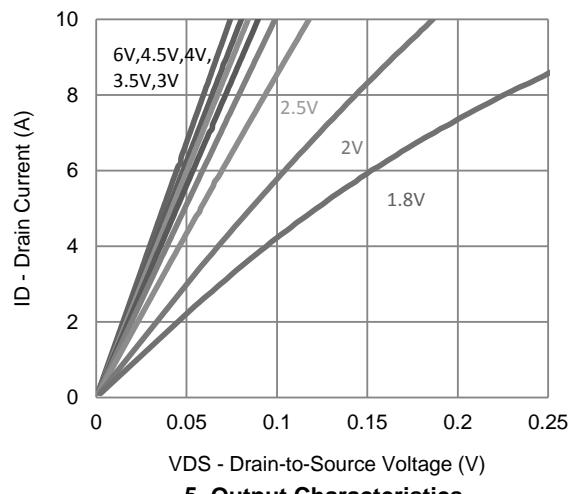
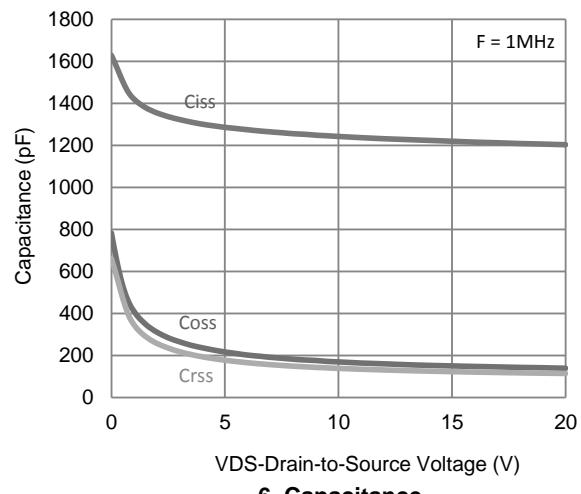
## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current <sup>1,4</sup>	$V_G=V_D=0V$ , Force Current	---	---	5	A
$I_{SM}$	Pulsed Source Current <sup>2,4</sup>		---	---	15	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V, I_s=1A, T_J=25°C$	---	0.76	1.3	V
$t_{rr}$	Reverse Recovery Time	$I_F=5A, dI/dt=100A/μs, T_J=25°C$	---	245	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	1105	---	nC

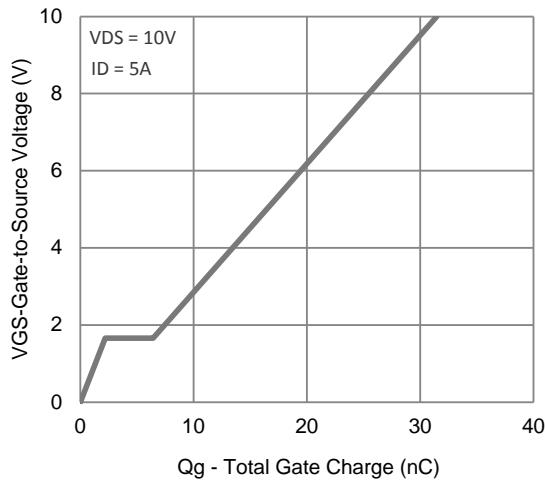
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, t<10sec.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

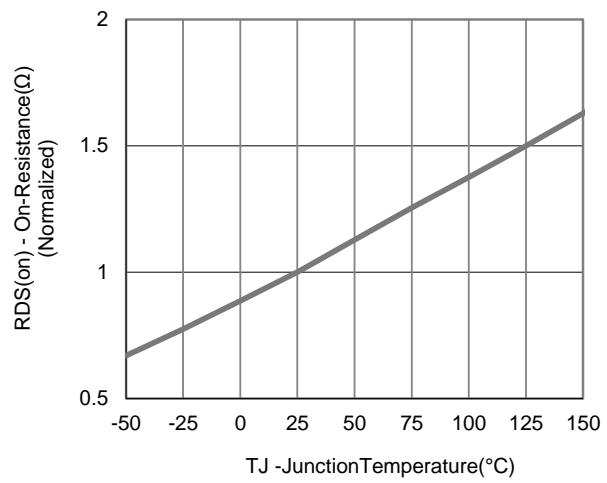
## Typical Characteristics


**1. On-Resistance vs. Drain Current**

**2. Transfer Characteristics**

**3. On-Resistance vs. Gate-to-Source Voltage**

**4. Drain-to-Source Forward Voltage**

**5. Output Characteristics**

**6. Capacitance**

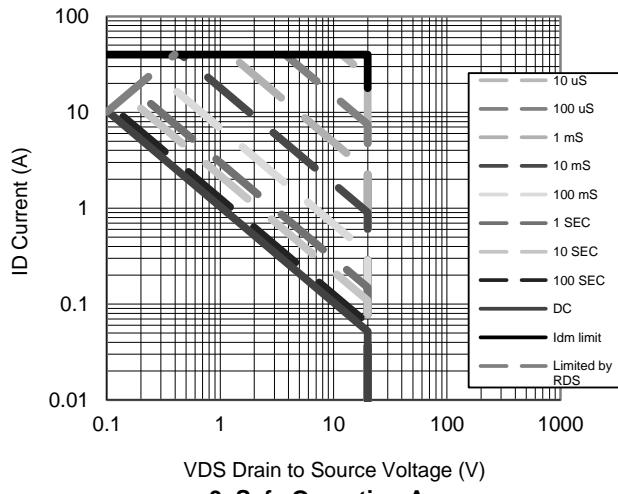
## Typical Characteristics (Cont.)



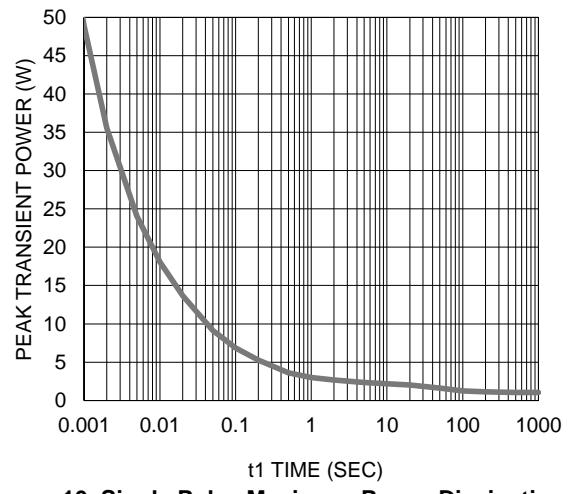
## 7. Gate Charge



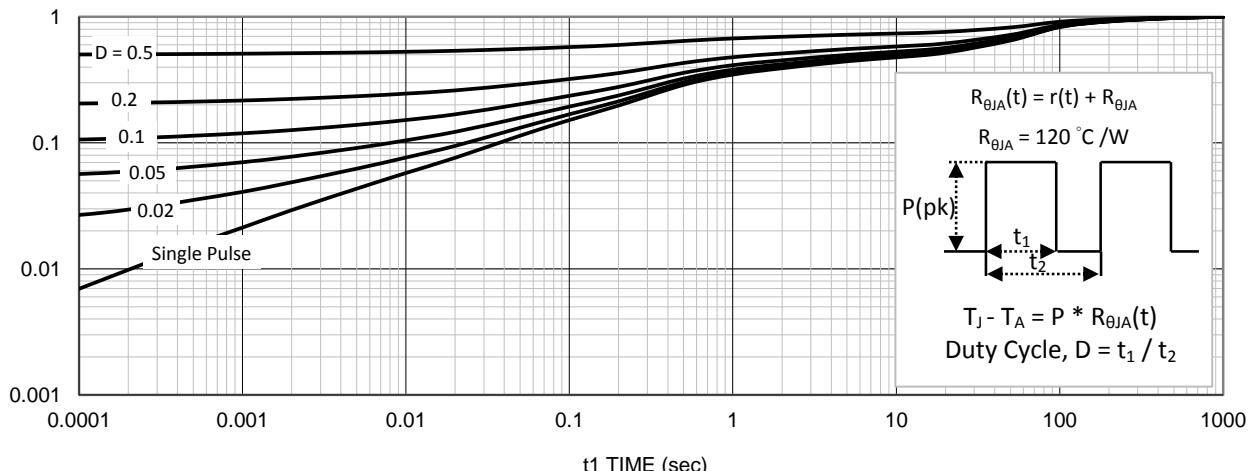
## 8. Normalized On-Resistance Vs Junction Temperature



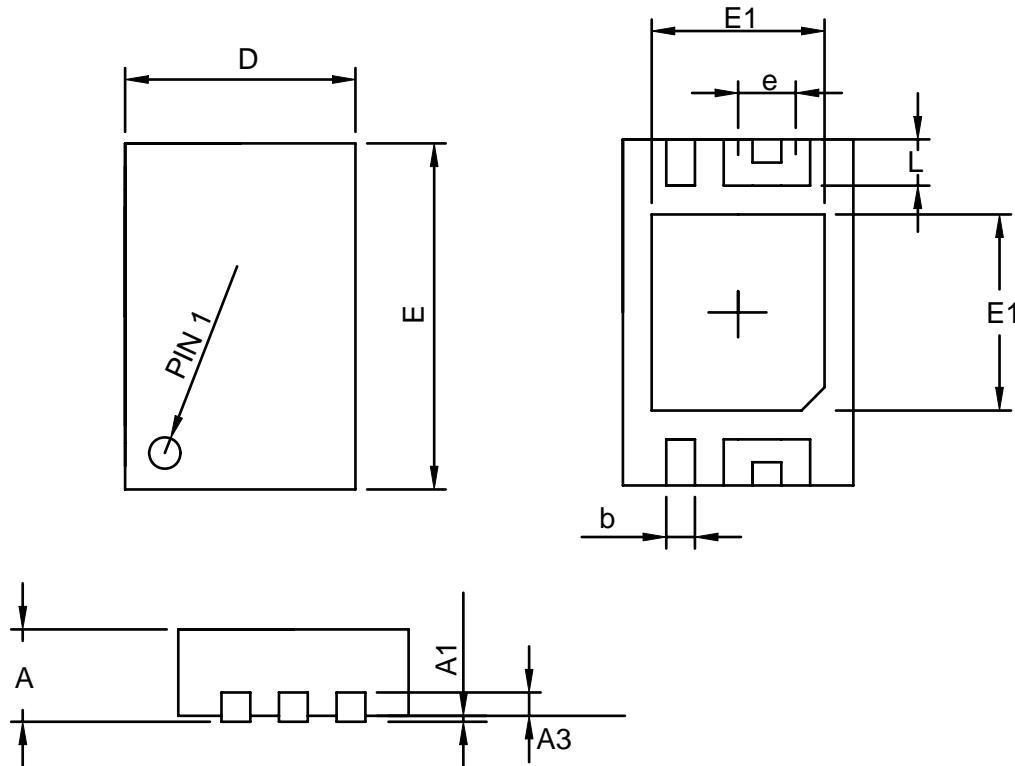
## 9. Safe Operating Area



## 10. Single Pulse Maximum Power Dissipation



## 11. Normalized Thermal Transient Junction to Ambient

**Packaging information**


SYMBOL	DFN2X3-6S			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.70	1.00	0.028	0.039
A1	0.00	0.05	0.000	0.002
A3	0.203 REF		0.008REF	
b	0.20	0.30	0.008	0.012
D	1.90	2.10	0.075	0.083
E1	1.60	1.80	0.063	0.071
E	2.90	3.10	0.114	0.122
D1	1.40	1.60	0.055	0.063
e	0.50 BSC		0.02 BSC	
L	0.30	0.50	0.012	0.20



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