

### General Description

The 12P10S uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications.

### Product Summary

BVDSS	RDS(ON)	ID
-100V	0.24Ω	-9A

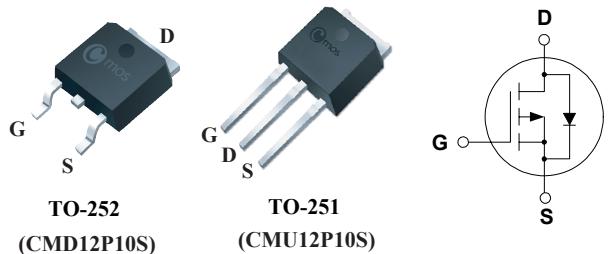
### Applications

- Power Switch
- DC / DC converter

### Features

- P-Channel
- Low ON-resistance.
- 100% avalanche tested
- RoHS Compliant

### TO-252/251 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-100	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current	-9	A
I <sub>D</sub> @T <sub>C</sub> =100 °C	Continuous Drain Current	-6.5	A
I <sub>DM</sub>	Pulsed Drain Current	-36	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	67	mJ
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation	40	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient	---	50	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-case	---	3.1	°C/W

Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-100	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-6\text{A}$	---	170	240	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-5\text{A}$	---	180	250	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=-250\mu\text{A}$	-1	---	-3	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-100\text{V}, V_{\text{GS}}=0\text{V}$	---	---	-1	$\text{uA}$
		$V_{\text{DS}}=-80\text{V}, T_c=125^\circ\text{C}$	---	---	-10	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-10\text{A}$	---	20	---	S
$Q_g$	Total Gate Charge	$I_{\text{D}}=-8\text{A}$	---	18	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=-80\text{V}$	---	5	---	
$Q_{\text{gd}}$	Gate-Drain Charge	$V_{\text{GS}}=-4.5\text{V}$	---	9	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DS}}=-50\text{V}$	---	13	---	$\text{ns}$
$T_r$	Rise Time	$I_{\text{D}}=-9\text{A}$	---	140	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$R_G=25\Omega$	---	30	---	
$T_f$	Fall Time		---	50	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1500	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	50	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	40	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-9	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	-36	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_F=-12\text{A}$	---	-0.94	-1.2	V

Note :

1.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=-50\text{V}, V_{\text{GS}}=-10\text{V}, L=1\text{mH}, I_{\text{AS}}=-11.6\text{A}$ .

This product has been designed and qualified for the consumer market.

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Cmos reserves the right to improve product design ,functions and reliability without notice.

### Typical Characteristics

