

General Description

These N-Channel enhancement mode power field effect transistors are produced using advanced technology which has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.

Features

- Originative New Design
- 100% avalanche tested
- Very Low Intrinsic Capacitances
- Fast switching
- Improved dv/dt capability

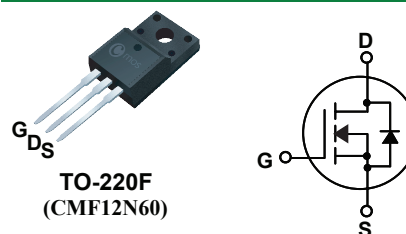
Product Summary

BVDSS	RDSON	ID
600V	0.66Ω	12A

Applications

- Charger
- Adaptor
- Power Supply
- Electrodeless lamp

TO-220F Pin Configuration



Absolute Maximum Ratings

$T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-Source Voltage	600	V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$) - Continuous ($T_C = 100^\circ\text{C}$)	12 *	A
		7.5 *	A
I_{DM}	Drain Current - Pulsed	48 *	A
V_{GSS}	Gate-Source Voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy ¹	980	mJ
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	51	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

* Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	Rating	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case Max.	2.45	$^\circ\text{C/W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	---	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient Max.	62.5	$^\circ\text{C/W}$

Electrical Characteristic

 $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off Characteristics

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	600	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 600\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
		$V_{DS} = 480\text{ V}, T_C = 125^\circ\text{C}$	--	--	10	
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 6.0\text{ A}$	--	0.57	0.66	Ω
gfs	Forward Transconductance	$V_{DS} = 20\text{ V}, I_D = 6.0\text{ A}$	--	16	--	S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	2700	--	pF
C_{oss}	Output Capacitance		--	160	--	pF
C_{rss}	Reverse Transfer Capacitance		--	120	--	pF

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 300\text{ V}, I_D = 12\text{ A}$ $R_G = 25\text{ }\Omega$	--	30	--	ns
t_r	Turn-On Rise Time		--	86	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	140	--	ns
t_f	Turn-Off Fall Time		--	95	--	ns
Q_g	Total Gate Charge	$V_{DS} = 480\text{ V}, I_D = 12\text{ A}$ $V_{GS} = 10\text{ V}$	--	40	--	nC
Q_{gs}	Gate-Source Charge		--	8.3	--	nC
Q_{gd}	Gate-Drain Charge		--	13.8	--	nC

Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	12	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	48	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 12A	--	--	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 12A	--	420	--	ns
Q _{rr}	Reverse Recovery Charge	di _F / dt = 100 A/μs	--	4.9	--	μC

Note :

1. The EAS data shows Max. rating . The test condition is $V_{DD}=80\text{V}, V_{GS}=10\text{V}, L=10\text{mH}, I_{AS}=14\text{A}$.

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