

# 40V, 5.3mΩ typ., 40A N-Channel MOSFET

### **General Description**

Advanced Power MOSFETs from
Cmos provide the designer with the
best combination of fast switching
and low on-resistance. This device
is well suited for Power Management
and load switching applications
common in Notebook Computers and
Portable Battery Packs.

### **Features**

- Low On-Resistance
- 100% avalanche tested
- Surface Mount Package
- RoHS Compliant

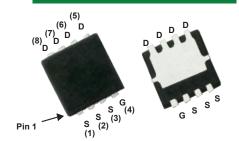
### **Product Summary**

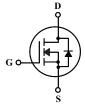
BVDSS	RDS(on) max.	ID		
40V	6.5mΩ	40A		

## **Applications**

- High side in DC DC Buck Converters
- Notebook battery power management
- Load switch in Notebook

#### **DFN-8 3.3x3.3 Pin Configuration**





DFN-8 3.3x3.3

Type	Package	Marking		
CMSC7240B	DFN-8 3.3x3.3	7240B		

### **Absolute Maximum Ratings**

Symbol	Symbol Parameter Rating		Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current	40	А
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current	28	Α
I <sub>DM</sub>	Pulsed Drain Current	120	Α
EAS	Single Pulse Avalanche Energy <sup>1</sup>	189	mJ
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation	38	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

#### **Thermal Data**

Symbol	Symbol Parameter		Max.	Unit
$R_{ heta JA}$	R <sub>θJA</sub> Thermal Resistance Junction-ambient		40	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction -Case		3.29	℃/W



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# Electrical Characteristics (T<sub>J</sub>=25℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	40			V
D	Static Drain-Source On-Resistance	$V_{GS}$ =10V , $I_D$ =20A		5.3	6.5	mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		11	14	
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu A$	1		3	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =32V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =10A		10		S
Qg	Total Gate Charge	V <sub>DS</sub> =20V , I <sub>D</sub> =20A V <sub>GS</sub> =10 V		32		
$Q_gs$	Gate-Source Charge			5		nC
$Q_gd$	Gate-Drain Charge			6		
T <sub>d(on)</sub>	Turn-On Delay Time			12		
Tr	Rise Time	$V_{DS}$ =20V , $V_{GS}$ =10V , $R_{GEN}$ =3 $\Omega$ $R_L$ =1 $\Omega$		12		, no
T <sub>d(off)</sub>	Turn-Off Delay Time			40		ns
T <sub>f</sub>	Fall Time			10		
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> =0V , f=1MHz		3000		
Coss	Output Capacitance			200		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			185		

### **Diode Characteristics**

	Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
	Is	Diode continuous forward current	Vg=Vp=0V , Force Current			40	Α
Г	I <sub>S,pulse</sub>	Diode pulse current				120	Α
	$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>F</sub> =20A , Tj=25℃			1.2	V

#### Notes:

This product has been designed and qualified for the counsumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserver the right to improve product design ,functions and reliability wihtout notice.

 $<sup>1.</sup> The \ EAS \ data \ shows \ Max. \ rating \ . \ The \ test \ condition \ is \ VDD=-30V, VGS=-10V, L=0.5mH, IAS=-27.5A$