

Voltage Controlled Oscillator 11.2 – 12.6 GHz

Rev. V4

Features

- Low Phase Noise
- Wide Tuning Range
- Divide-by-Two Output
- Integrated Buffer Amplifier
- Excellent Temperature Stability
- +5V Bias
- Lead-Free 5 mm 32-Lead PQFN Package
- Halogen-Free “Green” Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description

The MAOC-009267 is an InGaP HBT-based voltage controlled oscillator for frequency generation. No external matching components are required. This VCO is easily integrated into a phase lock loop using the divide-by-two output. The extremely low phase noise makes this part ideal for many radio applications including high capacity digital radios.

The MAOC-009267 primary applications are Point-to-Point Radio, Point-to-Multipoint Radio, Communications Systems, and Low Phase Noise applications.

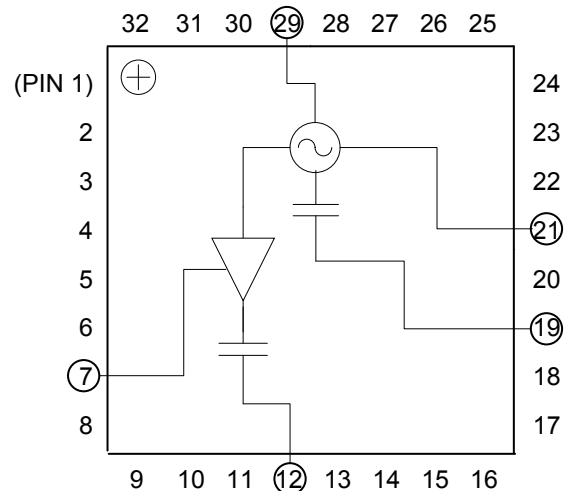
The 5 mm PQFN package has a lead-free finish that is RoHS compliant and compatible with a 260°C reflow temperature. The package also features low lead inductance and an excellent thermal path.

Ordering Information¹

Part Number	Package
MAOC-009267-TR0500	500 piece reel
MAOC-009267-TR1000	1000 piece reel
MAOC-009267-SMB003	Sample Board

1. Reference Application Note M513 for reel size information.

Block Diagram



Pin Designations²

Pin	Function	Pin	Function
1	N/C	17	N/C
2	N/C	18	N/C
3	N/C	19	RF
4	N/C	20	N/C
5	N/C	21	V _{CC}
6	N/C	22	N/C
7	V _{BUFFER}	23	N/C
8	N/C	24	N/C
9	N/C	25	N/C
10	N/C	26	N/C
11	N/C	27	N/C
12	RF/2	28	N/C
13	N/C	29	V _{TUNE}
14	N/C	30	N/C
15	N/C	31	N/C
16	N/C	32	N/C

2. The exposed pad centered on the package bottom must be connected to RF and DC ground. Connecting all N/C pins to RF/DC Ground in the layout is also recommended.

* Restrictions on Hazardous Substances. European Union Directive 2002/95/EC.

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Electrical Specifications: $T_A = +25^\circ\text{C}$, $V_{CC} = V_{BUFFER} = 5.0\text{ V}^3$, $Z_0 = 50\ \Omega$

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Output Power	RF Port, 11.2 - 12.6 GHz RF/2 Port, 5.6 - 6.3 GHz	dBm	0 -2	4 2	—
SSB Phase Noise $V_{CC}=V_{BUFFER}=V_{TUNE}=5\text{V}$	RF Port, 10KHZ Offset RF Port, 100KHZ Offset	dBc/Hz	—	-82 -112	—
Harmonics/Subharmonics $V_{CC}=V_{BUFFER}=V_{TUNE}=5\text{V}$	RF Port, $\frac{1}{2} F_0$ RF Port, $2 F_0$	dBc	—	-18 -26	—
Pulling (Sensitivity to Match) $V_{CC}=V_{BUFFER}=V_{TUNE}=5\text{V}$	RF Port, VSWR = 1.95:1 to 2.25:1	MHz pk-pk	—	7	—
Pushing (Sensitivity to Supply Voltage)	RF Port, $V_{TUNE} = 5\text{ V}$ RF/2 Port, $V_{TUNE} = 5\text{ V}$	MHz/V	—	12 6	—
Frequency Drift Rate (Sensitivity to Temperature)	RF Port, 11.2 - 12.6 GHz RF/2 Port, 5.6 - 6.3 GHz	MHz/ $^\circ\text{C}$	—	1 .5	—
Output Return Loss	RF Port, 11.2 - 12.6 GHz RF/2 Port, 5.6 - 6.3 GHz	dB	—	3 6	—
Tuning Sensitivity @ RF Port	$V_{TUNE} = 5\text{ V}$	GHz/V	—	0.21	—
Supply Current	$I_{TOTAL} (I_{CC} + I_{BUFFER})$ I_{CC} I_{BUFFER}	mA	—	175 155 20	205 175 30
Tune Voltage	V_{TUNE}	V	1	—	13
Tuning Current Leakage	$V_{TUNE} = 13\text{ V}$	μA	—	5	10

3. VCO can operate over the 4.75 V to 5.25 V supply voltage range.

Absolute Maximum Ratings ^{4,5,6}

Parameter	Absolute Maximum
Supply Voltage (V_{CC} & V_{BUFFER})	+5.5 Vdc
V_{TUNE}	0 to +15 Vdc
Storage Temperature	-55 $^\circ\text{C}$ to +150 $^\circ\text{C}$
Operating Temperature	-40 $^\circ\text{C}$ to +85 $^\circ\text{C}$
Case Temperature (T_C) (measured @ exposed pad)	+100 $^\circ\text{C}$
Junction Temperature ⁷	+135 $^\circ\text{C}$

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- Operating at nominal conditions with $T_J \leq +135^\circ\text{C}$ will ensure MTBF > 2.5×10^6 hours.
- Junction Temperature (T_J) = $T_C + \Theta_{jc} * (V * I)$
Typical thermal resistance (Θ_{jc}) = 35 $^\circ\text{C/W}$.
 - For $T_C = 25^\circ\text{C}$, $T_J = 56^\circ\text{C}$ @ 5 V, 175 mA
 - For $T_C = 85^\circ\text{C}$, $T_J = 117^\circ\text{C}$ @ 5 V, 180 mA

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.



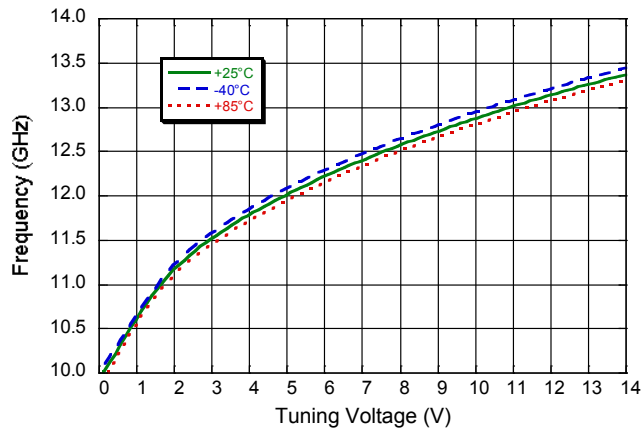
ESD Rating: Class 1A

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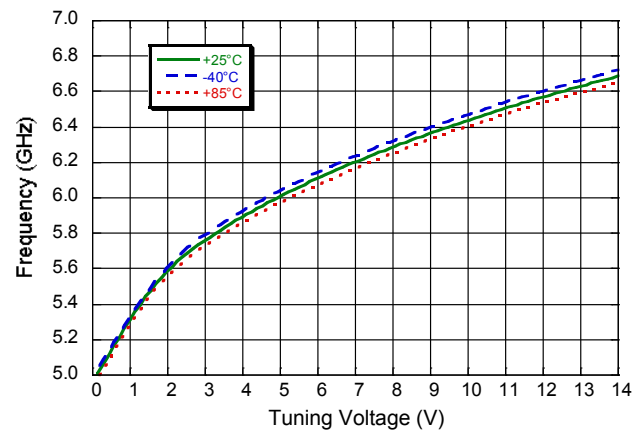
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Typical Performance Curves: $V_{CC} = V_{BUFFER} = 5V$, $T_A = +25^\circ C$ (unless otherwise indicated)

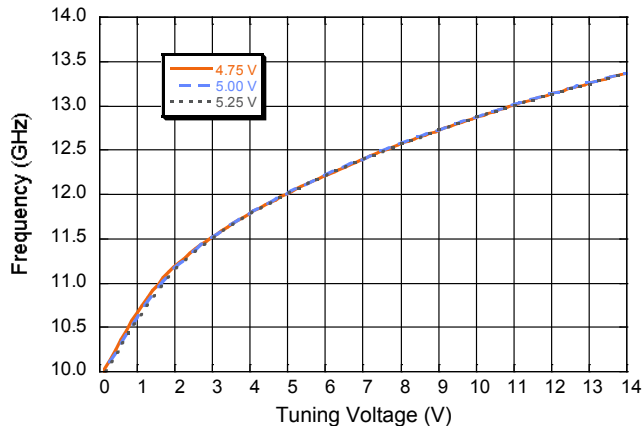
Output Frequency vs. Tuning Voltage - RF Port



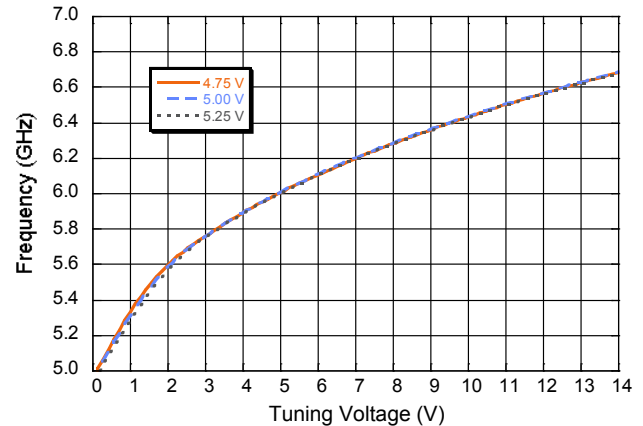
Output Frequency vs. Tuning Voltage - RF/2 Port



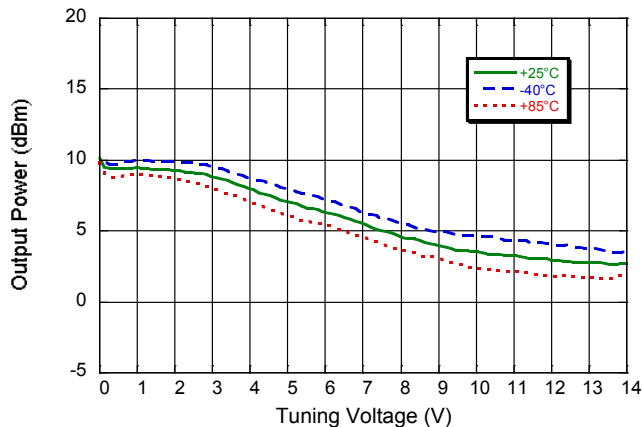
Output Frequency vs. Tuning / Supply Voltage - RF Port



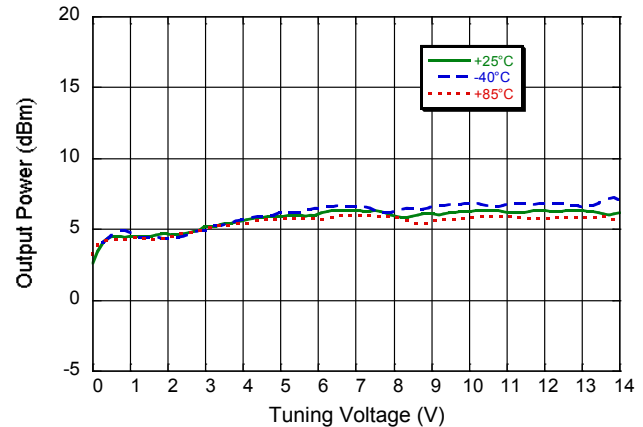
Output Frequency vs. Tuning / Supply Voltage - RF/2 Port



Output Power vs. Tuning Voltage - RF Port



Output Power vs. Tuning Voltage - RF/2 Port

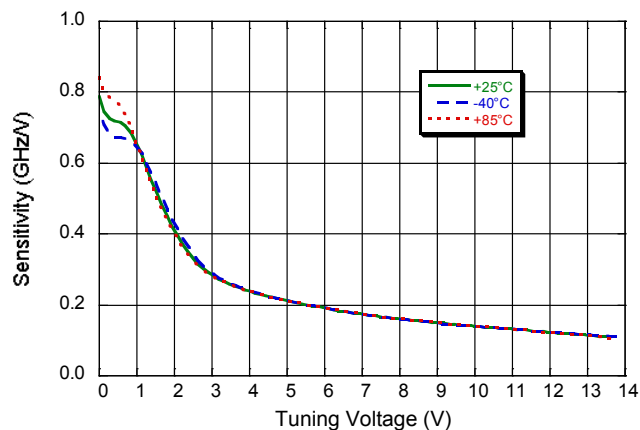


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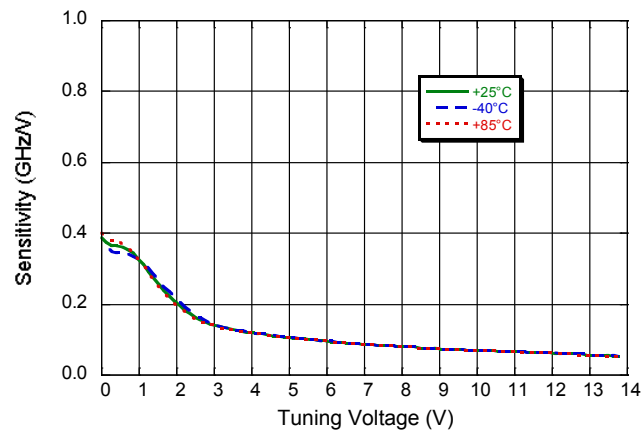
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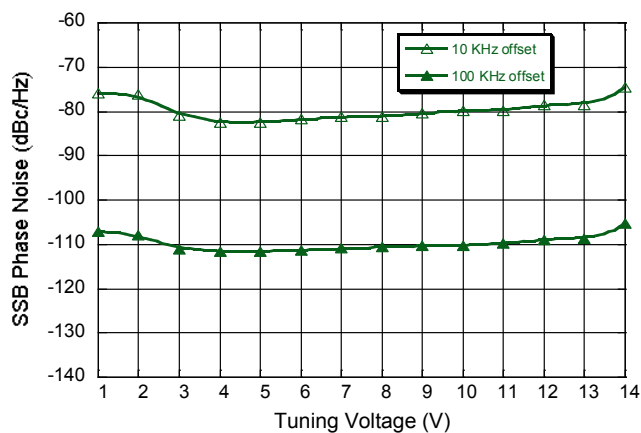
Frequency Sensitivity vs. Tuning Voltage - RF Port



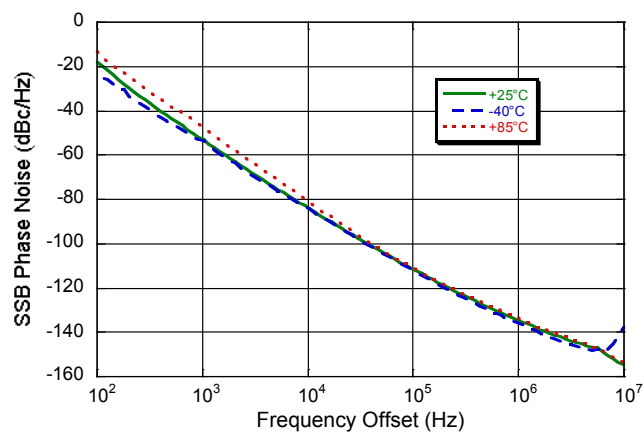
Frequency Sensitivity vs. Tuning Voltage - RF/2 Port



Single Side Band Phase Noise vs. Tuning Voltage
RF Port

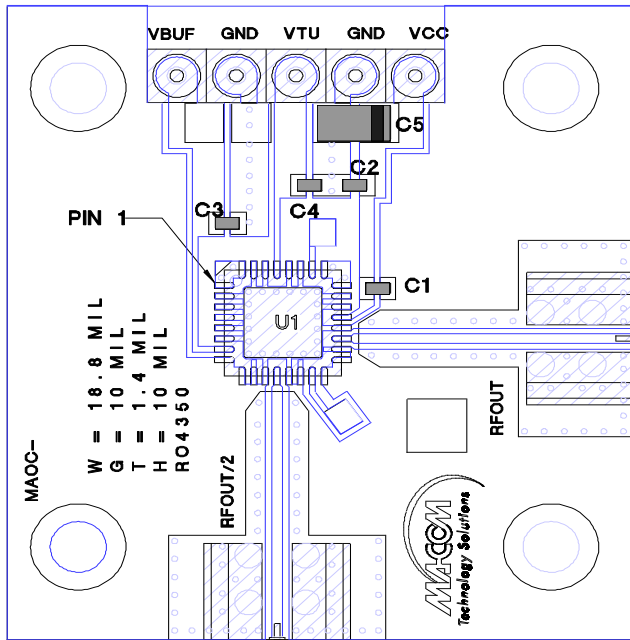


Single Side Band Phase Noise vs. Frequency Offset
RF Port ($V_{TUNE} = 5V$)



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Parts List



Component	Value	Case Size
C1	100 pF	0402
C2, C3, C4	0.1 μ F	0402
C5	10 μ F Tantalum	1206

Figure 1: Dimensions of the JEDEC MO-220-VHDD-5 package. The figure includes two views: a top view and a side view. The top view shows a square package with a central square area containing the text 'YYWW', 'OC9267', 'LLLLLL', and 'MTSR'. Dimensions include a 32x32mm footprint, a .0197 inch (0.5mm) central square, and a .1378 inch (3.5mm) square area. The side view shows a package height of .0354 inches (0.90mm) and a .0020 inch (0.05mm) thickness. It also shows a .0079 inch (0.20mm) gap between the package and the PCB, and a .0079 inch (0.20mm) gap between the package and the PCB. The package is labeled with 'PIN#1 IDENTIFIER', 'DATE CODE', 'LOT NUMBER', and 'PART MARKING'.

NOTES:

1. Reference JEDEC MO-220-VHDD-5 for additional dimensional and tolerance information.
2. All dimensions shown are in/mm.
3. Reference S2083 application note for PCB footprint information.

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