EVALUATION KIT USER'S GUIDE FOR THE Si3050+Si3019 VOICE DAA CHIPSET

Description

This document describes the operation of the Skyworks Solutions Si3050+Si3019 Voice DAA Chipset device evaluation platform. The Voice DAA evaluation platform is designed to provide observation of the ProSLIC's functionality and operation. The Voice DAA platform consists of a voice motherboard, an Si3050 daughtercard, and the Voice GUI software. The Voice GUI software is a graphical user interface program that runs in the Microsoft Windows® environment.

There are two Si3050 evaluation boards available:

- The Si3050E1EGO1SL0-EVB allows evaluation of the Si3050 and Si3019 devices in TSSOP packages.
- The Si3050E1EGO1SL1-EVB allows evaluation of the Si3050 and Si3019 devices in QFN packages.

Function Block Diagram

The Si3050 evaluation boards can be easily controlled from a PC using the supplied application software.
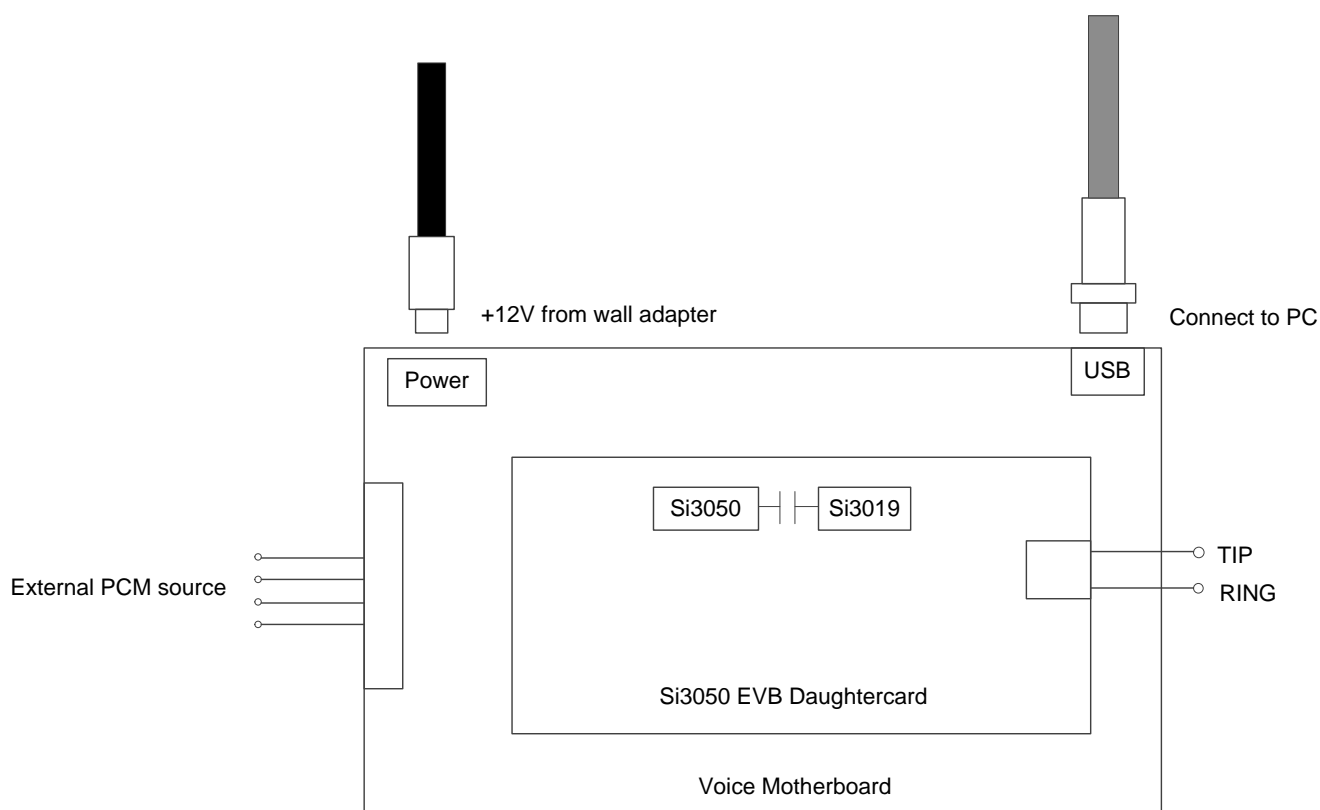
Equipment requirements:

- PC running Windows 7 or Windows 10
- 12 V, 2 A power supply

Features

- Ability to read and write DAA registers
- Recommended layout for key components
- Daisy-chain support

+12V from wall adapter

Connect to PC

Power

USB

External PCM source

Si3050 — Si3019

TIP

RING

Si3050 EVB Daughtercard

Voice Motherboard

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1. Voice Motherboard Setup

The voice motherboard has several jumpers that must be set to enable proper operation of this board. JP1 and JP2 provide power to the board from the connectors. The jumpers should be set as follows:

- JP1: 5V→5VU (pins 2–3)
- JP2: VIN→J10 (pins 3–4)

2. Daughtercard Setup

Daughtercard setup may be found in the schematic included in the documentation accompanying the evaluation kit.

3. Additional Reference Resources

- Si3050+Si3011/18/19 Data Sheet
- AN265: Skyworks Solutions Voice GUI User's Guide
- AN67: Si3050/52/54/56 Layout Guidelines
- AN84: Digital Hybrid with the Si305x DAAs
- Schematic and layout reference files are available in the EVB documentation kit

Si3050 Evaluation Kit User's Guide

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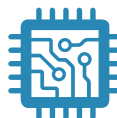
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