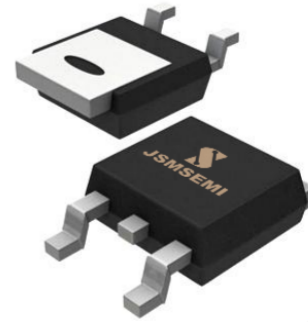


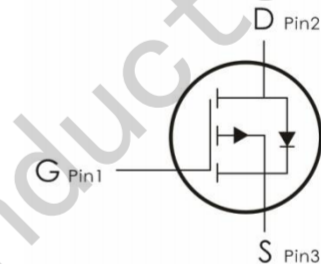
Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.



Features:

- ◆ $V_{DS}=-60V, I_D=-50A, R_{DS(ON)}<20m\Omega @ V_{GS}=-10V$
- ◆ Low gate charge.
- ◆ Green device available.
- ◆ Advanced high cell density trench technology for ultra low $R_{DS(ON)}$
- ◆ Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current- $T_C = 25^\circ C$	-50	A
	Continuous Drain Current- $T_C = 100^\circ C$	-40	A
I_{DM}	Pulsed Drain Current ¹	-280	A
P_D	Total Power Dissipation	270	W
E_{AS}	Single Pulsed Avalanche Energy	700	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.46	$^\circ C/W$

Electrical Characteristics: (Tc=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250 \mu A$	-60	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=-60V$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0A$	---	---	± 100	nA
On Characteristics						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250 \mu A$	-1.1	-1.6	-2.2	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-20A$	---	16.5	20	m Ω
		$V_{GS}=-4.5V, I_D=-20A$	---	18.5	22	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=-20V, V_{GS}=0V, f=1MHz$	---	4399	---	pF
C_{oss}	Output Capacitance		---	258	---	
C_{rss}	Reverse Transfer Capacitance		---	211	---	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD}=-30V, I_D=-20A,$ $V_{GS}=-10V, R_G=1 \Omega$	---	23	---	ns
t_r	Rise Time		---	17	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	55	---	ns
t_f	Fall Time		---	29	---	ns
Q_g	Total Gate Charge	$V_{GS}=-10V, V_{DS}=-30V,$ $I_D=-20A$	---	114	---	nC
Q_{gs}	Gate-Source Charge		---	27.3	---	nC
Q_{gd}	Gate-Drain“ Miller” Charge		---	49	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Drain Diode Forward Voltage	$V_{GS}=0V, I_S=-20A$	---	---	-1.2	V

I_S	Continuous Source Current	$V_G=V_D=0V$	---	---	-50	A
I_{SM}	Pulsed Source Current		---	---	-280	A
T_{rr}	Reverse Recovery Time	$T_J = 25^\circ C, I_F = -20A,$ $di/dt = 100A/\mu s$	---	117	---	nS
Q_{rr}	Reverse Recovery Charge		---	420	---	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: $T_J = 25^\circ C$, $V_{DD} = -25V$, $V_G = -5V$, $R_G = 25\Omega$, $L = 0.5mH$, I_{AS}
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Characteristics: ($T_C = 25^\circ C$ unless otherwise noted)

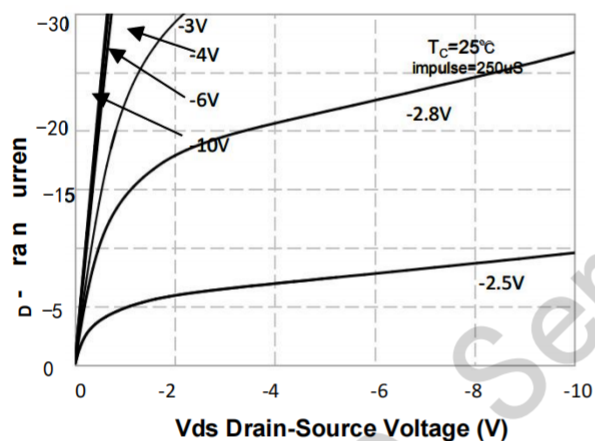


Figure 1. On-Region Characteristics

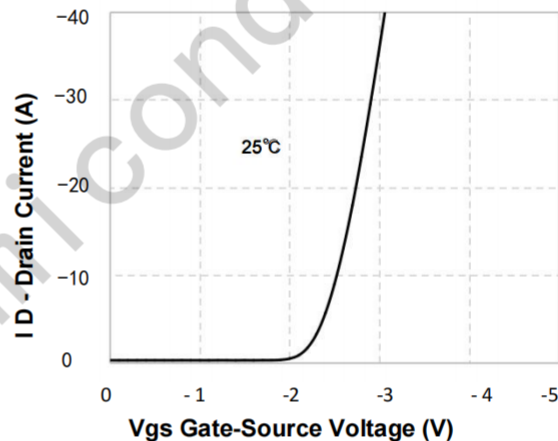


Figure 2. Transfer Characteristics

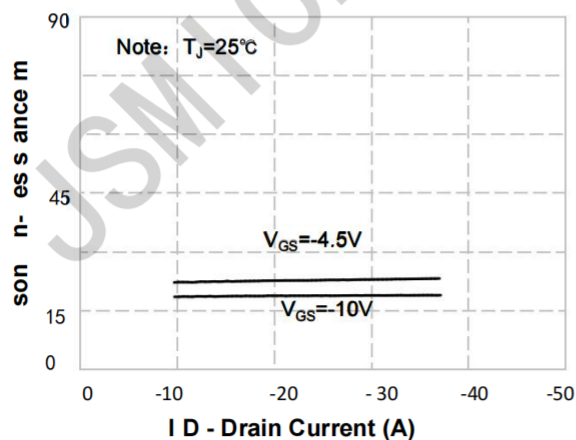


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

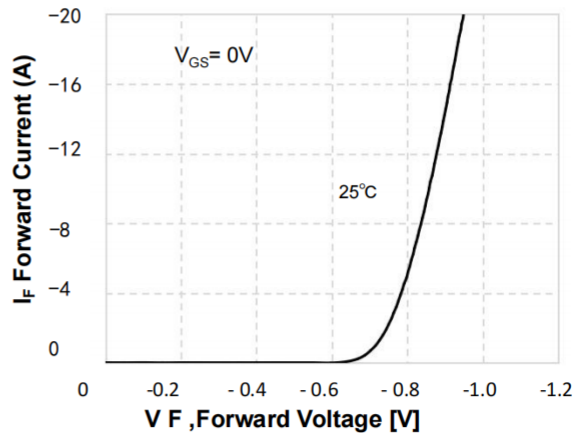


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

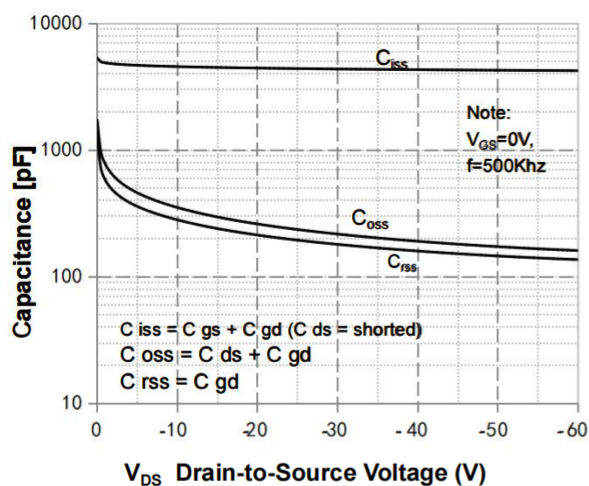


Figure 5. Capacitance Characteristics

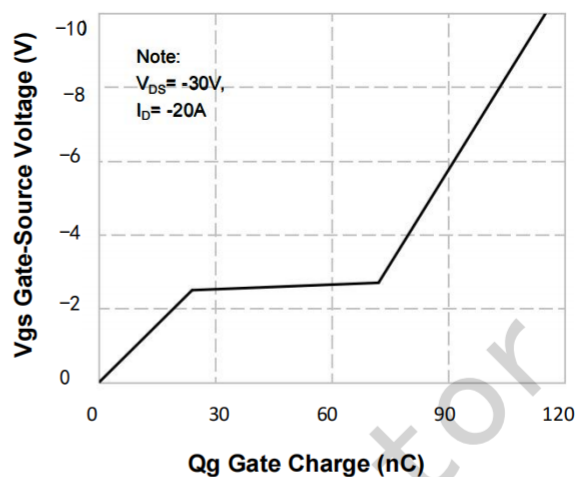


Figure 6. Gate Charge Characteristics

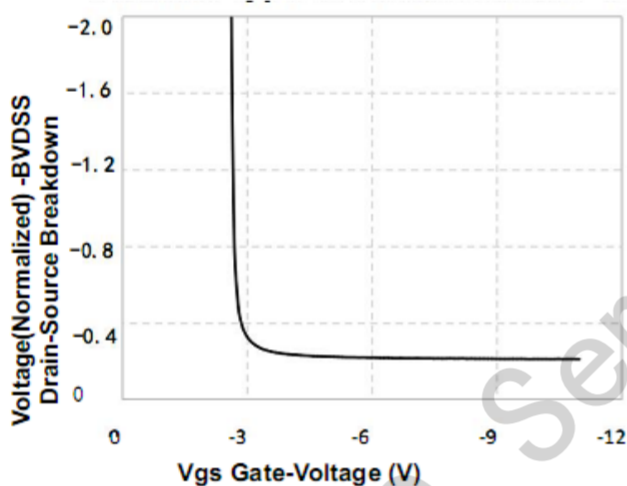


Figure 7. Breakdown Voltage Variation vs Gate-Voltage

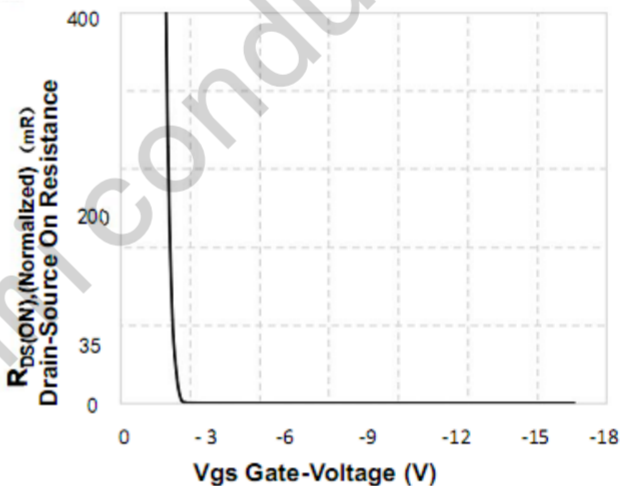


Figure 8. On-Resistance Variation vs Gate Voltage

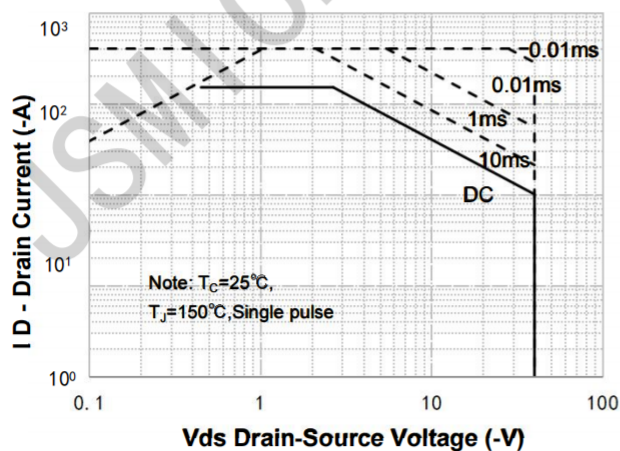


Figure 9. Maximum Safe Operating Area

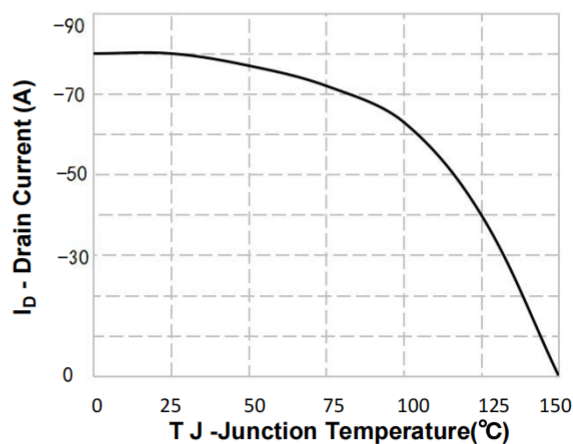


Figure 10. Maximum PContinuous Drain Current vs Case Temperature

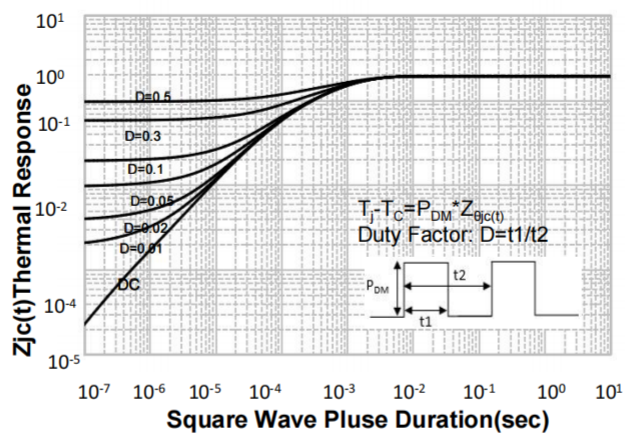


Figure 11. Transient Thermal Response Curve

TO-252 Package Dimensions

UNIT: mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	6.40		6.60	D	2.90		3.10
A1	5.20		5.40	D1	0.45		0.55
A2	4.40		4.60	D2	0.45		0.55
A3	4.40		4.60	e		2.30	
A4	0		0.15	E	2.20		2.40
A5	4.65		4.95	F	0.45		0.55
B	5.90		6.20	G		1.70	
B1	1.57		1.77	L	1.40		1.60
C	0.90		0.96	θ (度)	0		10.00

