

Description

The HIRLML6346TRPBF uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

V_{DS} = 30V I_D =5A

 $R_{DS(ON)} < 28 m\Omega @ V_{GS} = 10V$

Application

Battery protection

Load switch Uninterruptible power supply

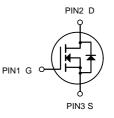
Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HIRLML6346TRPBF	SOT23-3L	X4HV	3000

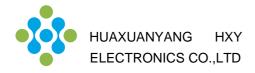
Absolute Maximum Ratings (TA=25℃ unless otherwise noted)

symbol	parameter	limit	unit
V _{DS}	Drain-source voltage		V
V _{GS}	Gate-source voltage		V
ID	Drain current-continuousª@Tj=125℃	5	А
IDM	-pulse d ^b	20	А
ls	Drain-source Diode forward current		А
PD	Maximum power dissipation	1.4	W
Тј	Operating junction Temperature range	-55—150	°C
Rth JA	Thermal Resistance junction-to ambient		°C/W





N-Channel MOSFET



N-Channel Enhancement Mode MOSFET

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Мах	Unit
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0V, I _D =250µA	30	-	-	V
Zero gate voltage drain current	IDSS	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
Gate-body leakage	IGSS	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
Gate threshold voltage	VGS(th)	V _{DS} =V _{GS} , I _D =250µA	0.8	1.4	2.2	V
		V _{GS} =10V, I _D =5A	-	24	28	
Drain-source on-state resistance	RDS(ON)	V _{GS} =4.5V, I _D =4A		26	32	mΩ
Forward transconductance	gfs	V _{GS} =5V, I _D =5A	-	33	-	S
Input capacitance	C _{ISS}	V _{DS} =15V ,V _{GS} =0V		255		pF
Output capacitance	COSS	f=1.0MHz		45		
Reverse transfer capacitance	CRSS	-		35		
Turn-on delay time	tD(ON)		-	4.5	-	
Rise time	tr	V _{DS} =15V V _{GS} =10V - R _L =2.6 ohm R _{GEN} =3ohm	-	2.5	-	- ns
Turn-off delay time	tD(OFF)		-	14.5	-	
Fall time	tf	-	-	3.5	-	
Total gate charge	Qg		-	5.2	-	
Gate-source charge	Qgs	V _{DS} =15V,I _D =5.8A	-	0.85	-	nC
Gate-drain charge	Qgd	V _{GS} =10V	-	1.3	-	
Diode forward voltage	Vsd	V _{GS} =0V,Is=1A	-	0.76	1.16	V

Notes:

1、 surface mounted on FR4 board,t≤10sec

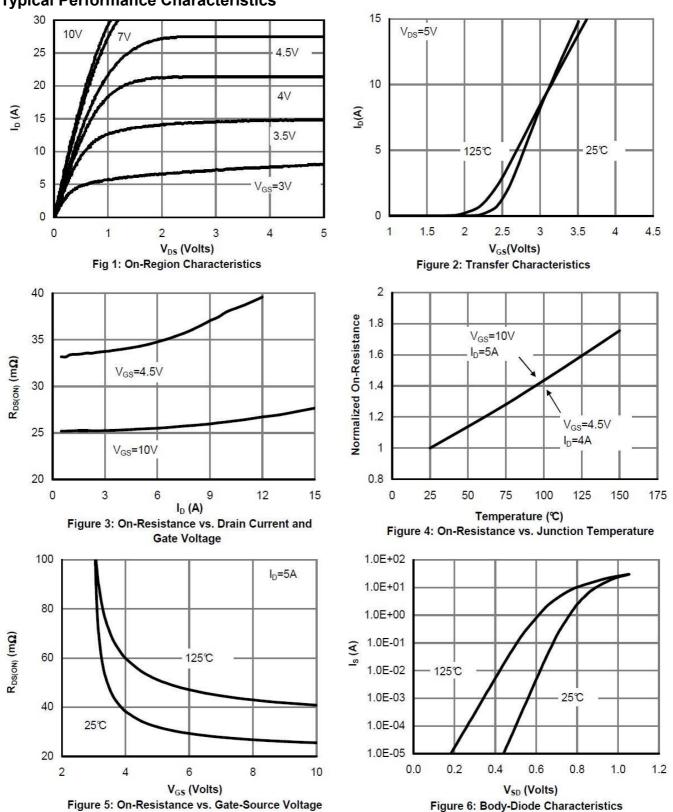
2、 pulse test: pulse width≤300µs,duty≤2%

3、guaranteed by design, not subject to production testing

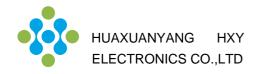


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Typical Performance Characteristics



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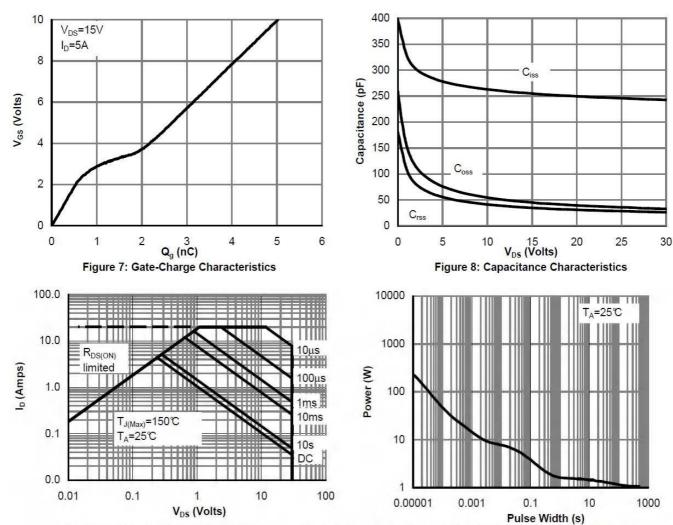
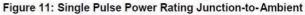


Figure 10: Maximum Forward Biased Safe Operating Area



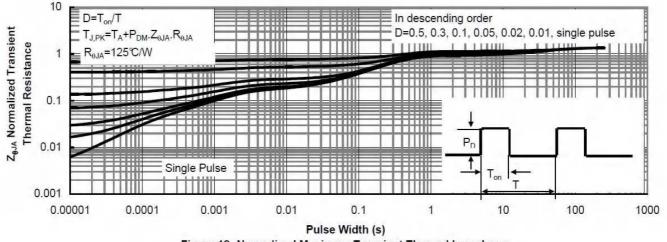
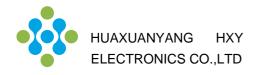
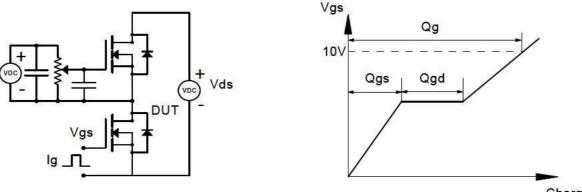


Figure 12: Normalized Maximum Transient Thermal Impedance



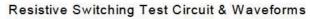
HIRLML6346TRPBF N-Channel Enhancement Mode MOSFET

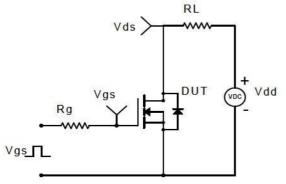
Gate Charge Test Circuit & Waveform

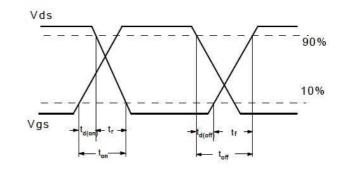


Charge

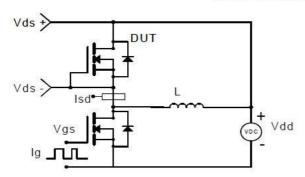
Resistive Switching Test Circuit & Waveforms

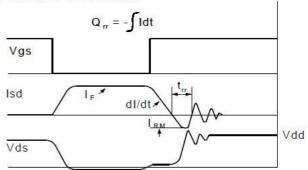






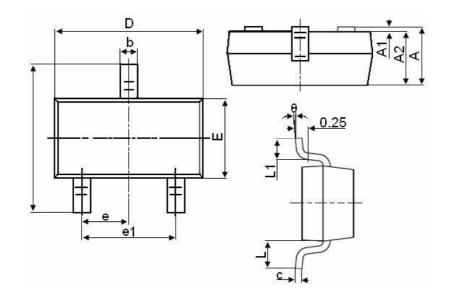
Diode Recovery Test Circuit & Waveforms







SOT23-3L Package Information



Symbol	Dimensions in Millimeters			
	MIN.	MAX.		
A	1.050	1.250		
A1	0.000	0.100		
A2	1.050	1.150		
b	0.300	0.500		
С	0.100	0.200		
D	2.800	3.000		
E	1.500	1.700		
E1	2.650	2.950		
е		0.950TYP		
e1	1.800	2.000		
L	0.550REF			
L1	0.300	0.600		
θ	0°	8°		



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