

## Description

The HIPD30N06S4L23 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 60V I<sub>D</sub> = 30 A

 $R_{DS(ON)} < 26m\Omega @ V_{GS}=10V$ 

### Application

Battery protection

Load switch

Uninterruptible power supply

#### **Package Marking and Ordering Information**

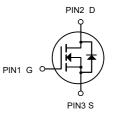
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Product ID	Pack	Brand	Qty(PCS)	
HIPD30N06S4L23	TO-252-2L(TO-252-3)	HXY MOSFET	2500	

#### Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	60	V	
Vgs	Gate-Source Voltage	±20	V	
I₀@Tc=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	30	A	
ID@Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	15	А	
Ідм	Pulsed Drain Current <sup>2</sup>	se Avalanche Energy <sup>3</sup> 25.5		
EAS	Single Pulse Avalanche Energy <sup>3</sup>			
las	Avalanche Current			
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	34.7	W	
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>4</sup>	2	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
R <sub>0JA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>			
Rejc	Thermal Resistance Junction-Case <sup>1</sup>			

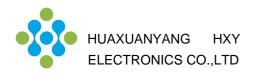


(TO-252-3)



N-Channel MOSFET

N-Channel Enhancement Mode MOSFET



Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	60			V
∆BVbss/∆Tj	Dess/△TJ BVDss Temperature Coefficient Reference to 25°C , ID=1mA			0.063		V/°C
		V <sub>GS</sub> =10V , I <sub>D</sub> =15A		22	26	
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		30	38	mΩ
VGS(th)	Gate Threshold Voltage		1.2		2.5	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient			-5.24		mV/°C
_	Drain-Source Leakage Current	V <sub>DS</sub> =48V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	
ldss		V <sub>DS</sub> =48V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5	uA
lgss	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =15A		17		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		3.2		Ω
Qg	Total Gate Charge (4.5V)			12.6		
Qgs	Gate-Source Charge			3.2		nC
$Q_{gd}$	Gate-Drain Charge	-		6.3		
Td(on)	Turn-On Delay Time			8		
Tr	Rise Time	V <sub>DD</sub> =30V , V <sub>GS</sub> =10V ,		14.2		ns
Td(off)	Turn-Off Delay Time	–R <sub>G</sub> =3.3 , I⊳=10A		24.4		
T <sub>f</sub>	Fall Time			4.6		
C <sub>iss</sub>	Input Capacitance			1378		
Coss	Output Capacitance	_ V⊳s=15V , V <sub>GS</sub> =0V , f=1MHz		86		pF
Crss	Reverse Transfer Capacitance	-		64		
Is	Continuous Source Current <sup>1,5</sup>				23	A
lsм	Pulsed Source Current <sup>2,5</sup>	−V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			46	Α
Vsd	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1.2	V

## Electrical Characteristics (TJ=25°C, unless otherwise noted)

Note :

1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width  $\leq 300 us$  , duty cycle  $\leq 2\%$ 

3.The EAS data shows Max. rating . The test condition is VDD=25V,VGS=10V,L=0.1mH,IAS=22.6A 4.The power dissipation is limited by 150°C junction temperature

5.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.



## HIPD30N06S4L23 N-Channel Enhancement Mode MOSFET

## **Typical Characteristics**

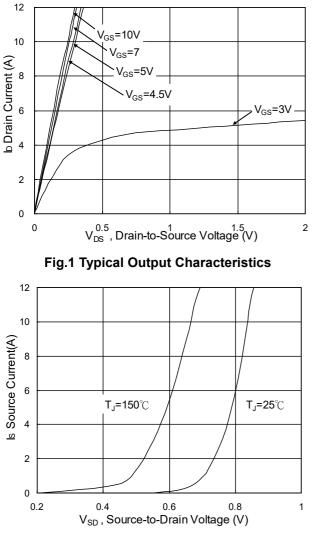


Fig.3 Forward Characteristics of Reverse

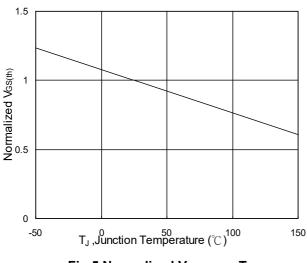


Fig.5 Normalized  $V_{GS(th)}$  v.s T<sub>J</sub>

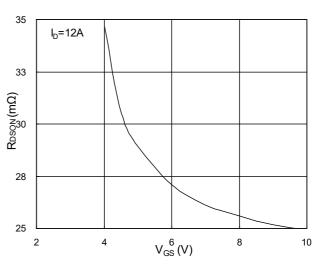


Fig.2 On-Resistance v.s Gate-Source

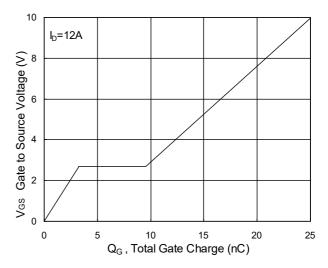


Fig.4 Gate-Charge Characteristics

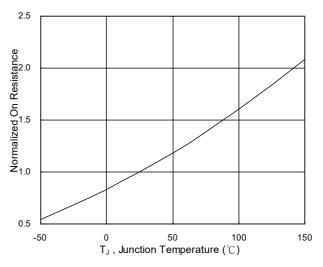
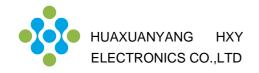


Fig.6 Normalized R<sub>DSON</sub> v.s T<sub>J</sub>



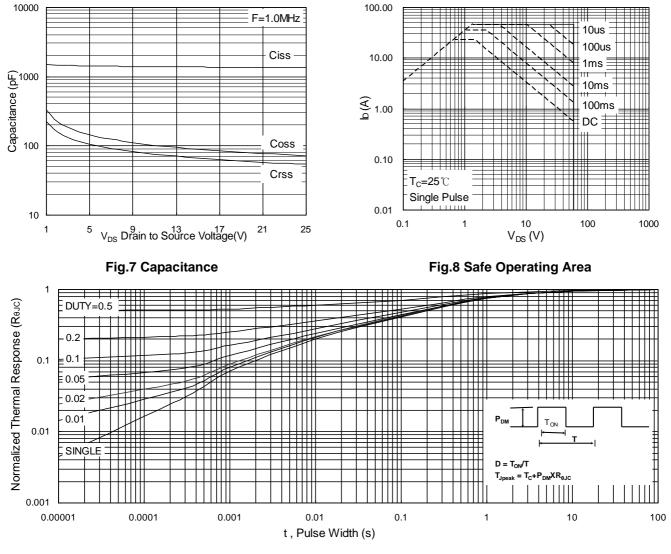


Fig.9 Normalized Maximum Transient Thermal Impedance

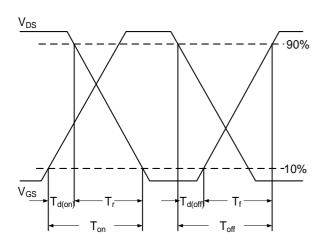
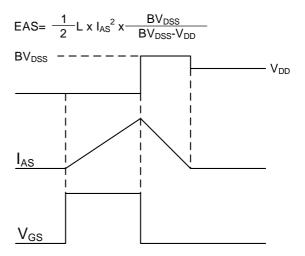
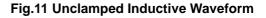
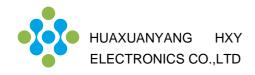


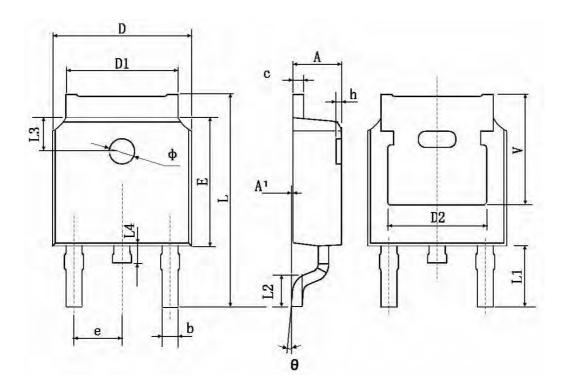
Fig.10 Switching Time Waveform







# TO-252-2L(TO-252-3) Package Information



	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
с	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.483	TYP.	0.190	) TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	2.900 TYP. 0.114 TYP.		4 TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063	3 ТҮР.	
L4	0.600	1.000	0.024	0.039	
Φ	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211 TYP.		



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