



General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

30V,200mA, $R_{DS(ON)} = 5\Omega$ @ VGS = 10V

Improved dv/dt capability

Fast switching

Green Device Available

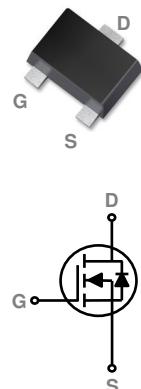
Applications

Notebook

Load Switch

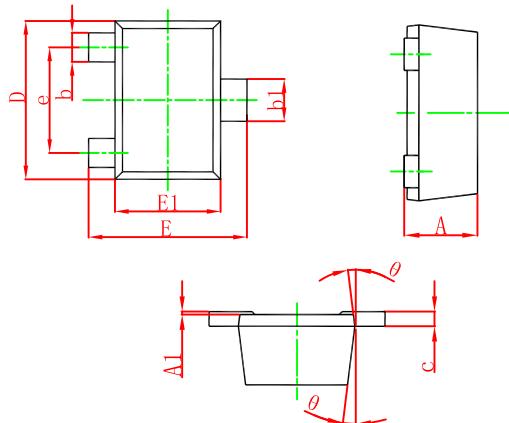
Battery Protection

Hand-held Instruments



BVDSS	RDS(ON)	ID
30V	5Ω	200mA

SOT-723 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.430	0.500	0.017	0.020
A1	0.000	0.050	0.000	0.002
b	0.170	0.270	0.007	0.011
b1	0.270	0.370	0.011	0.015
c	0.080	0.150	0.003	0.006
D	1.150	1.250	0.045	0.049
E	1.150	1.250	0.045	0.049
E1	0.750	0.850	0.030	0.033
e	0.800TYP.		0.031TYP.	
θ	7° REF.		7° REF.	

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_A=25^\circ C$)	200	mA
	Drain Current – Continuous ($T_A=70^\circ C$)	180	mA
I_{DM}	Drain Current – Pulsed ¹	1.2	A
P_D	Power Dissipation ($T_c=25^\circ C$)	313	mW
	Power Dissipation – Derate above 25°C	2.5	mW/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	800	°C/W

2SK3541

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	30	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.05	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=50^\circ\text{C}$	---	---	100	nA
		$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=85^\circ\text{C}$	---	---	400	nA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 6	μA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=4\text{V}$, $I_D=10\text{mA}$	---	5.0	7.5	Ω
		$V_{\text{GS}}=2.5\text{V}$, $I_D=1\text{mA}$	---	6.0	12	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	---	---	1.6	V
$\Delta V_{\text{GS(th)}}$	$V_{\text{GS(th)}}$ Temperature Coefficient		---	3	---	$\text{mV}/^\circ\text{C}$

Dynamic and switching Characteristics

C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	15	---	pF
			---	10	---	
			---	3	---	
C_{oss}	Output Capacitance	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	---	---	---	pF
C_{rss}	Reverse Transfer Capacitance		---	---	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	200	mA
			---	---	500	mA
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=0.2\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.4	V

Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.

RATING AND CHARACTERISTIC CURVES (2SK3541)

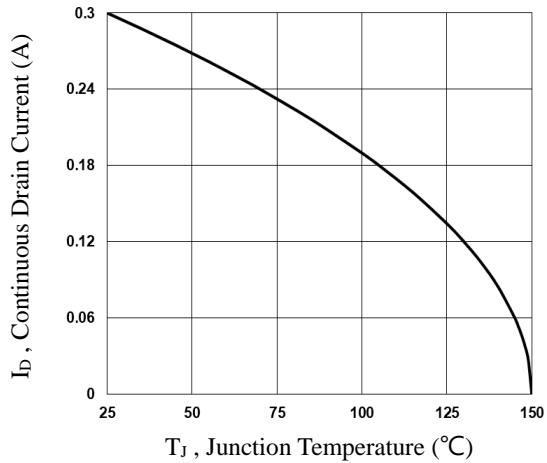


Fig.1 Continuous Drain Current vs. T_J

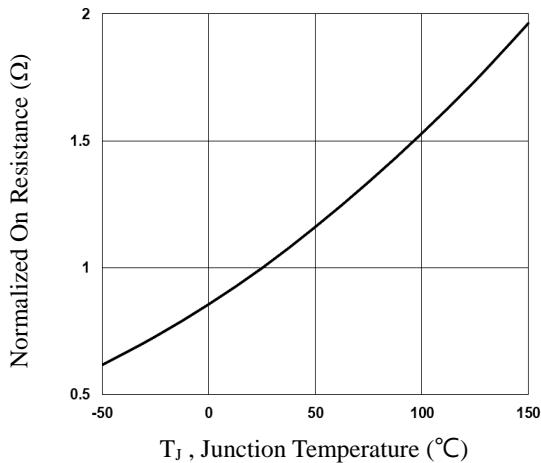


Fig.2 Normalized R_{DS(on)} vs. T_J

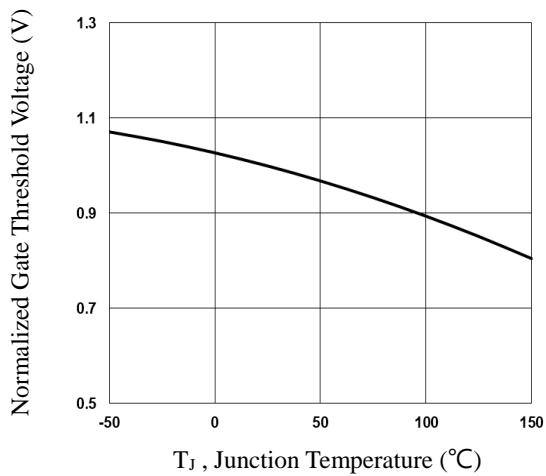


Fig.3 Normalized V_{th} vs. T_J

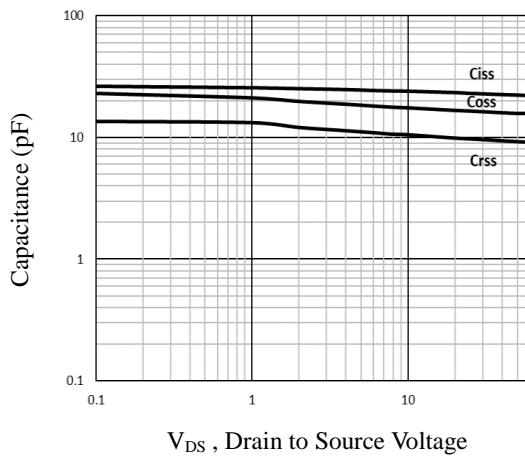


Fig.4 Capacitance Characteristics

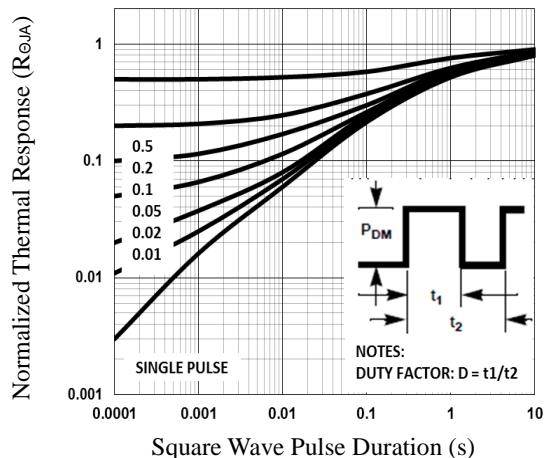


Fig.5 Normalized Transient Response

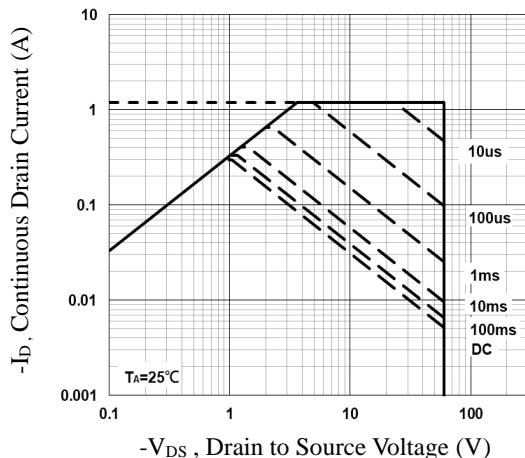


Fig.6 Maximum Safe Operation Area

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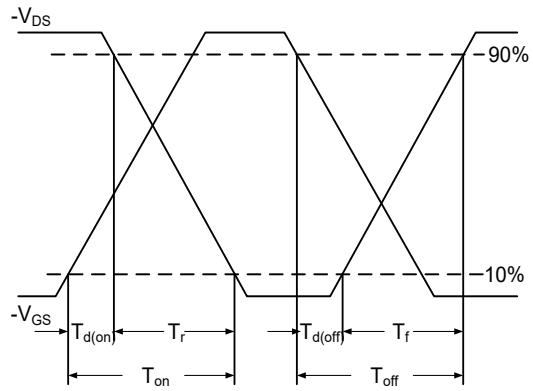


Fig.7 Switching Time Waveform

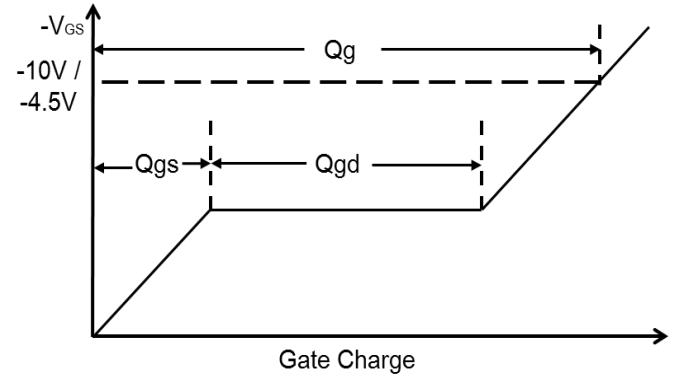


Fig.8 Gate Charge Waveform