# MSKSEMI 美森科













ESD

15

TSS

MOV

GDT

PIFD

## NTR4003NT1G-MS

**Product specification** 





#### **Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
30V	1.0Ω	0.5A

## **Features**

- 30V,0.5A, RDS(ON) =1.0Ω@VGS=10V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- G-S ESD Protection Diode Embedded
- ESD protected up to 2KV

## **Applications**

- Motor Drive
- Power Tools
- LED Lighting

#### **Reference News**

PACKAGE OUTLINE	N-Channel MOSFET	Marking
SOT-23	Go	TR8*
501-25		

## Absolute Maximum Ratings (T<sub>A</sub>=25℃unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	30	V
Vgs	Gate-Source Voltage	±20	V
	Drain Current – Continuous (T₄=25°C)	0.5	А
lb	Drain Current – Continuous (T₄=70°C)	0.3	А
Ірм	Drain Current – Pulsed1	1.0	А
Po	Power Dissipation (T <sub>A</sub> =25°C)	0.35	W
FU	Power Dissipation – Derate above 25℃	0.003	W/℃
Тѕтс	Storage Temperature Range	-55 to 150	
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
Rеja	Thermal Resistance Junction to ambient		357	°C/W



## Electrical Characteristics (T<sub>J</sub>=25 ℃, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
Ipss	Drain-Source Leakage Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			1	uA
-		V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			100	uA
Igss	Gate-Source Leakage Current	Vgs=±20V , Vps=0V			±10	uA
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V

#### **On Characteristics**

Process	Static Drain-Source On-Resistance	Vgs= 10V , Ip=0.2A		1.0	1.5	Ω
RDS(ON)	Static Drain-Source On-Resistance	Vgs=4.5V , ID=0.1A		1.5	2.5	Ω
V <sub>GS(th)</sub>	Gate Threshold Voltage	Vgs=Vps , Ip =250uA	0.8	1.1	1.5	V
gfs	Forward Transconductance	V <sub>DS</sub> = 10V , I <sub>D</sub> =0.2A		0.5		S

### **Dynamic and switching Characteristics**

Qg	Total Gate Charge <sup>2, 3</sup>		 3.7	
Qgs	Gate-Source Charge <sup>2, 3</sup>	$V_{DS}$ =30 $V$ , $V_{GS}$ =10 $V$ , $I_{D}$ =0.2 $A$	 0.9	 nC
Qgd	Gate-Drain Charge <sup>2, 3</sup>		 0.4	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2,3</sup>		 3	
Tr	Rise Time <sup>2, 3</sup>	V <sub>DD</sub> =30V , V <sub>GS</sub> =10V ,	 5	 
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2, 3</sup>	R <sub>G</sub> =6Ω l <sub>D</sub> =0.2A	 14	 ns
Tf	Fall Time <sup>2, 3</sup>		 9	
Ciss	Input Capacitance		 25.5	
Coss	Output Capacitance	VDS=30V , VGS=0V , F=1MHz	 17	 рF
Crss	Reverse Transfer Capacitance		 7.8	

## **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			0.5	Α
Іѕм	Pulsed Source Current				1.0	Α
Vsp	Diode Forward Voltage	V <sub>G</sub> s=0V , I <sub>S</sub> =0.2A , T <sub>J</sub> =25℃			1.4	V

#### Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leqq$  300us , duty cycle  $\leqq$  2%.
- 3. Essentially independent of operating temperature.

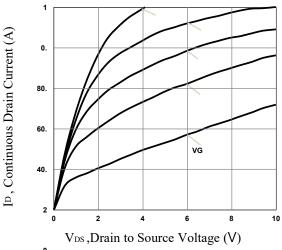


Fig.1 Typical Output Characteristics

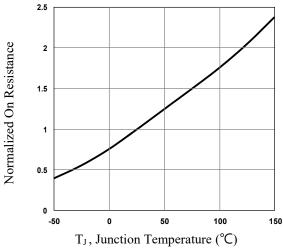


Fig.3 Normalized RDSON vs. T<sub>J</sub>

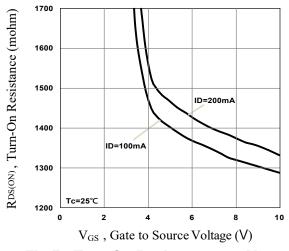


Fig.5 Turn-On Resistance vs. V<sub>GS</sub>

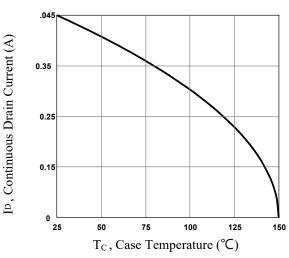


Fig.2 Continuous Drain Current vs. Tc

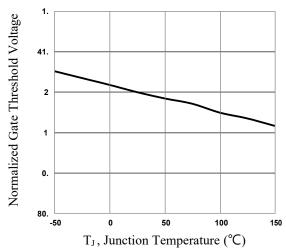


Fig.4 Normalized V<sub>th</sub> vs. T<sub>J</sub>

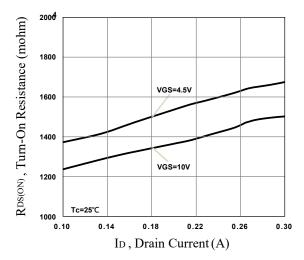


Fig.6 Turn-On Resistance vs. ID



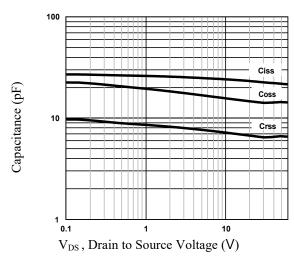


Fig.7 Capacitance Garacteristics

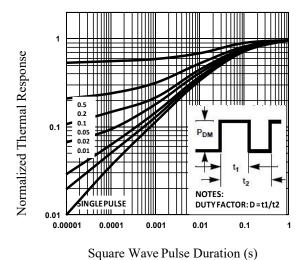


Fig.9 Normalized Transient

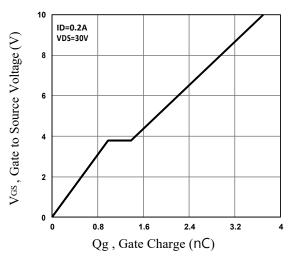
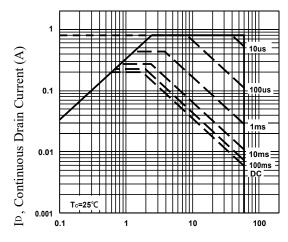


Fig.8 Gate Charge Characteristics

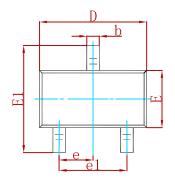


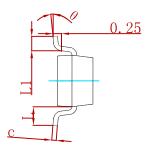
V<sub>DS</sub>, Drain to Source Voltage (V)

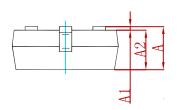
Fig.10 Maximum Safe Operation Area



## PACKAGE MECHANICAL DATA

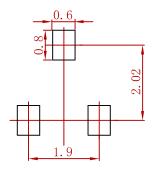






Cumbal	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
Α	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
С	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
е	0.950 TYP		0.037	7 TYP
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022	REF
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

## **Suggested Pad Layout**



#### Note:

- 1. Controlling dimension: in millimeters.
- 2.General tolerance:± 0.05mm.
  3.The pad layout is for reference purposes only.

## **REELSPECIFICATION**

P/N	PKG	QTY
NTR4003NT1G-MS	SOT-23	3000



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