

Features

- · 3rd generation SiC MOSFET technology
- · Optimized package with separate driver source pin
- High blocking voltage with low on-resistance
- · High-speed switching with low capacitances
- · Fast intrinsic diode with low reverse recovery (Qrr)
- · Halogen free, RoHS compliant

Benefts

- · Reduce switching losses and minimize gate ringing
- Higher system effciency
- · Reduce cooling requirements
- · Increase power density
- Increase system switching frequency

Applications

- Renewable energy
- · EV battery chargers
- High voltage DC/DC converters
- Switch Mode Power Supplies

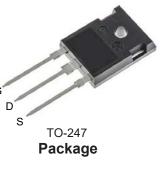
Ordering Part Number	Package	Marking	
HSCTWA35N65G2V	TO-247	HC3M0045065D	

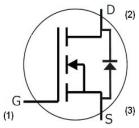
Maximum Ratings (Tc = 25 °C unless otherwise specifed)

Parameter	Symbol	Value	Unit
Drain-source voltage	Vds	650	V
Continuous drain current Tc = 25°C Tc = 100°C	١D	49 35	A
Pulsed drain current (Tc = 25°C, t_P limited by T_{jmax})	D pulse	123	А
Avalanche energy, single pulse (L=10mH)	Eas	1000	mJ
Gate-Source voltage	Vgs	-5/+20	V
Gate-Source voltage (dynamic,Absolute maximum values)	VGSmax	-10/+25	V
Power dissipation ($Tc = 25^{\circ}C$)	Ptot	242	W
Operating junction and storage temperature	Tj,Tstg	-55+175	°C

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	RthJC	0.62	°C/W
Thermal resistance, junction – ambient. Max	RthJA	40	0/00







Parameter	Symbol	Value			Unit	Test Condition	
Falalletei	Symbol	min.	typ.	max.	Unit	Test Condition	
Static Characteristic							
Drain-source breakdown voltage	BVDSS	650	-	-	V	Vgs=0V, Id=250uA	
Gate threshold voltage	VGS(th)	2	-	4	V	Vds=Vgs,Id=7mA	
Zero gate voltage drain current	IDSS	-	1 10	100 -	μA	Vbs=650V,Vgs=0V Tj=25°C Tj=175°C	
Gate-source leakage current	lgss	-		250	nA	Vgs=20V,Vds=0V	
		-	45	-		Vgs=18V, Ib=17.6A,	
Drain-source on-state resistance	RDS(on)	-	33 50	49 -	m	V _{GS} =20V, I _D =17.6A, T _j =25°C T _j =175°C	
Transconductance	g fs	-	5.6	-	S	Vps=20V,Ip=17.6A	
Dynamic Characteristic			1	•	1	•	
Input Capacitance	Ciss	-	1823	-		$V_{DS} = 650V$ $V_{GS} = 0V$ $T_J = 25^{\circ}C$ $V_{AC} = 25mV$ $f = 1MHz$	
Output Capacitance	Coss	-	190	-	pF		
Reverse Transfer Capacitance	Crss	-	19	-			
Gate Total Charge	QG	-	96	-		VDS = 400V VGS = -5/20V ID = 17.6A	
Gate-Source charge	Qgs	-	25	-	nC		
Gate-Drain charge	Qgd	-	26	-			
Turn-On Switching Energy	Eon	-	188	-	μJ	VDD = 400V VGS = -5/+20V ID = 17.6A RG = 10	
Turn-Off Switching Energy-	Eoff	-	19				
Turn-on delay time	t _{d(on)}	-	20	-	20		
Rise time	tr	-	26	-			
Turn-off delay time	td(off)	-	48	-	ns	L = 100uH	
Fall time	tf	-	15	-			
Gate resistance	Rg	-	1.7	-		Vac = 25mV, f=1MHz	

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

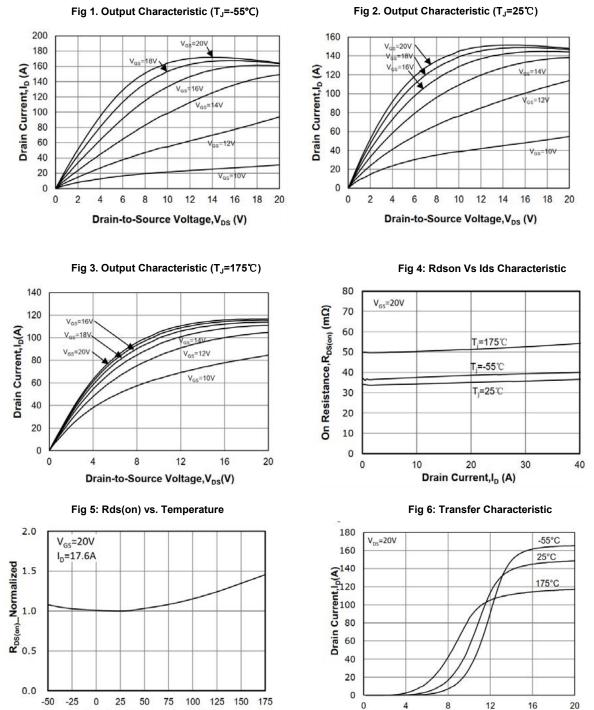


Body Diode Characteristic

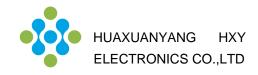
Parameter	Symbol	Value			Unit	Test Condition	
i arameter	Symbol	min.	typ.	max.	Onit	Test condition	
Body Diode Forward Voltage	Vsd		3.2		V	Vgs=0V,Isd=8.8A, Tj=25°C	
Body Diode Forward Voltage	VSD		2.6			Vgs=0V,Isd=8.8A, Tj=175°C	
Body Diode Reverse Recovery Time	trr	-	40	-	ns	Vr = 400V, Id = 17.6A	
Body Diode Reverse Recovery Charge	Qrr	-	156	-	nC	di/dt = 1000A/µS	

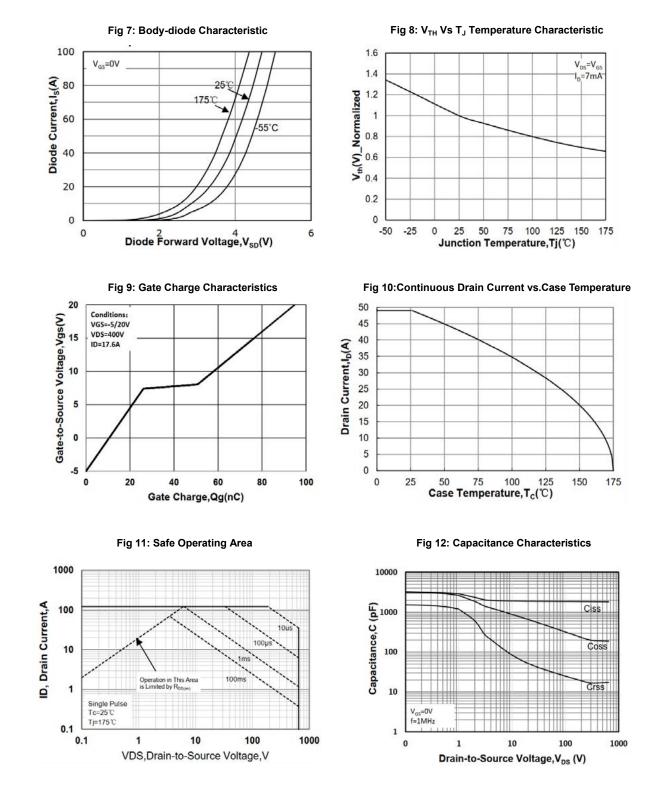


Typical Performance Characteristics



Junction Temperature, Tj (°C)

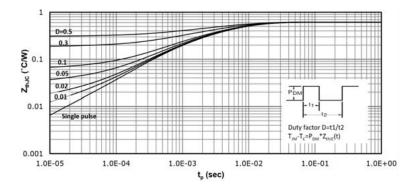




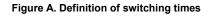


HSCTWA35N65G2V SiC Power MOSFET N-Channel Enhancement Mode

Fig 13: Transient Thermal Impedance



Test Circuit & Waveform



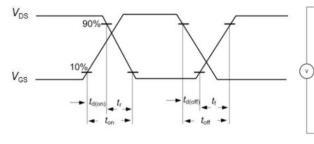


Figure B. Dynamic test circuit

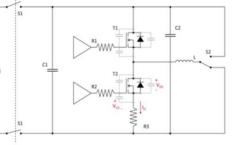
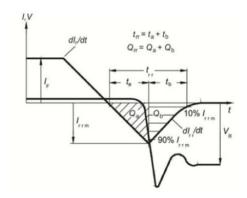
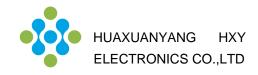


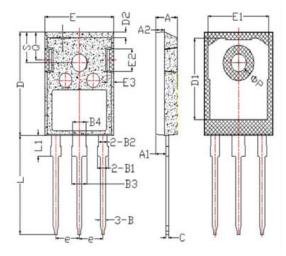
Figure C. Definition of body diodeswitching characteristics





Package Dimensions

Package TO-247



Items	Values(mm)					
	MIN	MAX				
А	4.6	5.2				
A1	2.2	2.6				
В	0.9	1.4				
B1	1.75	2.35				
B2	1.75	2.15				
B3	2.8	3.35				
B4	2.8	3.15				
С	0.5	0.7				
D	20.6	21.3				
D1	16	18				
E	15.5	16.1				
E1	13	14.7				
E2	3.8	5.3				
E3	0.8	2.6				
e	5.2	5.2				
L	19	20.5				
L1	3.9	4.6				
Фр	3.3	3.7				
Q	5.2	6				
S	5.8	6.6				



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