

TLP3250

Measurement Instruments

Logic IC Testers / Memory Testers

Board Testers / Scanners

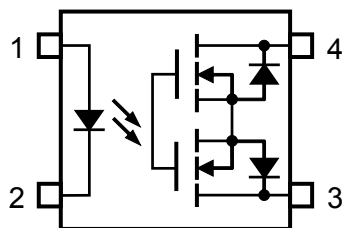
The TOSHIBA TLP3250 is a super small-outline photorelay, suitable for surface-mount assembly. The TLP3250 consists of an infrared emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

Its characteristics also include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measuring instruments.

Features

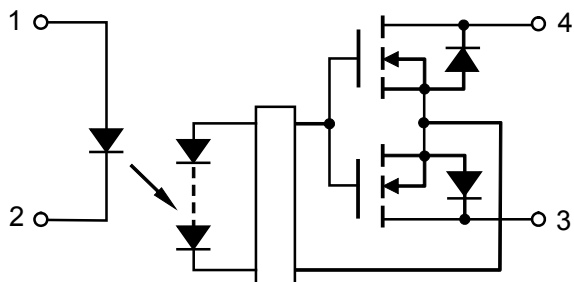
- 4 pin SSOP (SSOP4) : 1.8 mm high, 1.27 mm pitch
- 1-Form-A
- Peak off-state voltage : 20 V (min)
- Trigger LED current : 3 mA (max)
- On-state current : 200 mA (max)
- On-state resistance : 5Ω (max), 3Ω (typ.)
- Output capacitance : 1.1 pF (max), 0.8 pF (typ.)
- Isolation voltage : 1500 Vrms (min)
- UL-recognized : UL 1577, File No.E67349

Pin configuration (top view)

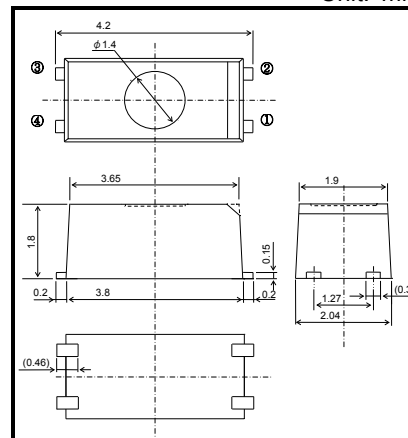


1 : Anode
2 : Cathode
3 : Drain
4 : Drain

Schematic



Unit: mm



Enlarged drawing is shown on page 4.

JEDEC	
JEITA	
TOSHIBA	11-2B1

Weight: 0.03 g (typ.)

Start of commercial production
2007-08

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
LED	Forward current	I _F	30	mA
	Forward current derating (Ta ≥ 25°C)	ΔI _F /°C	-0.3	mA/°C
	Reverse voltage	V _R	5	V
	Diode power dissipation	P _D	50	mW
	Diode power dissipation derating (Ta ≥ 25°C)	ΔP _D /°C	-0.5	mW/°C
	Junction temperature	T _j	125	°C
Detector	Off-State output terminal voltage	V _{OFF}	20	V
	On-State current	I _{ON}	200	mA
	On-State current derating (Ta ≥ 25°C)	ΔI _{ON} /°C	-2.0	mA/°C
	Output power dissipation	P _C	200	mW
	Output power dissipation derating (Ta ≥ 25°C)	ΔP _C /°C	-2.0	mW / °C
	Junction temperature	T _j	125	°C
Storage temperature range		T _{stg}	-40 to 125	°C
Operating temperature range		T _{opr}	-20 to 85	°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
Isolation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)		BV _S	1500	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: Pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

Precautions

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

Recommended Operating Conditions

Characteristic	Symbol	Min	Typ.	Max	Unit
Forward current	I _F	—	—	20	mA
Operating temperature	T _{opr}	25	—	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V _F	I _F = 5 mA	1.15	1.30	1.45	V
	Reverse current	I _R	V _R = 5 V	—	—	10	μA
	Capacitance	C _T	V _F = 0 V, f = 1 MHz	—	30	—	pF
Detector	Off-state current	I _{OFF}	V _{OFF} = 20 V	—	10	200	pA
	Capacitance	C _{OFF}	V = 0 V, f = 100 MHz, t < 1 s	—	0.8	1.1	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	I_{FT}	$I_{ON} = 100 \text{ mA}$	—	—	3	mA
Return LED current	I_{FC}	$I_{OFF} = 1 \text{ } \mu\text{A}$	0.1	—	—	mA
On-state resistance	R_{ON}	$I_{ON} = 200 \text{ mA}$, $I_F = 5 \text{ mA}$, $t < 1 \text{ s}$	—	3	5	Ω

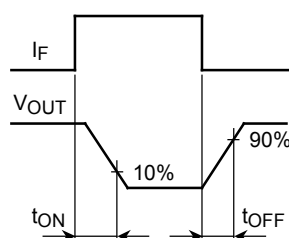
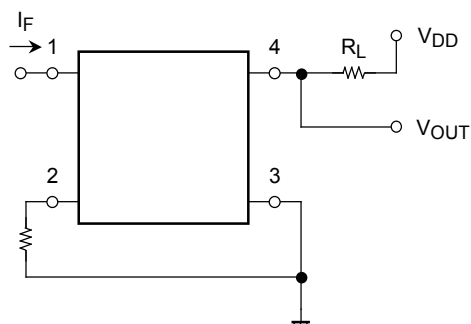
Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance input to output	C_S	$V_S = 0 \text{ V}$, $f = 1 \text{ MHz}$	—	0.6	—	pF
Isolation resistance	R_S	$V_S = 500 \text{ V}$, R.H. $\leq 60 \%$	5×10^{10}	10^{14}	—	Ω
Isolation voltage	BV_S	AC, 60 s	1500	—	—	Vrms

Switching Characteristics (Ta = 25°C)

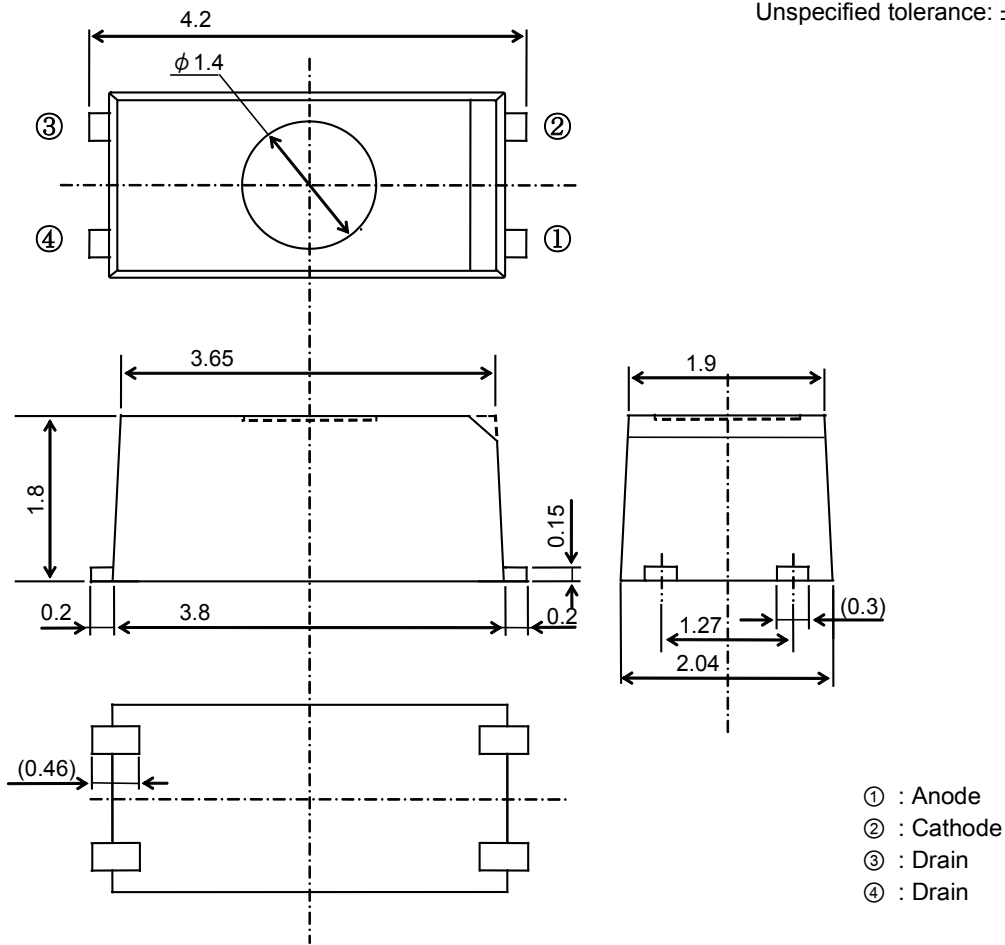
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Turn-on time	t_{ON}	$R_L = 200 \text{ } \Omega$ $V_{DD} = 10 \text{ V}$, $I_F = 5 \text{ mA}$ (Note 2)	—	26	200	μs
Turn-off time	t_{OFF}		—	45	200	

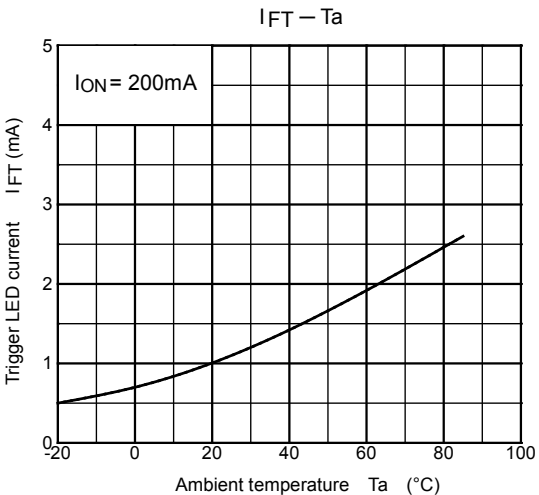
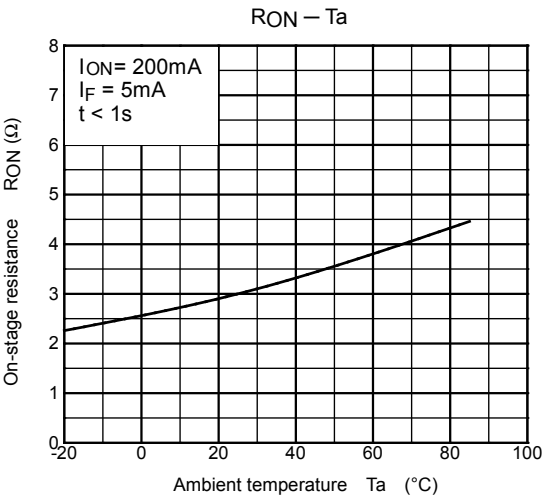
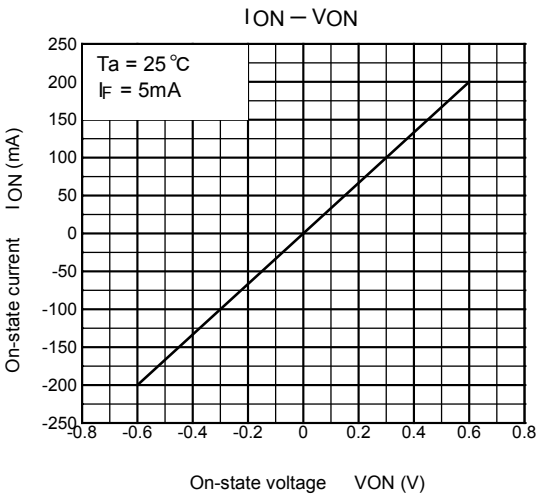
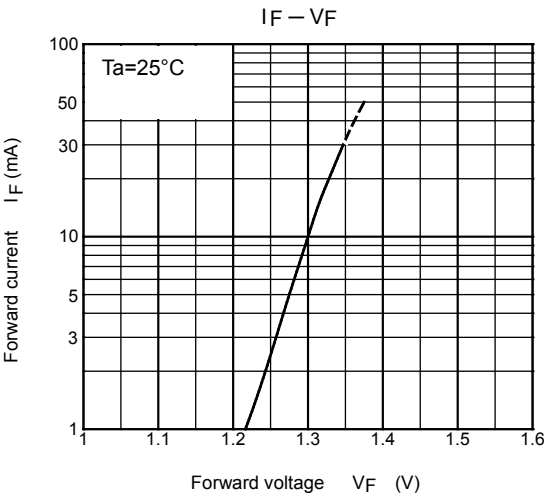
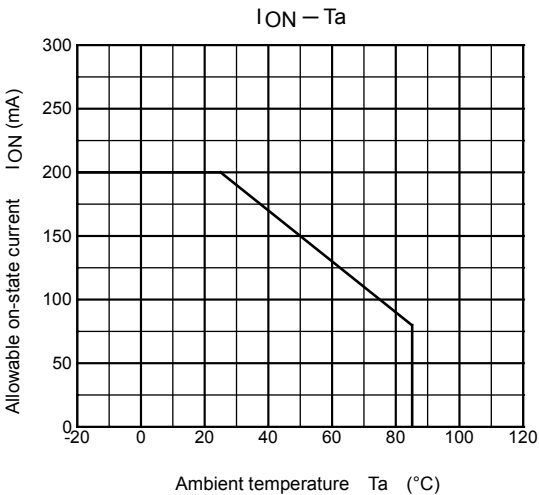
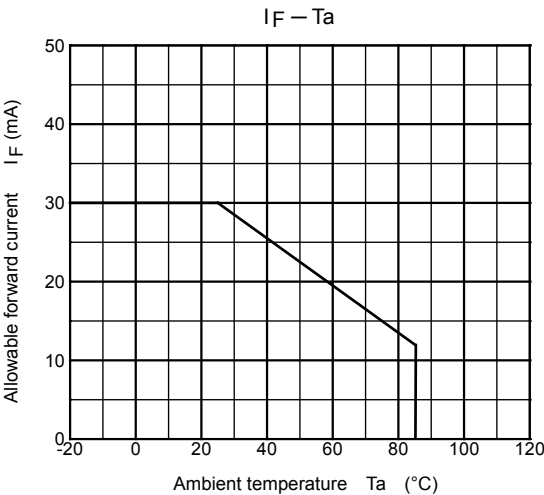
Note 2: switching time test circuit



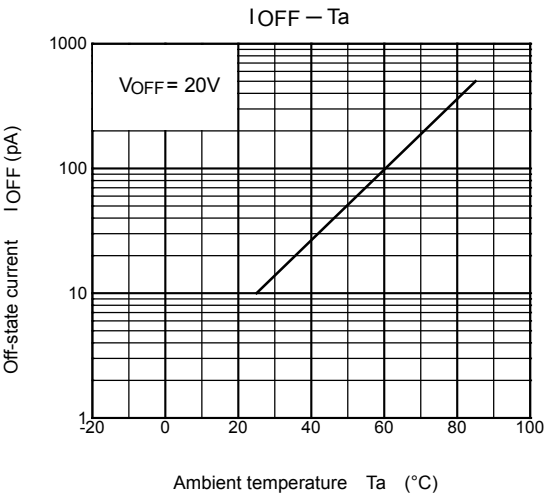
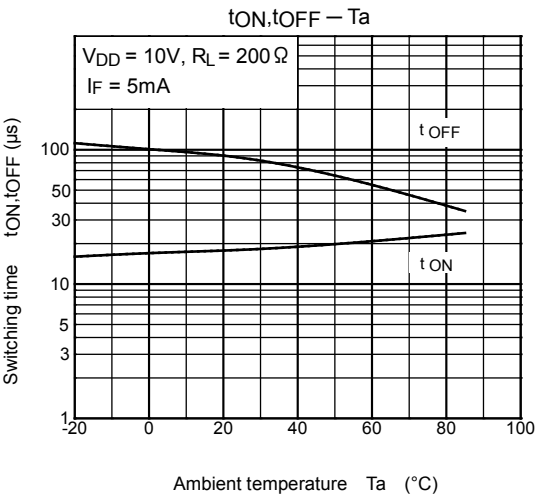
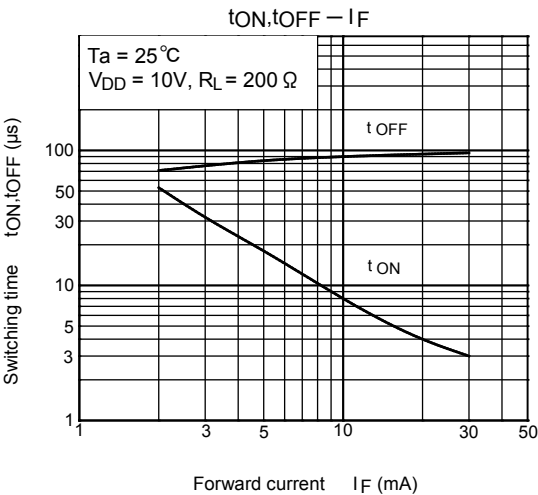
Package Dimensions

Unit: mm
Unspecified tolerance: ±0.1





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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