

1. General description

Planar passivated high commutation three quadrant triac in a SOT186A "full pack" plastic package. This triac is intended for use in motor control circuits where very high blocking voltage, high static and dynamic dV/dt as well as high dI_{com}/dt can occur. This "series C0T" triac will commute the full RMS current at the maximum rated junction temperature ($T_{j(max)} = 150\text{ °C}$) without the aid of a snubber. It is used in applications where "high junction operating temperature capability" is required.

2. Features and benefits

- High minimum IGT for guaranteed immunity to gate noise
- Full cycle AC conduction
- High junction operating temperature capability ($T_{j(max)} = 150\text{ °C}$)
- Over-voltage withstand capability to IEC 61000-4-5
- Pin compatible with standard triacs
- Planar passivated for voltage ruggedness and reliability
- Protective self turn-on capability for high energy transients
- Less sensitive gate for high noise immunity
- Triggering in three quadrants only
- Very high immunity to false turn-on by dV/dt and IEC 61000-4-4 fast transient
- Package meets UL94V0 flammability requirement
- Package is RoHS compliant
- Package meets UL1557 isolation test requirement rated at 2500V RMS

3. Applications

- AC fan, pump and compressor controls
- Highly inductive, resistive and safety loads
- Large and small appliances (White Goods)
- Reversing induction motor controls e.g. vertical axis washing machines
- Applications subject to high temperature ($T_{j(max)} = 150\text{ °C}$)

4. Quick reference data

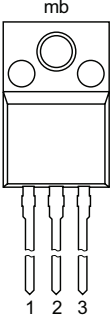

Table 1. Quick reference data

Symbol	Parameter	Conditions	Values	Unit
Absolute maximum rating				
V_{DRM}	repetitive peak off-state voltage		1000	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_h \leq 105\text{ °C}$; Fig. 1 ; Fig. 2 ; Fig. 3	8	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $t_p = 20\text{ ms}$; $T_{j(init)} = 25\text{ °C}$; Fig. 4 ; Fig. 5	100	A
		full sine wave; $t_p = 16.7\text{ ms}$; $T_{j(init)} = 25\text{ °C}$	110	A
T_j	junction temperature		150	°C

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
I_{GT}	gate trigger current	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G+ $T_j = 25\text{ }^\circ\text{C}$; Fig. 7	5	-	35	mA
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G- $T_j = 25\text{ }^\circ\text{C}$; Fig. 7	5	-	35	mA
		$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2- G- $T_j = 25\text{ }^\circ\text{C}$; Fig. 7	5	-	35	mA
I_H	holding current	$V_D = 12\text{ V}$; $T_j = 25\text{ }^\circ\text{C}$; Fig. 9	-	-	40	mA
V_T	on-state voltage	$I_T = 10\text{ A}$; $T_j = 25\text{ }^\circ\text{C}$; Fig. 10	-	1.21	1.6	V
Dynamic characteristics						
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 670\text{ V}$; $T_j = 125\text{ }^\circ\text{C}$; ($V_{DM} = 67\%$ of V_{DRM}); exponential waveform; gate open circuit	1500	-	-	V/ μs
		$V_{DM} = 670\text{ V}$; $T_j = 150\text{ }^\circ\text{C}$; ($V_{DM} = 67\%$ of V_{DRM}); exponential waveform; gate open circuit	1000	-	-	V/ μs
dI_{com}/dt	rate of change of commutating current	$V_D = 400\text{ V}$; $T_j = 150\text{ }^\circ\text{C}$; $I_{T(RMS)} = 8\text{ A}$; $dV_{com}/dt = 20\text{ V}/\mu\text{s}$; gate open circuit; snubberless condition	12	-	-	A/ms
		$V_D = 400\text{ V}$; $T_j = 150\text{ }^\circ\text{C}$; $I_{T(RMS)} = 8\text{ A}$; $dV_{com}/dt = 10\text{ V}/\mu\text{s}$; gate open circuit	15	-	-	A/ms
		$V_D = 400\text{ V}$; $T_j = 150\text{ }^\circ\text{C}$; $I_{T(RMS)} = 8\text{ A}$; $dV_{com}/dt = 1\text{ V}/\mu\text{s}$; gate open circuit	20	-	-	A/ms

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		
2	T2	main terminal 2		
3	G	gate		
mb	n.c.	mounting base; isolated		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BTA408X-1000C0T	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack"	SOT186A

7. Marking

Table 4. Marking codes

Type number	Marking codes
BTA408X-1000C0T	BTA408X-1000C0T

8. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Values	Unit
V_{DRM}	repetitive peak off-state voltage		1000	V
$I_{\text{T(RMS)}}$	RMS on-state current	full sine wave; $T_h \leq 105^\circ\text{C}$; Fig. 1 ; Fig. 2 ; Fig. 3	8	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $t_p = 20\text{ ms}$; $T_{\text{j(init)}} = 25^\circ\text{C}$; Fig. 4 ; Fig. 5	100	A
		full sine wave; $t_p = 16.7\text{ ms}$; $T_{\text{j(init)}} = 25^\circ\text{C}$	110	A
I^2t	I^2t for fusing	$t_p = 10\text{ms}$; sine wave	50	A^2s
dI_{T}/dt	rate of rise of on-state current	$I_{\text{G}} = 70\text{mA}$	100	$\text{A}/\mu\text{s}$
I_{GM}	peak gate current		2	A
P_{GM}	peak gate power		5	W
$P_{\text{G(AV)}}$	average gate power	over any 20 ms period	0.5	W
T_{stg}	storage temperature		-40 to 150	$^\circ\text{C}$
T_{j}	junction temperature		150	$^\circ\text{C}$
V_{pp}	peak pulse voltage	$T_{\text{j}} = 25^\circ\text{C}$; non-repetitive, off-state; ten pulses on each voltage polarity; 20s or more between successive pulses	2	kV

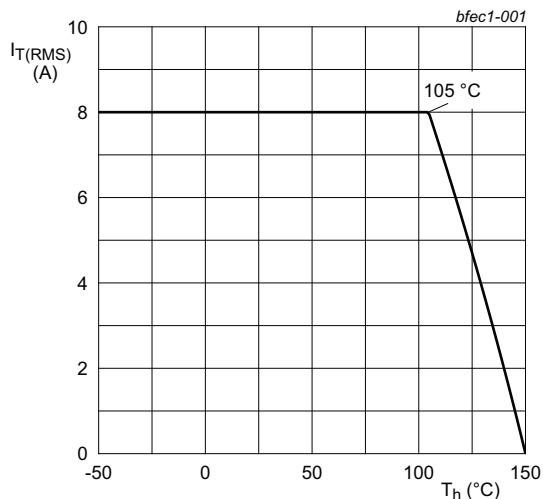
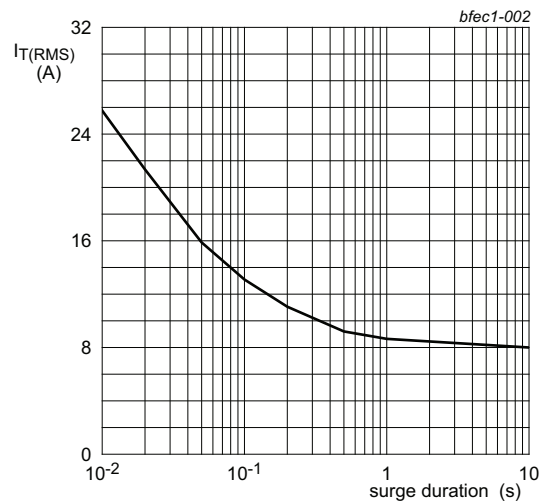


Fig. 1. RMS on-state current as a function of heatsink temperature; maximum values



$f = 50\text{Hz}$; $T_h = 105^\circ\text{C}$

Fig. 2. RMS on-state current as a function of surge duration; maximum values

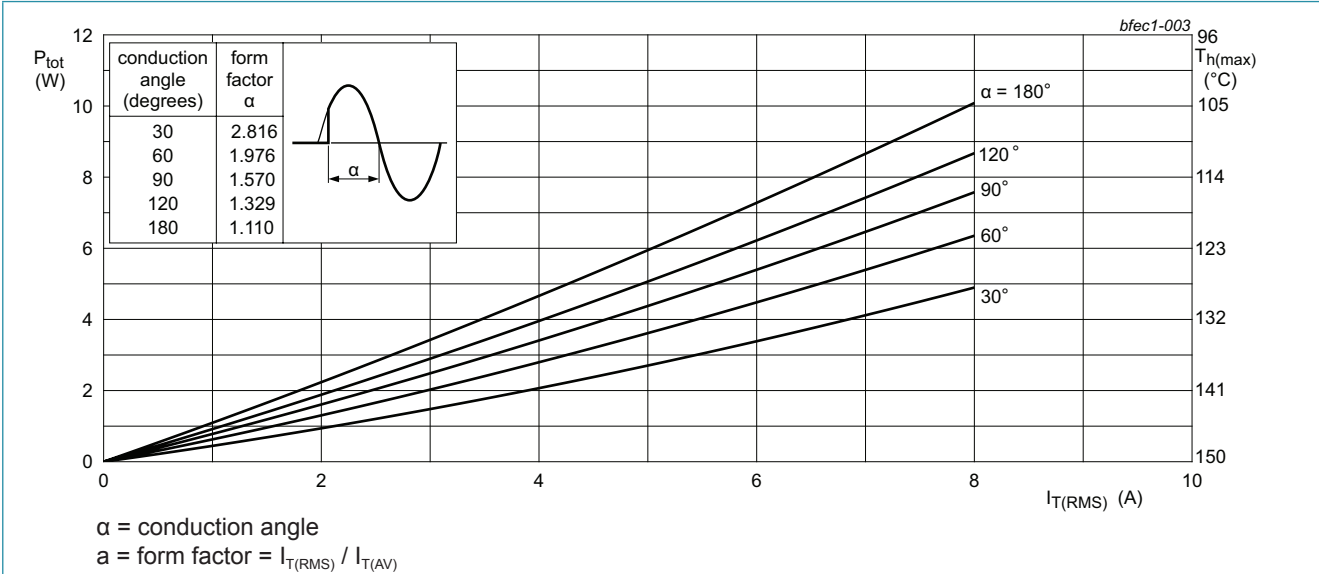


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

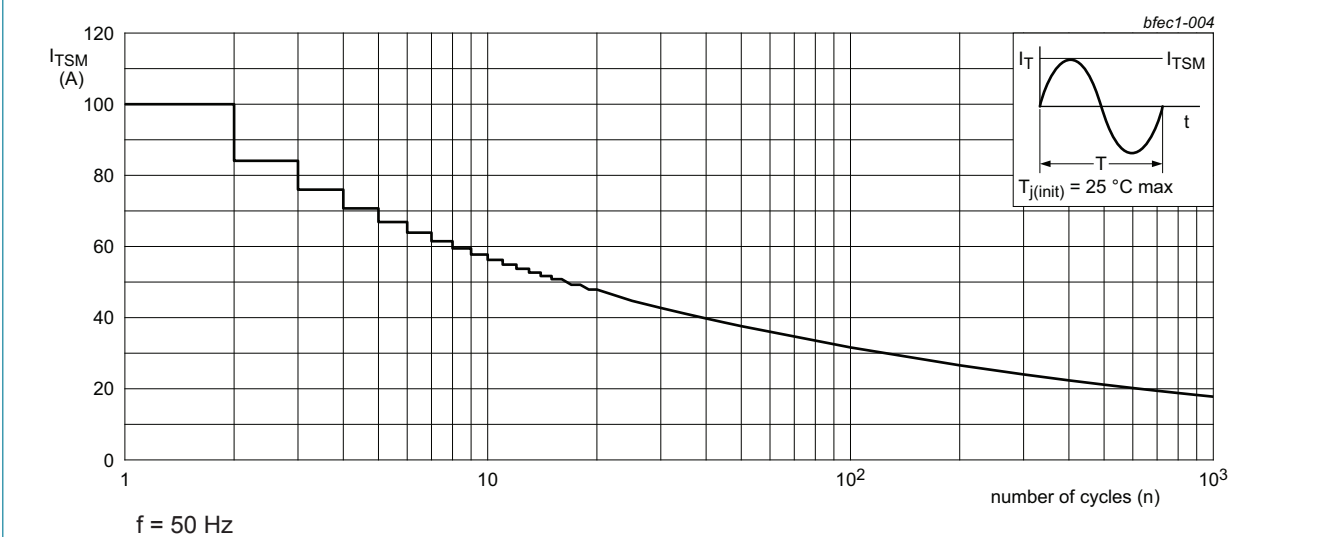


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

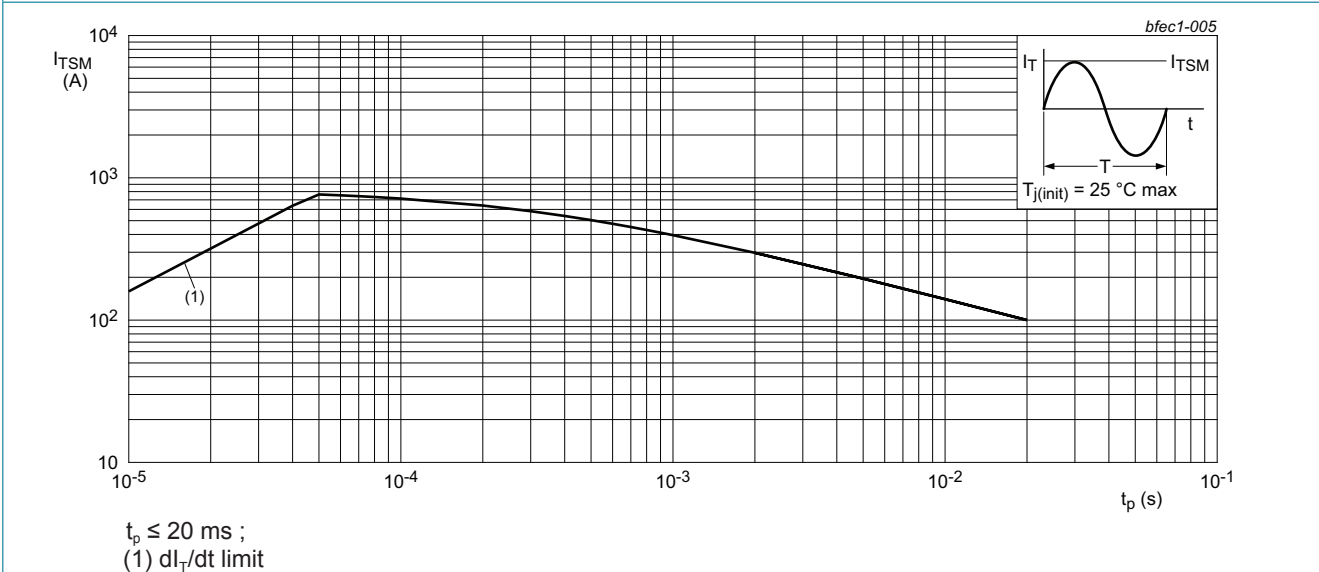


Fig. 5. Total power dissipation as a function of RMS on-state current; maximum values

9. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-h)}$	thermal resistance from junction to heatsink	with heatsink compound; Fig. 6	-	-	4.5	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	in free air	-	55	-	K/W

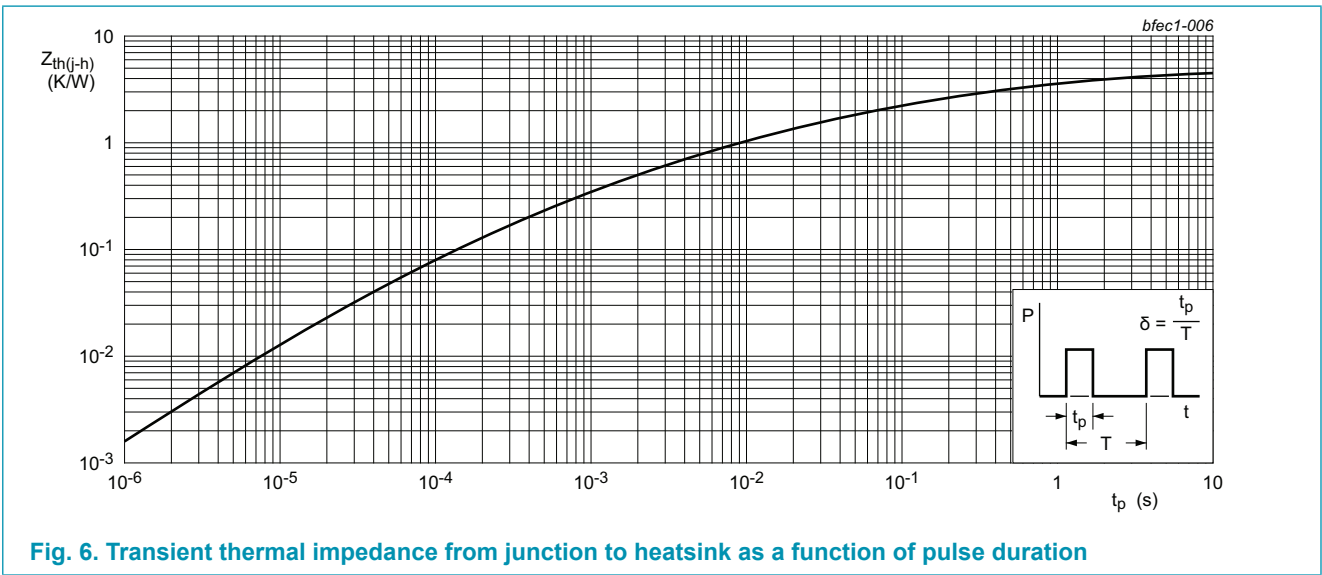


Fig. 6. Transient thermal impedance from junction to heatsink as a function of pulse duration

10. Isolation characteristics

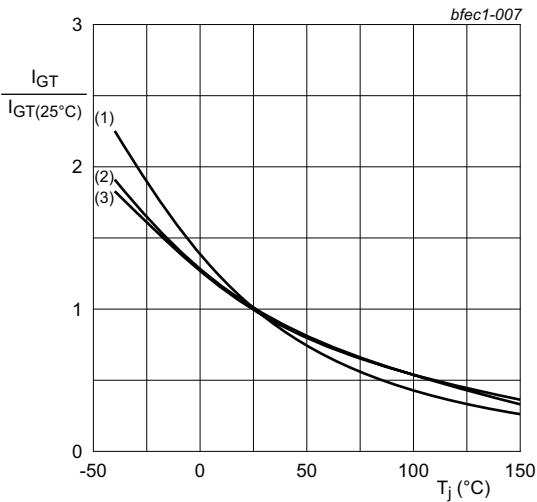
Table 6. Isolation characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{isol(RMS)}$	RMS isolation voltage	50 Hz ≤ f ≤ 60 Hz; RH ≤ 65 %; from all pins to external heatsink; sinusoidal waveform; clean and dust free	-	-	2500	V
C_{isol}	isolation capacitance	from cathode to external heatsink	-	10	-	PF

11. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _J = 25 °C; Fig. 7		5	-	35	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _J = 25 °C; Fig. 7		5	-	35	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _J = 25 °C; Fig. 7		5	-	35	mA
I _L	latching current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _J = 25 °C; Fig. 8		-	-	50	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _J = 25 °C; Fig. 8		-	-	70	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _J = 25 °C; Fig. 8		-	-	50	mA
I _H	holding current	V _D = 12 V; T _J = 25 °C; Fig. 9		-	-	40	mA
V _T	on-state voltage	I _T = 10 A; T _J = 25 °C; Fig. 10		-	1.21	1.6	V
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _J = 25 °C; Fig. 11		-	0.7	1	V
		V _D = 400 V; I _T = 0.1 A; T _J = 150 °C; Fig. 11		0.25	0.4	-	V
I _D	off-state current	V _D = 1000 V; T _J = 25 °C		-	-	10	μA
		V _D = 1000 V; T _J = 150 °C		-	0.4	2	mA
Dynamic characteristics							
dV _D /dt	rate of rise of off-state voltage	V _{DM} = 670 V; T _J = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit		1500	-	-	V/μs
		V _{DM} = 670 V; T _J = 150 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit		1000	-	-	V/μs
dI _{com} /dt	rate of change of commutating current	V _D = 400 V; T _J = 150 °C; I _{T(RMS)} = 8 A; dV _{com} /dt = 20 V/μs; gate open circuit; snubberless condition		12	-	-	A/ms
		V _D = 400 V; T _J = 150 °C; I _{T(RMS)} = 8 A; dV _{com} /dt = 10 V/μs; gate open circuit		15	-	-	A/ms
		V _D = 400 V; T _J = 150 °C; I _{T(RMS)} = 8 A; dV _{com} /dt = 10 V/μs; gate open circuit		20	-	-	A/ms



(1) T2- G-
(2) T2+ G-
(3) T2+ G+

Fig. 7. Normalized gate trigger current as a function of junction temperature

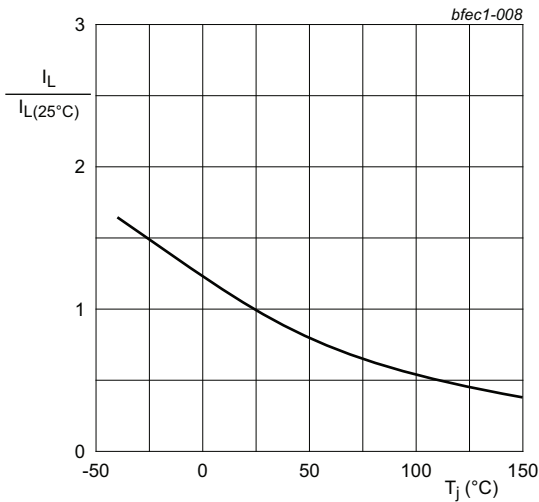


Fig. 8. Normalized latching current as a function of junction temperature

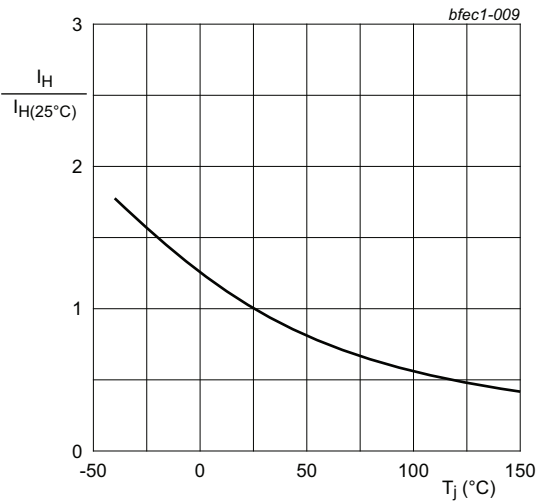
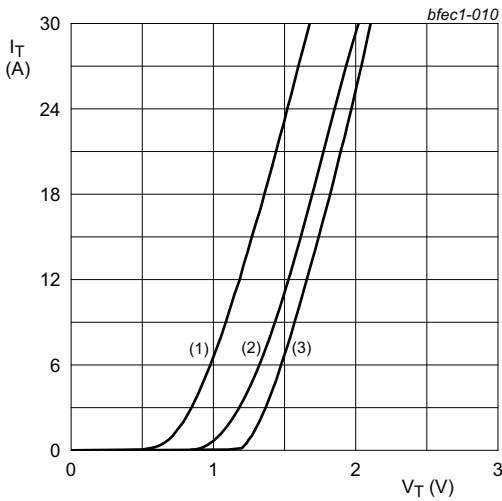


Fig. 9. Normalized holding current as a function of junction temperature



$V_o = 1.188 \text{ V}$; $R_s = 0.0237 \Omega$

(1) $T_J = 150^\circ\text{C}$; typical values
(2) $T_J = 150^\circ\text{C}$; maximum values
(3) $T_J = 25^\circ\text{C}$; maximum values

Fig. 10. On-state current as a function of on-state voltage

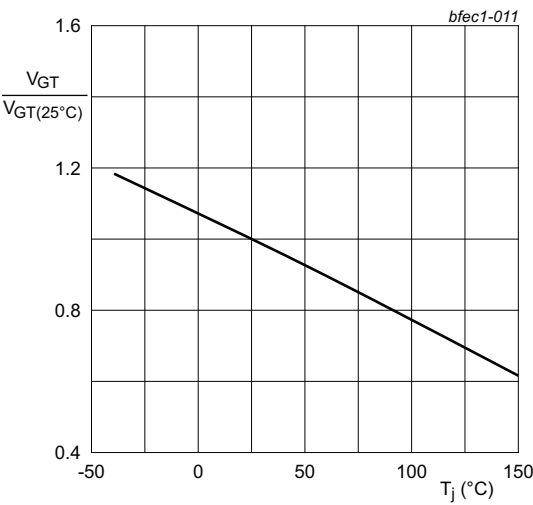


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

13. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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