



- 1 MATES WITH XFP-MSA COMPLIANT TRANSCEIVERS.
- 2 DATUM -X- AND -Y- ESTABLISHED BY CUSTOMER.
- 3 DATUM -A- IS TOP SURFACE OF HOST BOARD.
- 4 INDICATED SURFACES TO BE CONDUCTIVE AND CONNECTED TO CHASSIS GROUND.
- 5 THE PART IS PRELIMINARY
- 6 THE SELECTIVE DIMENSION 6.1 MAX ONLY FOR CUSTOMER HUAWEI

LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
ES	00		F	REVISED	03DEC2012	DZ	AC
			G	REVISED AS PER ECN-23-243336	15DEC2023	-	-

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN D. STAHL 14AUG02	STE TE Connectivity			
DIMENSIONS: mm		CHK J. KOPPENHEFFER 15JAN04				
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD M. WALMSLEY 15JAN04	NAME CAGE ASSEMBLY, 10 GIGABIT, XFP			
0 PLC ±.1 1 PLC ±.01 2 PLC ±.001 3 PLC ±.0001 4 PLC ANGLES ±.0001 FINISH		PRODUCT SPEC 108-2127 APPLICATION SPEC 114-13096 WEIGHT -	SIZE A200779		DRAWING NO C-1489951	RESTRICTED TO -
MATERIAL -		Customer Drawing		SCALE 4:1	SHEET 1 OF 3	REV G



