



### N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BV <sub>DSS</sub>	RDS(ON) Max	I <sub>D</sub> T <sub>A</sub> = +25°C
20V	$24m\Omega$ @ $V_{GS} = 4.5V$	6.7A
200	$32mΩ @ V_{GS} = 2.5V$	5.8A

## **Description and Applications**

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Backlighting

## **Features and Benefits**

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

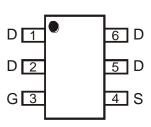
### **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe.
  Solderable per MIL-STD-202, Method 208 <sup>©3</sup>
- Weight: 0.013 grams (Approximate)

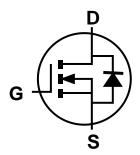
#### TSOT26



Top View



Top View Pin Configuration



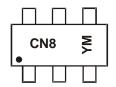
**Equivalent Circuit** 

### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2040UVT-7	TSOT26	3,000/Tape & Reel
DMN2040UVT-13	TSOT26	10,000/Tape & Reel

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# Marking Information



CN8 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	201	8	2019		2020	20	21	2022		2023	2	2024
Code	F		G		Н		I	J		K		L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	20	V
Gate-Source Voltage	$V_{GSS}$	±8	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	Ι <sub>D</sub>	6.7 5.3	А
Maximum Body Diode Forward Current (Note 6)	I <sub>S</sub>	1.2	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	40	Α

# Thermal Characteristics ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		$P_{D}$	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ heta JA}$	104	°C/W
Total Power Dissipation (Note 6)	·	P <sub>D</sub>	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ heta JA}$	77	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		1	μA	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V		
Gate-Source Leakage	I <sub>GSS</sub>	_		±100	nA	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	$V_{GS(TH)}$	0.4		1.5	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$		
Static Drain-Source On-Resistance			18	24	mΩ	$V_{GS} = 4.5V, I_D = 6.2A$		
Static Diam-Source On-Resistance	R <sub>DS(ON)</sub>		24	32	11177	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 5.2A		
Diode Forward Voltage	$V_{SD}$		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.3A$		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	Ciss	_	667			101/11/		
Output Capacitance	Coss		91	_	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	83			= 1.0ivii iz		
Gate Resistance	$R_g$	_	1.2		Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$		
Total Gate Charge	Qg	_	7.5	_		$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$		
Gate-Source Charge	Qgs	_	0.8	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$		
Gate-Drain Charge	Q <sub>gd</sub>	_	2.5	_		$I_D = 8.2A$		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	3.9	_	-			
Turn-On Rise Time	t <sub>R</sub>		5.1		ns	$V_{DD} = 10V, V_{GS} = 4.5V,$		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	21		lio	$R_L = 10\Omega$ , $R_g = 6\Omega$		
Turn-Off Fall Time	t <sub>F</sub>	_	9.4	_				
Reverse Recovery Time	t <sub>RR</sub>	_	12	_	ns	1 5 0 \ d:/dt 400 \ \ / \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
Reverse Recovery Charge	Q <sub>RR</sub>	_	3.4		nC	I <sub>F</sub> = 5.0A, di/dt = 100A/μs		

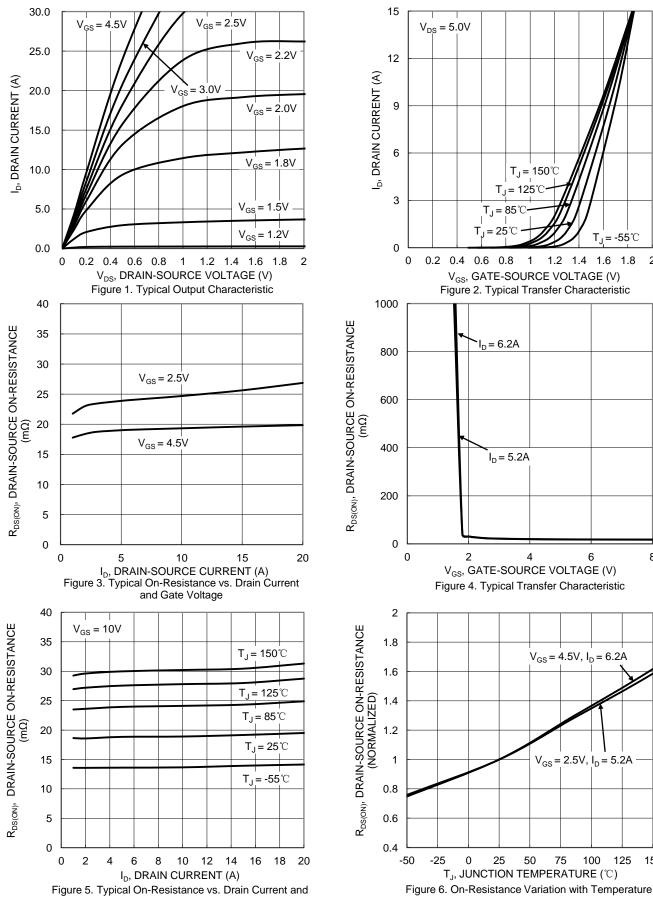
Notes:

- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
  Short duration pulse test used to minimize self-heating effect.
  Guaranteed by design. Not subject to production testing.

T₁= -55°C

1.6 1.8





Temperature

125





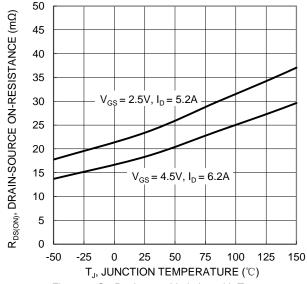
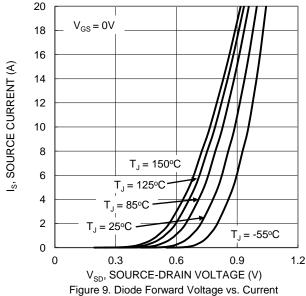
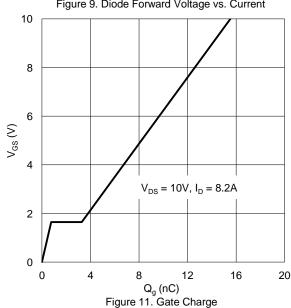


Figure 7. On-Resistance Variation with Temperature





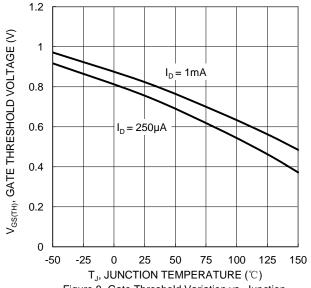


Figure 8. Gate Threshold Variation vs. Junction Temperature

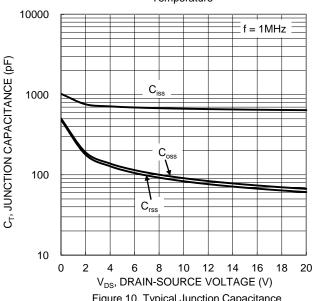
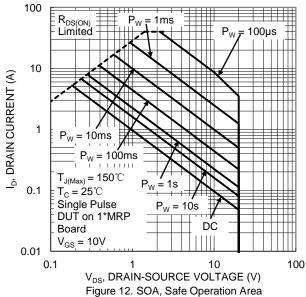


Figure 10. Typical Junction Capacitance





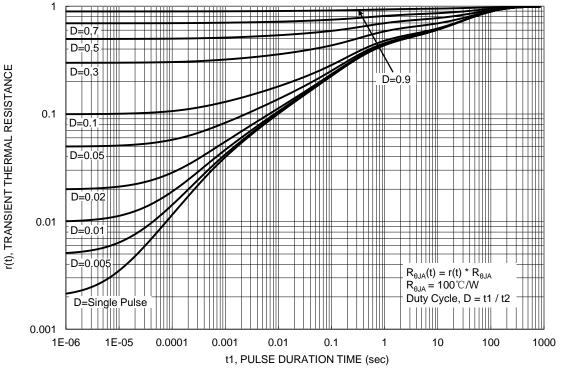


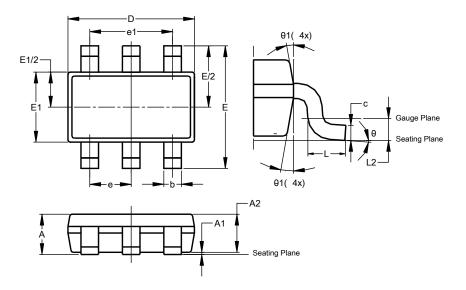
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### TSOT26

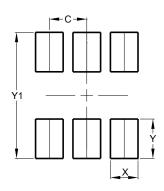


	TSOT26							
Dim	Min	Max	Тур					
Α	-	1.00	-					
A1	0.010	0.100	-					
A2	0.840	0.900	-					
D	2.800	3.000	2.900					
Е	2	2.800 BSC						
E1	1.500	1.700	1.600					
b	0.300	0.450	-					
С	0.120	0.200	-					
е	0.950 BSC							
e1	1.900 BSC							
L	0.30	0.50	-					
L2	0.250 BSC							
θ	0°	8°	4°					
θ1	4°	12°	-					
All Dimensions in mm								

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199



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