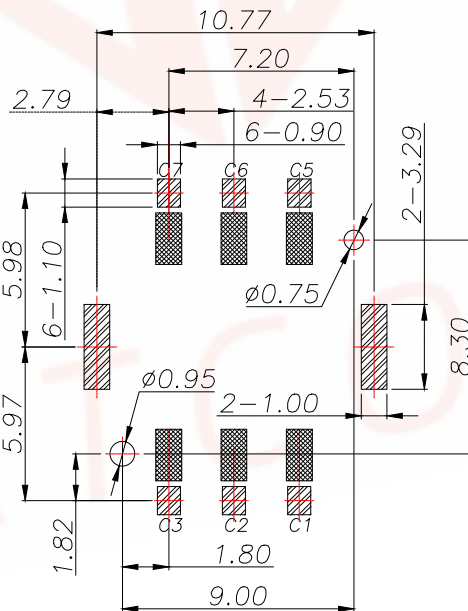
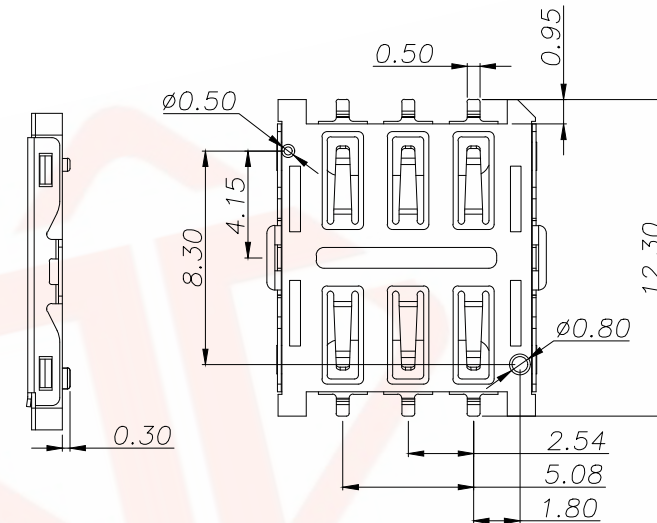
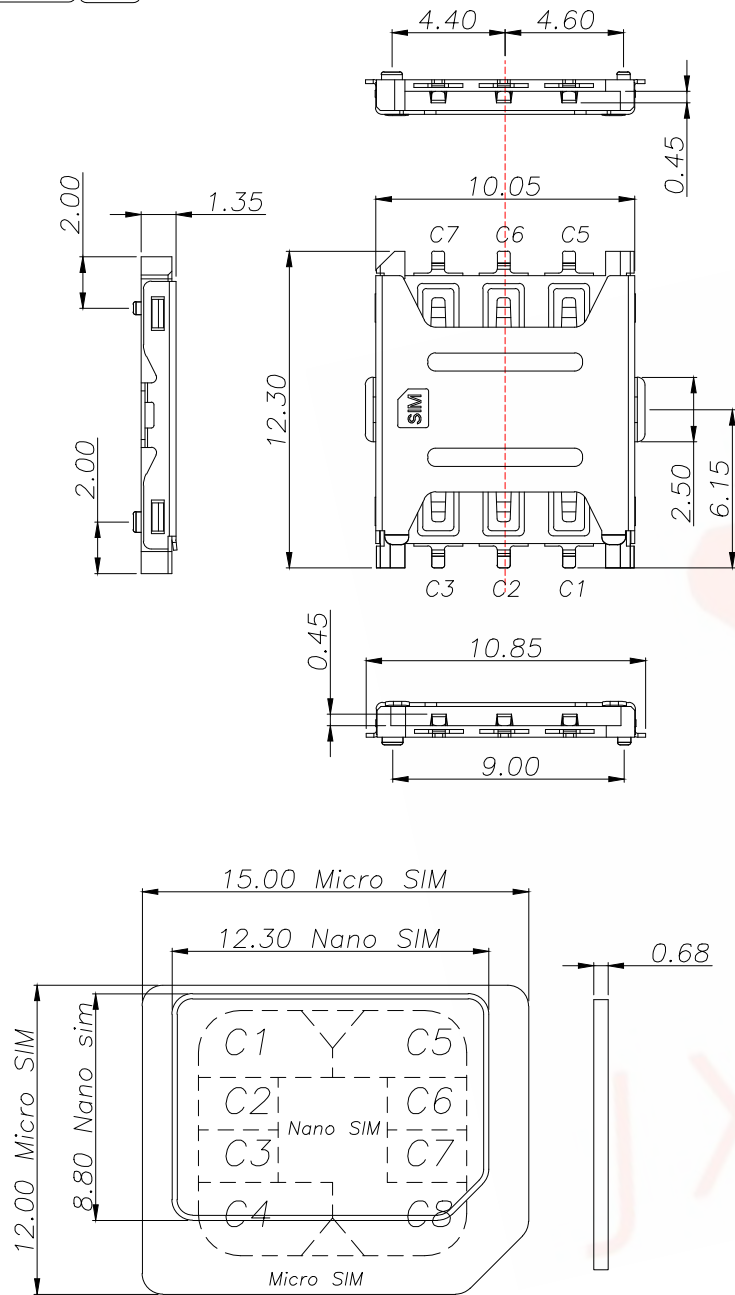


REV.	ECN NO.	DESCRIPTION	DATE	DESIGN



PAD AREA

KEEP OUT AREA

RECOMMENDED PCB LAYOUT

GENERAL TOLERANCE ± 0.05

SPECIFICATION

MATERIAL:

INSULATOR: HIGH TEMPERATURE THERMOPLASTIC, UL 94V-0.

CONTACT: C5210

SHELL: SUS

PLATING:

CONTACT: PLATED 50U" NI OVERALL CONTACT ALL AU 1U

SHELL: PLATED 50U" NI OVERALL, PAD AU 1U

ELECTRICAL:

CURRENT RATING: 0.5MA AC/DC MAX

VOLTAGE RATING: 125V AC/DC

AMBIENT TEMPERATURE RANGE: $-20^{\circ}\text{C} \sim +60^{\circ}\text{C}$

STORAGE TEMPERATURE RANGE: $-40^{\circ}\text{C} \sim +70^{\circ}\text{C}$

AMBIENT HUMIDITY RANGE: 95% R.H. MAX.

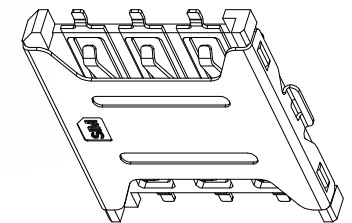
CONTACT RESISTANCE: 100M Ω MAX.

INSULATION RESISTANCE: 1000M Ω MIN./500VDC

REFLOW PEAK TEMP: $260^{\circ}\text{C} \pm 5^{\circ}\text{C}$, 3~5 S

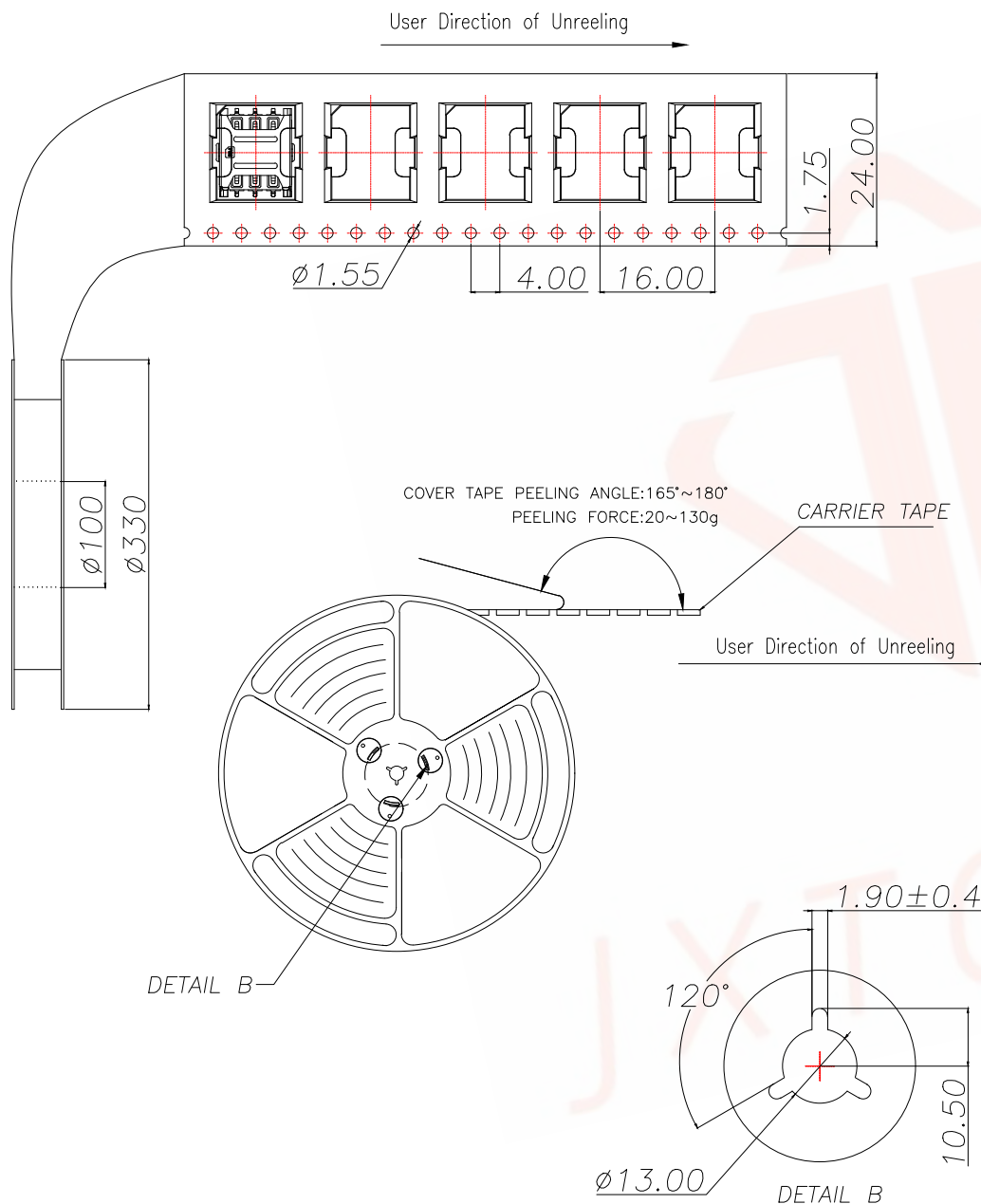
MATING CYCLES: 3,000 INSERTIONS MIN

NANO SIM CARD	
Pin No.	NAME
C1	VCC OF SIM
C2	RST OF SIM
C3	CLK OF SIM
C5	GND OF SIM
C6	VPP OF SIM
C7	I/O OF SIM
C4/C8	N/A



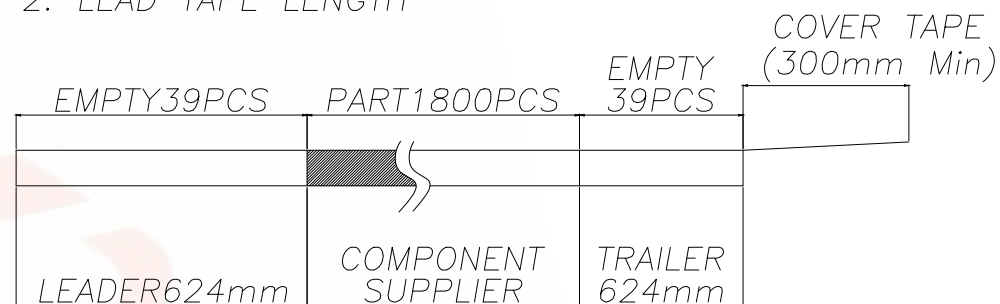
TOLERANCE		深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd	
X.: ± 0.50	X': $\pm 2^{\circ}$	PART NO.	TITLE: NANO SIM CARD
X.X: ± 0.30	X.X': $\pm 1^{\circ}$	CSIM-1151-6P	PUSH PULL 6PIN H1.35#
X.XX: ± 0.20	X.XX': $\pm 0.5^{\circ}$	DESIGN: Ethan	MANUFACTURE DWG
UNITS	mm	CHED: Olvia	SCALE
MATERILA	---	APPD: Ava	N/A
FINISH	---		SHEET
			1/1

REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

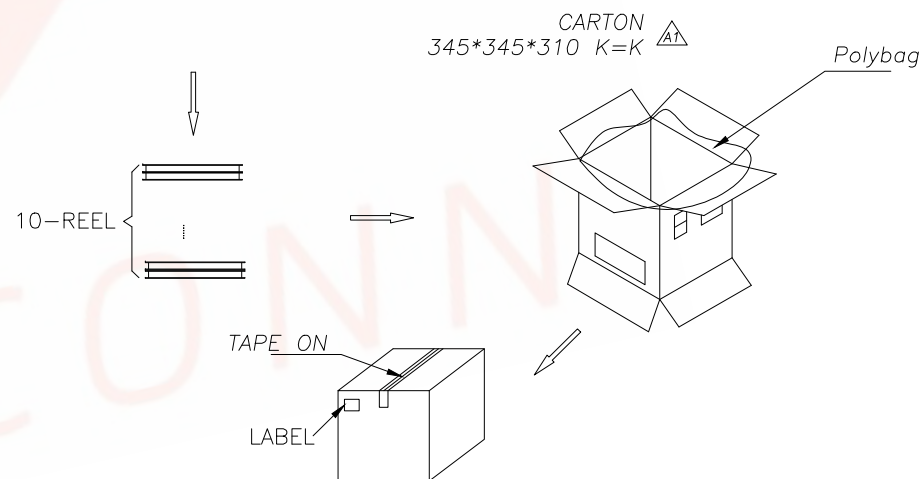


NOTES

1. NUMBER OF CONNECTORS:1800PCS/REEL
2. LEAD TAPE LENGTH



3. QUANTITY:18000PCS/CARTON



TOLERANCE		 深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd		
X.:±0.50	X.:±2°	PART NO. CSIM-1151-6P	TITLE: NANO SIM CARD PUSH PULL 6PIN H1.35#	
X.X:±0.30	X.X':±1°			
X.XX:±0.20	X.XX':±0.5°	DESIGN: Ethan	MANUFACTURE DWG	
UNITS	mm	CHED: Olvia		
MATERILA	---	APPD: Ava	 SCALE N:A	SHEET 1/1
FINISH	---			