



### 60V 175°C P-CHANNEL ENHANCEMENT MODE MOSFET

# **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
201/	$33m\Omega @ V_{GS} = -10V$	-35A
-60V	40mΩ @ V <sub>GS</sub> = -4.5V	-32A

## **Description**

This MOSFET has been designed to meet the stringent requirements of Automotive applications.

# **Applications**

It is qualified to AECQ101, supported by a PPAP and is ideal for use in:

- Engine Management Systems
- Body Control Electronics
- DCDC Converters

### **Features**

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low On-Resistance
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

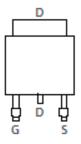
## **Mechanical Data**

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Weight: 0.33 grams (Approximate)

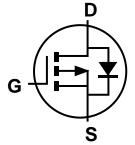
#### **TO252 (DPAK)**



Top View



Pin Out Top View



**Equivalent Circuit** 

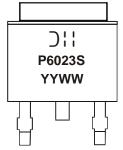
### Ordering Information (Note 4)

Part Number	Case	Packaging
DMPH6023SK3-13	TO252 (DPAK)	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

# Marking Information



Dil =Manufacturer's Marking
P6023S = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Digit of Year (ex: 15 = 2015)
WW = Week Code (01 to 53)



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			$V_{DSS}$	-60	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Compant (Nata C) V	Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ID	-35 -27	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	-7.3 -6.1	А
Pulsed Drain Current (380µs pulse, duty cycle = 1%)			I <sub>DM</sub>	-60	Α
Maximum Continuous Body Diode Forward Current (Note 6)			Is	-2.2	Α
Avalanche Current (Note 7) L = 0.1mH			I <sub>AS</sub>	-35	Α
Avalanche Energy (Note 7) L = 0.1mH			E <sub>AS</sub>	60	mJ

# **Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		$P_{D}$	2.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	80	°C/W
Total Power Dissipation (Note 6)		$P_D$	3.2	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	41	°C/W
Thermal Resistance, Junction to Case		R <sub>0</sub> JC	1.6	C/VV
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +175	°C

## Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-60	_	_	<b>V</b>	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	-	_	-1	μΑ	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.0	_	-3.0	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$	
Static Drain-Source On-Resistance	D	_	_	33	mΩ	$V_{GS} = -10V, I_{D} = -10A$	
Static Dialit-Source Off-Nesistance	R <sub>DS(ON)</sub>		_	40	11122	$V_{GS} = -4.5V, I_D = -8A$	
Diode Forward Voltage	V <sub>SD</sub>	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 9)	DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	_	2,569	_	pF		
Output Capacitance	Coss	-	179	_	pF	$V_{DS} = -30V, V_{GS} = 0V,$ - f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	1	143		рF	1 = 1.01/11/12	
Gate Resistance	$R_g$	-	5	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	$Q_{g}$	1	26.5		nC		
Total Gate Charge (V <sub>GS</sub> = -10V)	$Q_{g}$	1	53.1	_	nC	Vns = -30V. In = -5A	
Gate-Source Charge	$Q_{gs}$	-	7.1	_	nC	VDS = -30V, ID = -5A	
Gate-Drain Charge	$Q_{gd}$	_	12.6	_	nC		
Turn-On Delay Time	t <sub>D(on)</sub>	_	6	_	nS	V <sub>GS</sub> = -10V, V <sub>DS</sub> = -30V,	
Turn-On Rise Time	t <sub>r</sub>	_	7.1	_	nS		
Turn-Off Delay Time	t <sub>D(off)</sub>	_	110	_	nS	$R_G = 3\Omega$ , $I_D = -5A$	
Turn-Off Fall Time	t <sub>f</sub>	_	62	_	nS	7	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	_	20	_	nS	I <sub>F</sub> = -5A, di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	_	14	_	nC	- 1F = -5A, αι/αι = 100A/μs	

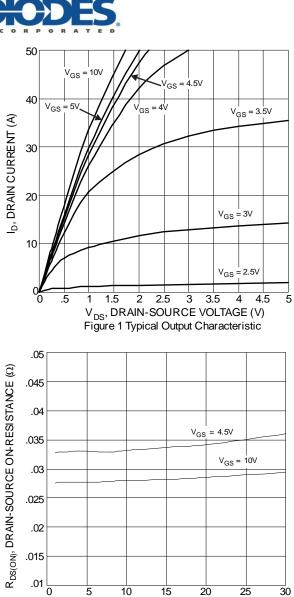
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout, see http://www.diodes.com/datasheets/ap02001.pdf Notes: for the latest version.

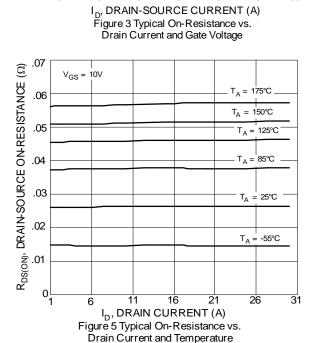
<sup>6.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.

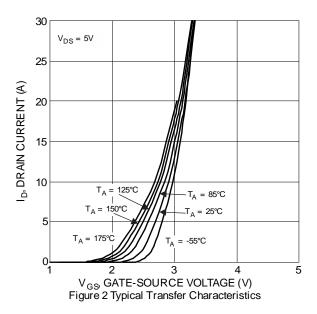
<sup>7.</sup> IAS and EAS rating are based on low frequency and duty cycles to keep  $T_J = +25^{\circ}C$  8. Short duration pulse test used to minimize self-heating effect.

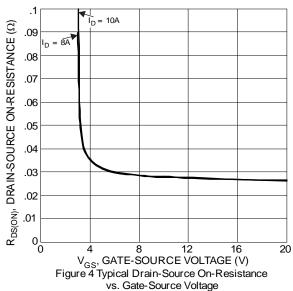
<sup>9.</sup> Guaranteed by design. Not subject to product testing.

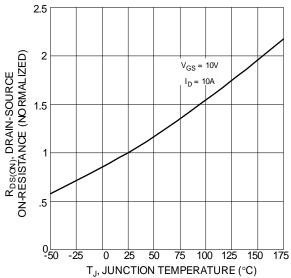




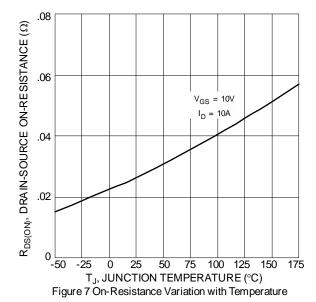


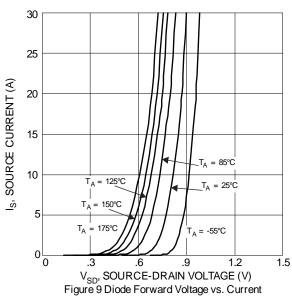


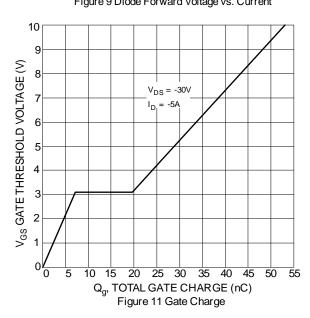












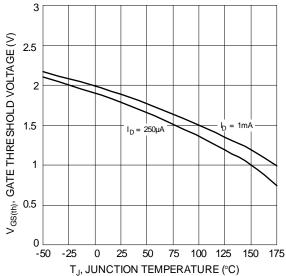


Figure 8 Gate Threshold Variation vs. Ambient Temperature

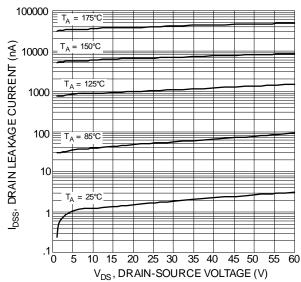
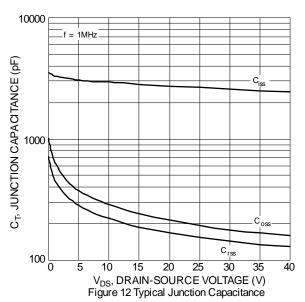
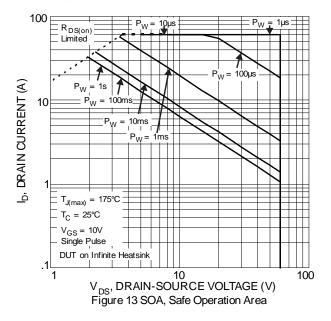
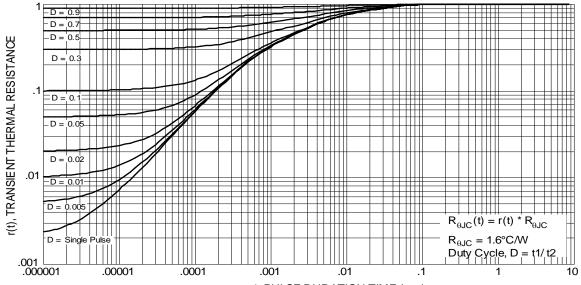


Figure 10 Typical Drain-Source Leakage Current vs. Voltage







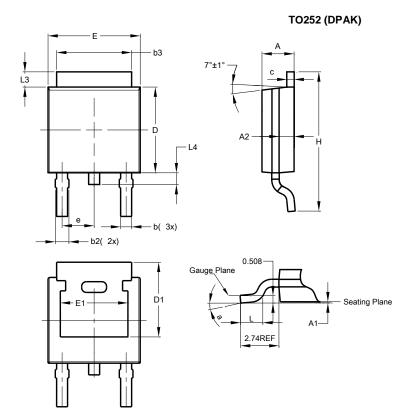


t1, PULSE DURATION TIME (sec) Figure 14 Transient Thermal Resistance



# **Package Outline Dimensions**

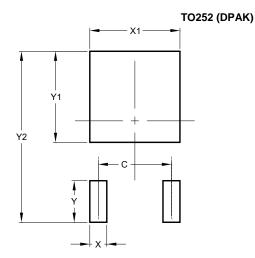
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



TO252 (DPAK)						
Dim	Min	Max	Тур			
Α	2.19	2.39	2.29			
<b>A1</b>	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
С	0.45	0.58	0.531			
D	6.00	6.20	6.10			
D1	5.21	_	_			
е	_	_	2.286			
Е	6.45	6.70	6.58			
E1	4.32	_	_			
H	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°	_			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)				
С	4.572				
Х	1.060				
X1	5.632				
Y	2.600				
Y1	5.700				
Y2	10.700				



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