

Features

- Ultra low leakage: nA level
- Operating voltage: 8V
- Low clamping voltage
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC61000-4-4 (EFT) 40A (5/50ns)
 - IEC61000-4-5 (Lightning) 8A (8/20 μs)
- RoHS Compliant

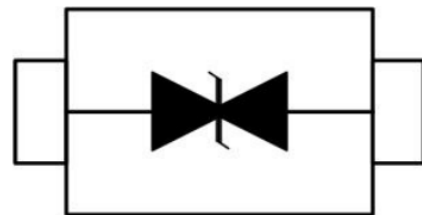
Dimensions SOD-523



Applications

- Cell Phone Handsets and Accessories
- Microprocessor based equipment
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops, and Servers
- Portable Instrumentation
- Networking and Telecom
- Serial and Parallel Ports
- Peripherals

Pin Configuration



Mechanical Characteristics

- Package: SOD-523
- Lead Finish: Lead Free
- UL Flammability Classification Rating 94V-0
- Quantity Per Reel: 3,000 pcs
- Reel Size: 7 inch
- Device Marking: 8C

Absolute Maximum Ratings (T_{amb}=25°C unless otherwise specified)

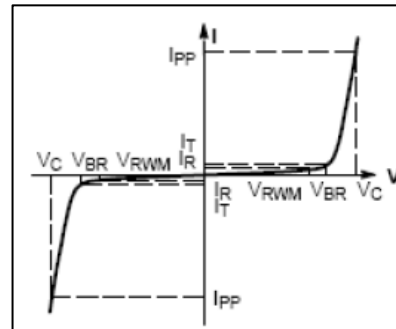
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	P _{pp}	150	W
ESD per IEC 61000-4-2 (Air)	V _{ESD}	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	T _J	-55 to +150	°C
Storage Temperature Range	T _{STJ}	-55 to +150	°C

Electrical Characteristics(TA=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				8	V
Breakdown Voltage	V_{BR}	$I_T = 1mA$	8.2	8.9	13	V
Reverse Leakage Current	I_R	$V_{RWM} = 8V$			1	μA
Clamping Voltage	V_C	$I_{PP} = 1A$ (8 x 20 μs pulse)			13	V
Clamping Voltage	V_C	$I_{PP} = 8A$ (8 x 20 μs pulse)		11	20	V
Junction Capacitance	C_J	$V_R = 0V$, $f = 1MHz$		18	30	pF

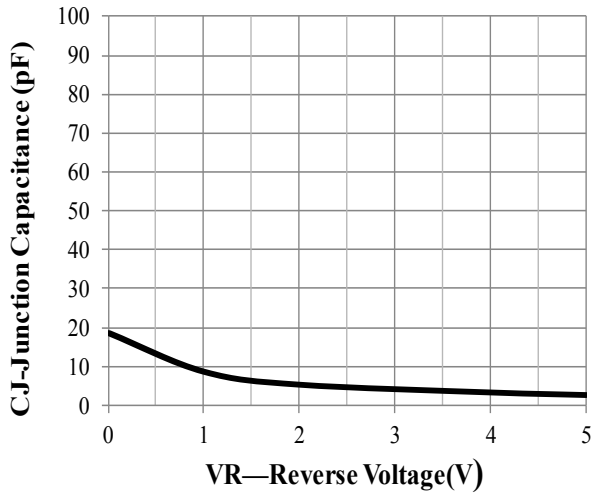
Electrical Parameters(TA=25°C unless otherwise specified)

Symbol	Parameter
V_C	Clamping Voltage @ I_{PP}
I_{PP}	Peak Pulse Current
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_R	Reverse Leakage Current @ V_{RWM}
V_{RWM}	Reverse Standoff Voltage

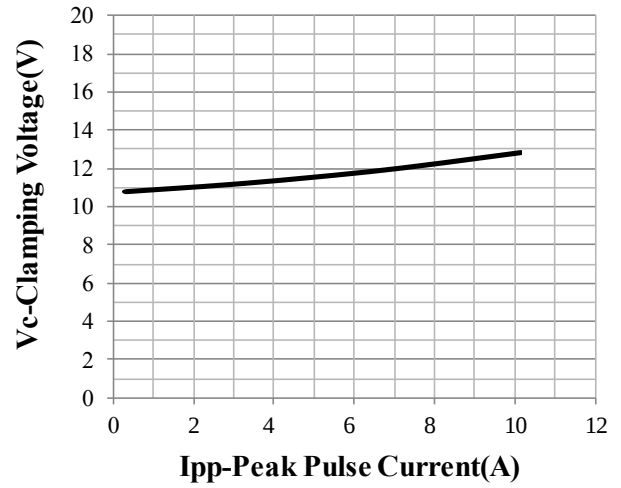


V-I characteristics for a Bi-directional TVS

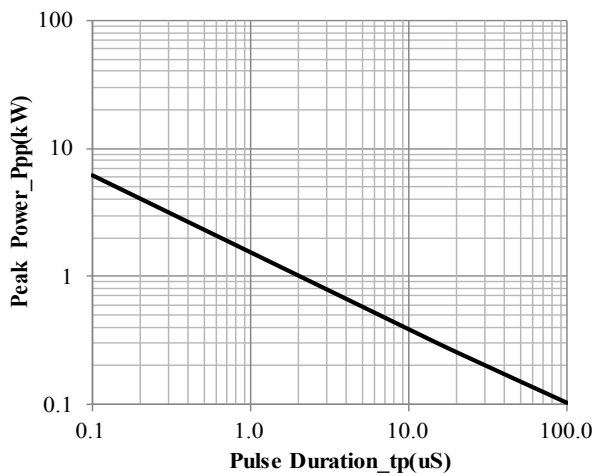
Typical Performance Characteristics (TA=25°C unless otherwise specified)



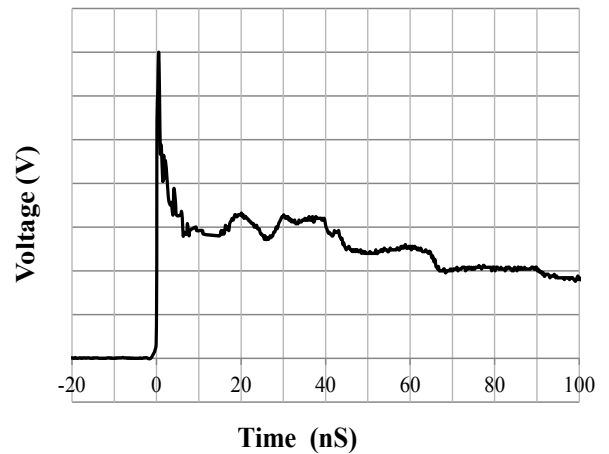
Junction Capacitance vs. Reverse Voltage



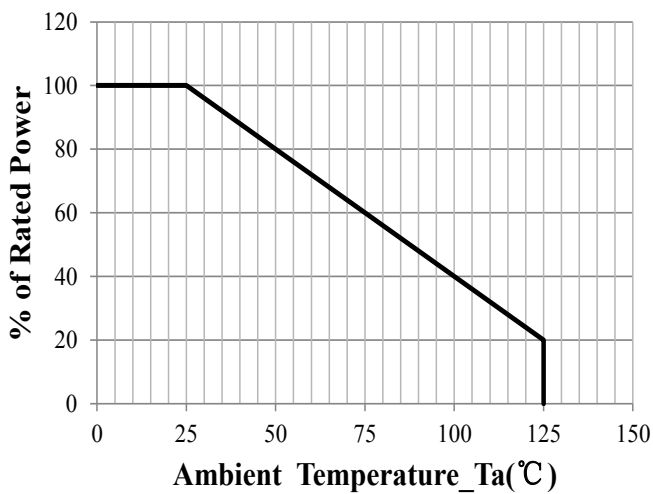
Clamping Voltage vs. Peak Pulse Current



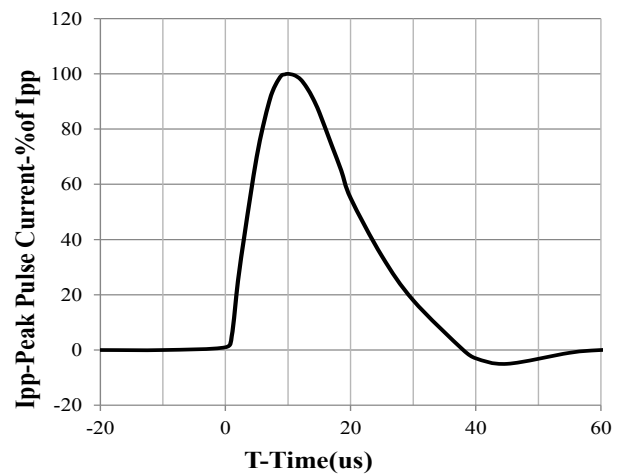
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform

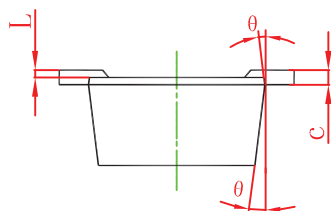
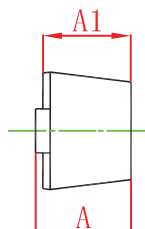
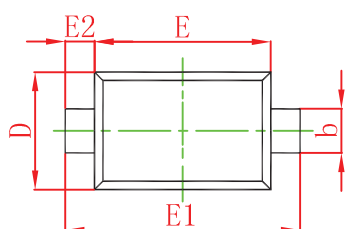


Power Derating Curve



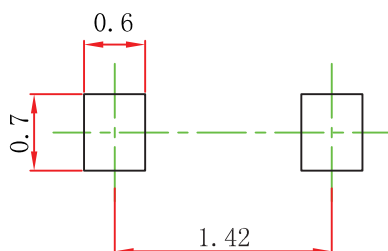
8 X 20us Pulse Waveform

SOD-523 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.510	0.770	0.020	0.031
A1	0.500	0.700	0.020	0.028
b	0.250	0.350	0.010	0.014
c	0.080	0.150	0.003	0.006
D	0.750	0.850	0.030	0.033
E	1.100	1.300	0.043	0.051
E1	1.500	1.700	0.059	0.067
E2	0.200 REF		0.008 REF	
L	0.010	0.070	0.001	0.003
θ	7° REF		7° REF	

SOD-523 Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

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