

General Description

The CMSA015N06 uses advanced SGT technology to provide excellent RDS(ON). This is suitable device for Synchronous Rectification For Server and general purpose applications.

Features

- Low On-Resistance
- 100% avalanche tested
- Small Footprint (5x6 mm) for Compact Design
- RoHS Compliant

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current	240	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current	168	A
I_{DM}	Pulsed Drain Current	960	A
EAS	Single Pulse Avalanche Energy ¹	1152	mJ
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation	200	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ($t \leq 10$ s)	---	19	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-case (Steady-State)	---	0.63	$^\circ\text{C}/\text{W}$

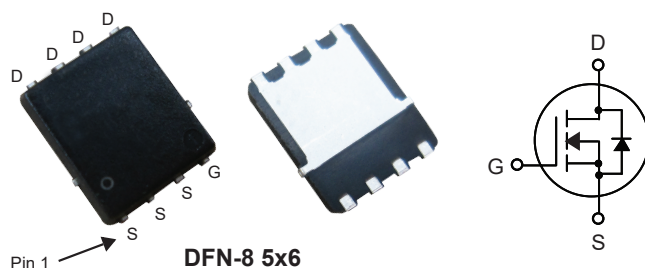
Product Summary

BVDSS	$R_{DS(on)}$ max.	ID
60V	1.5mΩ	240A

Applications

- Battery management
- High Frequency Switching and Synchronous Rectification

DFN-8 5x6 Pin Configuration



Type	Package	Marking
CMSA015N06	DFN-8 5x6	CMSA015N06

Electrical Characteristics($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	60	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V$, $I_D=50A$	---	1.3	1.5	mΩ
		$V_{GS}=4.5V$, $I_D=30A$	---	1.8	2.6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	1.0	---	3.0	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=60V$, $V_{GS}=0V$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=10V$, $I_D=20A$	---	42	---	S
R_g	Gate Resistance	$V_{DS}=0V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	1.0	---	Ω
Q_g	Total Gate Charge	$I_D=50A$ $V_{DS}=30V$ $V_{GS}=10V$ (Note 2)	---	110	---	nC
Q_{gs}	Gate-Source Charge		---	24.5	---	
Q_{gd}	Gate-Drain Charge		---	21	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=30V$ $V_{GS}=10V$ $R_{G_{ext}}=2.7\Omega$ $I_D=50A$ (Note 2)	---	17	---	ns
T_r	Rise Time		---	61	---	
$T_{d(off)}$	Turn-Off Delay Time		---	80	---	
T_f	Fall Time		---	50	---	
C_{iss}	Input Capacitance	$V_{DS}=25V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	6150	---	pF
C_{oss}	Output Capacitance		---	3300	---	
C_{rss}	Reverse Transfer Capacitance		---	150	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	240	A
I_{SM}	Pulsed Source Current		---	---	960	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=50A$, $T_J=25^{\circ}\text{C}$	---	0.82	1.4	V

Note :

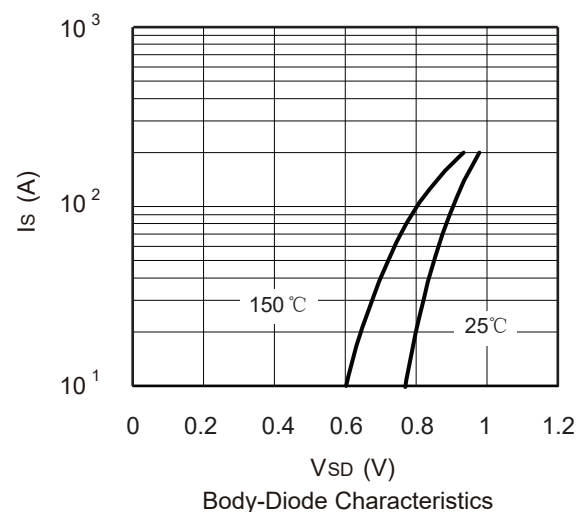
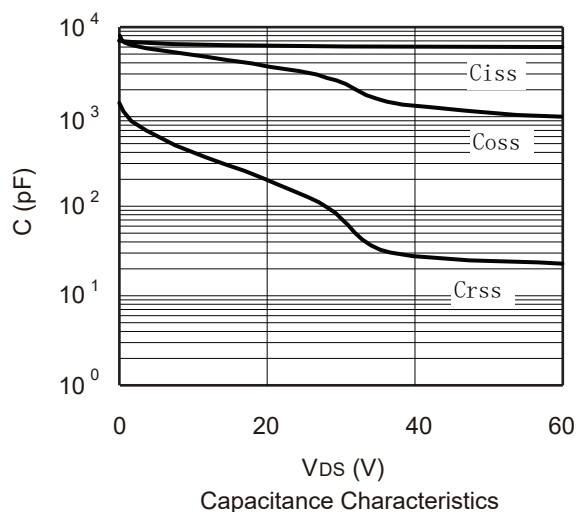
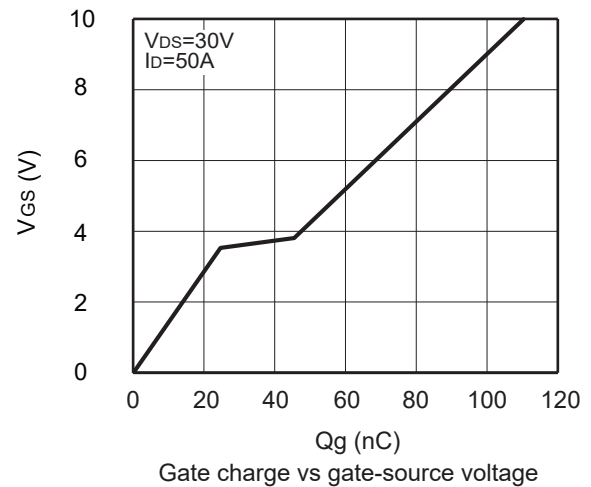
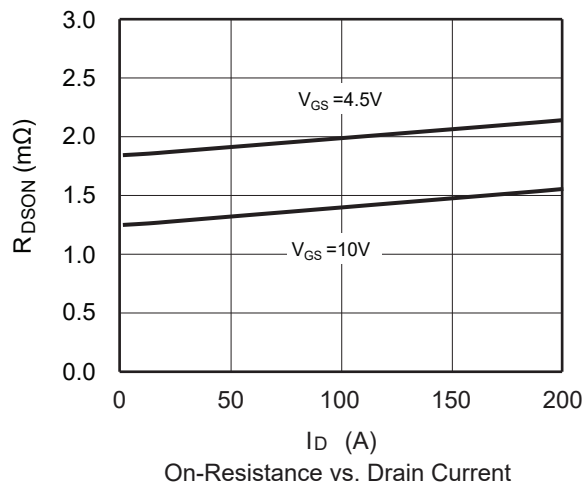
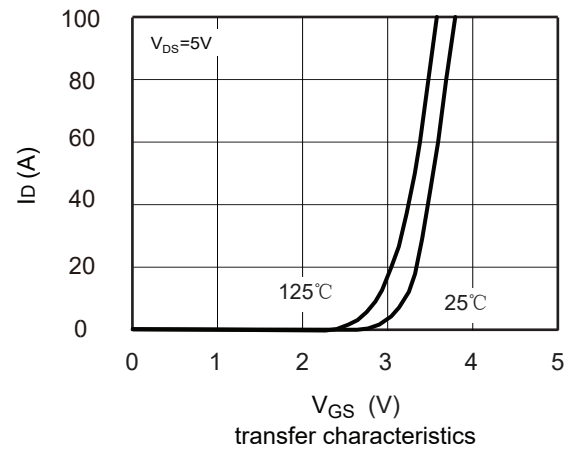
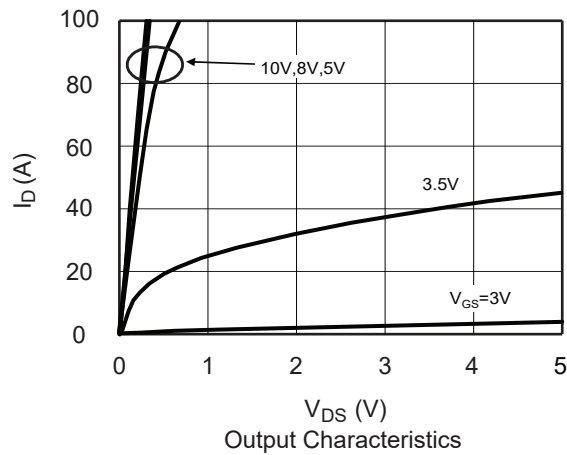
- 1.The EAS data shows Max. rating . The test condition is $V_{DD}=40V$, $V_{GS}=10V$, $L=1\text{mH}$, $I_{AS}=48A$.
- 2.Guaranteed by design, not subject to production testing.

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.Please refer to the latest version of specification.

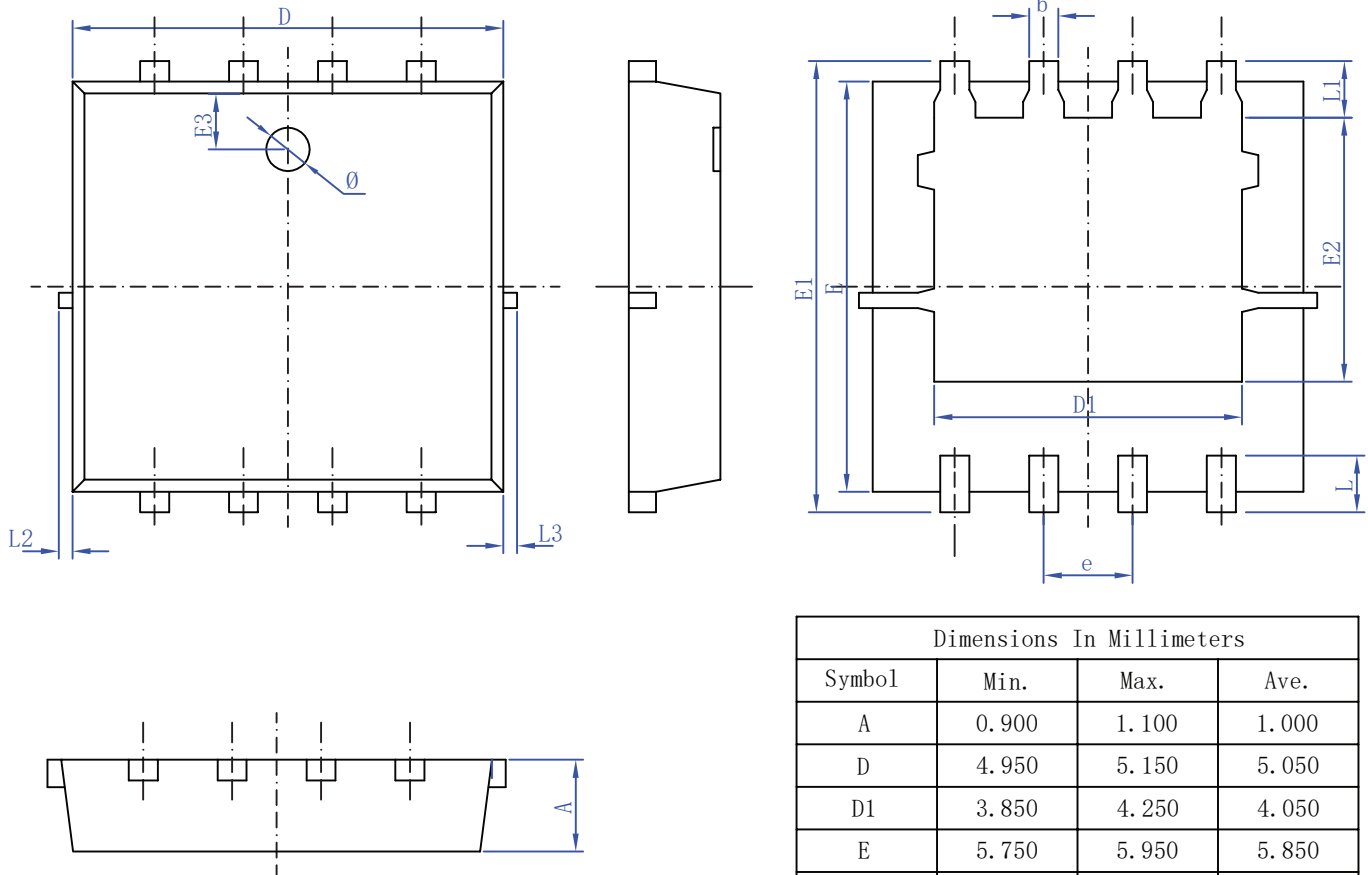
Typical Characteristics



Package Dimension

DFN-8 5x6

Unit :mm



注:

1. 未注公差 ± 0.05 未标注圆角 $R_{\max}=0.25$

Dimensions In Millimeters			
Symbol	Min.	Max.	Ave.
A	0.900	1.100	1.000
D	4.950	5.150	5.050
D1	3.850	4.250	4.050
E	5.750	5.950	5.850
E1	5.950	6.350	6.150
E2	3.300	3.700	3.500
E3	0.900	1.300	1.100
b	0.250	0.350	0.300
e	1.220	1.320	1.270
L	0.585	0.785	0.685
L1	0.525	0.725	0.625
Ø	1.000	1.400	1.200
L2	0~0.100		
L3	0~0.100		