A

Max

2.3

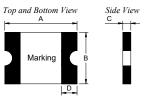
Min

2.0

1. Physical Dimensions(size of 0805)

Unit:mm

0 1111111111								
	D	Manleina						
	Min	Marking						
	0.20	4						



2. Electrical Characteristics

Part Number

KPSMD050/24

Part Number	I _H (A)	I _T (A)	V _{max} (V)	I max (A)	T _{trip} (Max time to trip) Current(A) Time(S)		Pd _{typ} (W)	$R_{min} (\Omega)$	$R1_{max}$ (Ω)
KPSMD050/24	0.50	1.00	24.0	40	8.0	0.10	0.50	0.150	0.900

Min

0.70

C

Max

1.20

I_H: Holding Current: maximum current at which the device will not trip in 25°C still air.

В

Max

1.5

Min

1.2

 $I_T\!\!:$ Tripping Current minimum current at which the device will trip in 25 $^\circ\!\!C$ $\,$ still air.

 V_{max} : Maximum voltage device can withstand without damage at rated current.

 I_{max} : Maximum fault current device can withstand without damage at rated voltage.

T trip: Maximum time to trip(s) at assigned current.

Pd_{typ}: Rated working power.

R min: Minimum resistance of device prior to trip at 25°C.

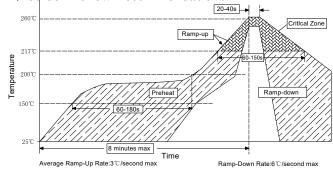
R1 max: Maximum resistance of device is measured one hours post reflow at 25°C.

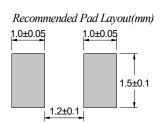
Noted: All electrical function test is conducted after PCB mounted.

3. Thermal Derating

KPSMD050/24	Maximum ambient operating temperature									
TAT GIVIDOSO/24	-40°C	-20°C	0℃	25℃	40℃	50℃	60℃	70°C	85℃	
Hold Current(A)	0.68	0.62	0.55	0.50	0.40	0.37	0.33	0.29	0.23	
Trip Current(A)	1.36	1.24	1.10	1.00	0.80	0.74	0.66	0.58	0.46	

4. Solder Reflow Recommendations





Notes:If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

5. Package Information

Packing quantity:3500PCS/Reel

Note:Reel packaging per EIA-481-1 standard