

#### **Product Features**

- Excellent Insertion Loss and Isolation performance
- High Linearity
- GPIO Control Interface
- Broadband frequency range: 0.7 to 2.7 GHz
- Small package: QFN-10 1.1mm x 1.5mm x 0.45mm
- No DC blocking capacitors required
- 1kV HBM ESD Protection on all pins

#### **Product Applications**

- Band Selecting
- Antenna Tuning

### **Product Description**

The LX8545 is a Silicon On Insulator (SOI) Single Pole, Four Throw (SP4T) antenna tuner which require very low insertion loss, high isolation and high linearity performance.

The high linearity performance and low insertion loss for 5G and 4G LTE applications.

The LX8545 is manufactured in a compact 1.1mm x 1.5mm x 0.45mm, 10-pin surface mount Quad Flat No-Lead (QFN) package.

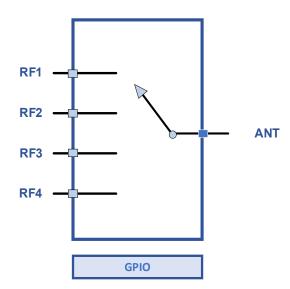


Figure 1 Functional Block Diagram



#### **Absolute Maximum Conditions**

Parameters	Symbol	Minimum	Maximum	Units
Supply voltage	$V_{DD}$		4.8	V
Control voltage	VCTL		3.3	V
RF input power	Pin		+43	dBm
Storage temperature	T <sub>STG</sub>	-55	+150	$^{\circ}$ C
Operating temperature	T <sub>OP</sub>	-40	+90	$^{\circ}$ C
Human Body Model, Class 1C	ESD	1000		V

<sup>1:</sup> Test condition 50% duty cycle, VSWR=1:1, +25 ° C

**Note:** Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

# **General Electrical Specifications**

Parameters	Symbol	Test Condition	Min.	Тур.	Max.	Units
Supply voltage	$V_{DD}$		2.5	2.8	3.6	V
Supply current, active mode	I <sub>DD</sub>			100	120	μA
Control signal:						
High	VCT		1.35	1.8	2.70	V
Low					0.35	
RF Operating Voltage	VP			60		<b>&gt;</b>
(ANT to RF1/2/3/4)	ΛÞ			00		V
Turn-on time		Measured from 50% of				
	T <sub>ON</sub>	final VDD supply voltage to		3	5	μs
(PIN = +27 dBm)		90% of RF power				
Switching time		Measured from 50% of				
(PIN = +27 dBm)	Tsw	final VDD supply voltage to		3	5	μs
(FIN - +21 UDIII)		90% of RF power				

(VDD = 2.85 V, VCT= 1.8 V, TOP = +25  $\,^{\circ}$ C, Characteristic Impedance [ZO] = 50  $\,^{\circ}$ C, Unless Otherwise Noted)



# **RF Specifications**

Parameters	Symbol	Test Condition	Min.	Тур.	Max.	Units
Operating frequency	f		0.7		2.7	GHz
		Up to 0.9 GHz		0.28	0.40	
Insertion loss	IL	Up to 1.9 GHz		0.42	0.50	dB
		Up to 2.7 GHz		0.50	0.80	
Isolation (ANT part to any		Up to 0.9 GHz	20	24		
Isolation (ANT port to any	Iso	Up to 1.9 GHz	15	19		dB
receive port)		Up to 2.7 GHz	12	15		
Jackstian (Active DE Dorte to		Up to 0.9 GHz	23	25		
Isolation (Active RF Ports to other inactive RF Ports)	I <sub>SO</sub>	Up to 1.9 GHz	17	20		dB
		Up to 2.7 GHz	14	17		
0	2fo	Pin = +26 dBm,900MHz	-73	-68		dBm
2nd Order harmonics		Pin = +35 dBm,900MHz	-63	-60		UDIII
3rd Order harmonics	3fo	Pin = +26 dBm,900MHz	-78	-68		dBm
order narmonics		Pin = +35 dBm,900MHz	-61	-59		asm
0.1 dB Compression Point	P0.1dB	000M 500		+43		dBm
50% duty cycle, VSWR=1:1	PU. IUB	900M, 50Ω		+43		UDIII
Ron		Path on		1.1	1.4	Ohm
Coff		Path off		140	160	fF



### **Truth Table**

CTL1	CTL2	ANT-RFX
0	0	ANT-RF1 on
0	1	ANT-RF2 on
1	0	ANT-RF3 on
1	1	ANT-RF4 on



### **Pin-out Information**

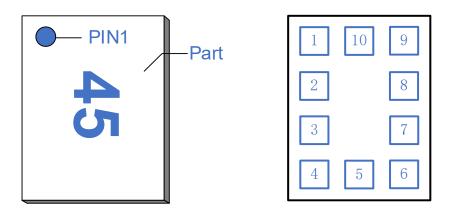


Figure 2 Pin-out Information

**Table 1. Pin Description** 

Pin#	Name	Description	Pin #	Name	Description
1	RF1	RF Port 1	6	CTL2	Control voltage2
2	RF2	RF Port 2	7	GND	Ground
3	GND	Ground	8	RF4	RF Port 4
4	VDD	Supply voltage	9	RF3	RF Port 3
5	CTL1	Control voltage1	10	ANT	Antenna in



# **Application circuit**

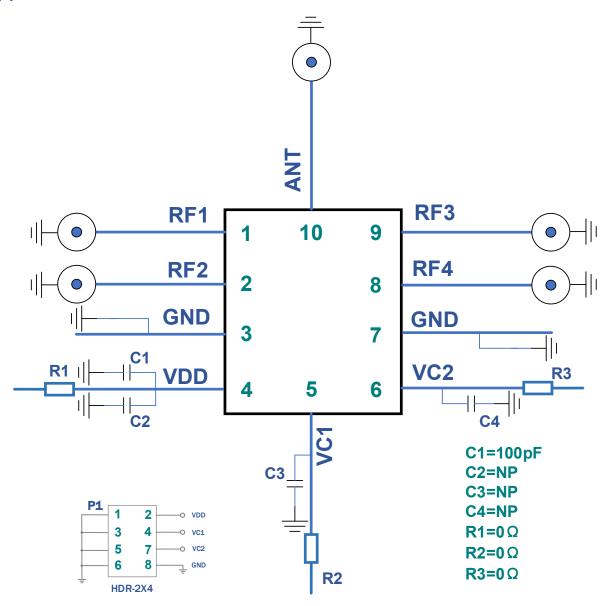


Figure 3 Application circuit



#### **Evaluation Board**

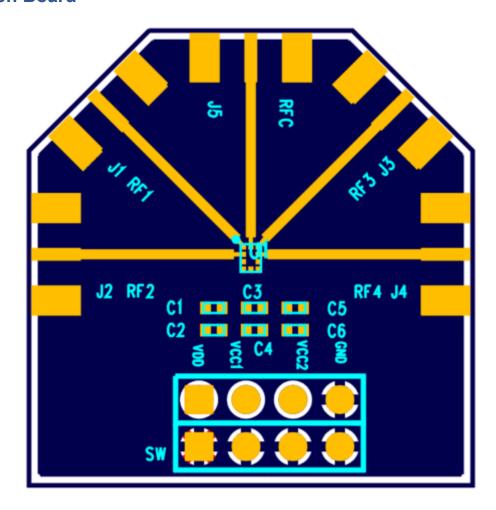


Figure 4 Evaluation Board Assembly Diagram

MILLIMETER

MAX

0.50

1.55

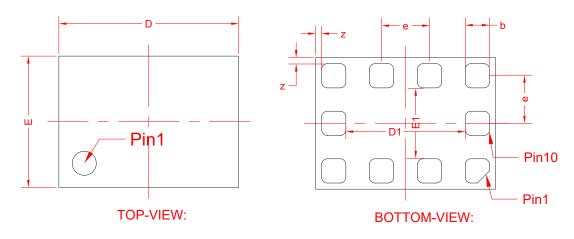
1.050

0.650

0.425



# **Package Outline Dimension**



SYMBOL

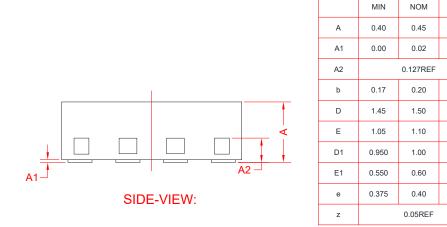
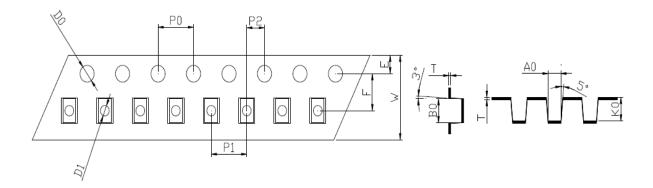


Figure 5 Package Outline Dimension



# Package Dimensions (5000pcs)



W	8.00±0.05	Т	0. 20±0. 02	D1	0.80±0.10
Е	1.75±0.10	F	3.50±0.10	DO	1.60±0.10
PO	4.00±0.10	P1	4.00±0.10	P2	2.00±0.10
AO	1.25±0.05	ВО	1.88±0.05	КО	0.75±0.05

Figure 6 Tape and Reel Dimensions

### **Declaration of No Harmful Substances**

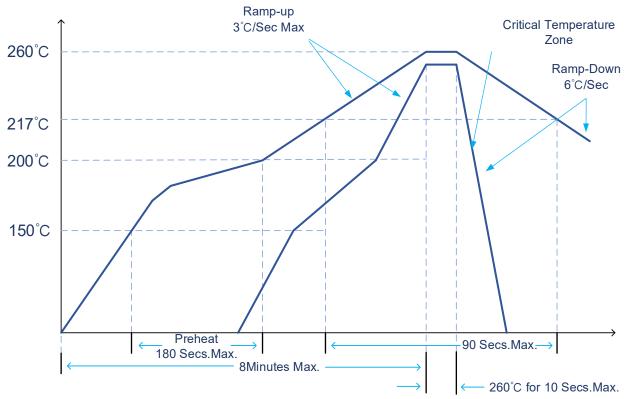
This part is compliant with 2005/20/EC packaging directive, 1907/2006/EC REACH directive and the 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment), as amended by Directive 2015/863/EU.

This product also has the following attributes:

- · Lead free
- Halogen Free (Chlorine, Bromine)
- SVHC Free



### **Reflow Chart**



NOTE: Reflow Profile with 240°C peak also acceptable.