MSKSEMI 美森科













ESD

T\/C

TSS

MOV

GDT

PLED

TPD4E001DBVR-MS

Product specification





Features

- 150Watts peak pulse power (tp = 8/20µs)
- SOT-23-6 package
- Solid-state silicon-avalanche technology
- Low clamping voltage
- Low leakage current
- Low capacitance (Cj=0.3pF typ. IO to IO)
- Protection one data/power line to:
- IEC 61000-4-2 ±12kV contact ±15kV air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 5A (8/20µs)

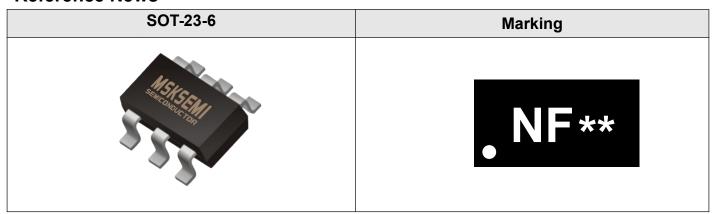
Mechanical Data

- SOT23-6 package
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel
- RoHS/WEEE Compliant

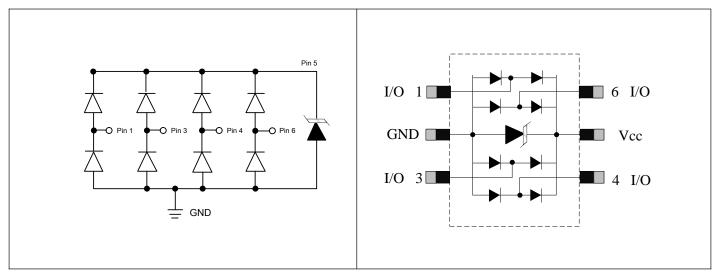
Applications

- Ethernet
- Digital Visual Interface (DVI)
- USB2.0
- Notebook and PC Computers

Reference News



Schematic & PIN Configuration





Absolute Maximum Rating

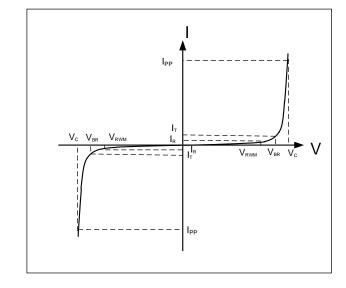
Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20μs)	P _{PP}	150	Watts
Peak Pulse Current (t _p =8/20μs) (note1)	I _{pp}	5.0	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	15 12	kV
Lead Soldering Temperature	T∟	260(10seconds)	$^{\circ}$
Junction Temperature	TJ	-55 to + 125	$^{\circ}$
Storage Temperature	T_{stg}	-55 to + 125	°C

Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V_{RWM}				5.0	V
Reverse Breakdown Voltage	V_{BR}	I _T =1mA	6			V
Reverse Leakage Current	I _R	V _{RWM} =5V,T=25℃			1.0	uA
Peak Pulse Current	I_{PP}	tp =8/20μs			5.0	А
Clamping Voltage	Vc	I _{PP} =5.0A,t _p =8/20µs			16	V
Junction Capacitance	C _j	V _R = 0V, f= 1MHz IO to IO		0.3	0.45	pF
		V _R = 0V, f= 1MHz IO to GND		0.6	0.9	

Electrical Parameters (TA = 25°C unless otherwise noted)

Symbol	Parameter	
I PP	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
VRWM	Working Peak Reverse Voltage	
l _R	Maximum Reverse Leakage Current @ VRWM	
V _{BR}	Breakdown Voltage @ I⊤	
lτ	Test Current	



Note:. $8/20\mu s$ pulse waveform.



Typical Characteristic Curves

Fig.1 Peak Pulse Power Rating Curve

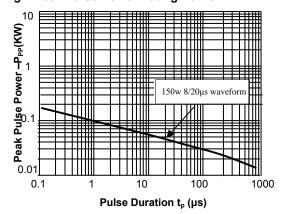


Fig.3 Pulse Waveform-8/20µs

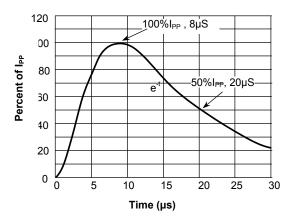


Fig.2 Pulse Derating Curve

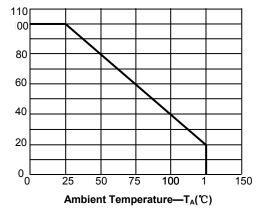
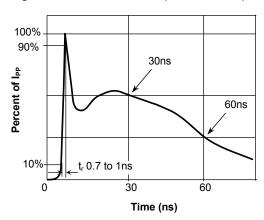
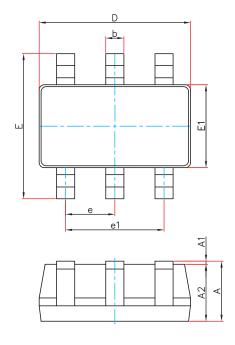


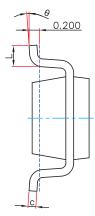
Fig.4 Pulse Waveform-ESD(IEC61000-4-2)





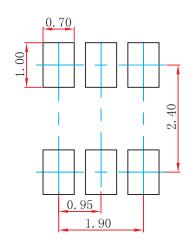
Package Outline Dimensions





Symbol	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
Е	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037((BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

Order information

Orderable Device	Package	Packing Option
TPD4E001DBVR-MS	SOT-23-6	3000PCS



Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.