MSKSEMI 美森科













ESD

TSS

MOV

GDT

PLED

ESD18VF1BL-MS

Product specification





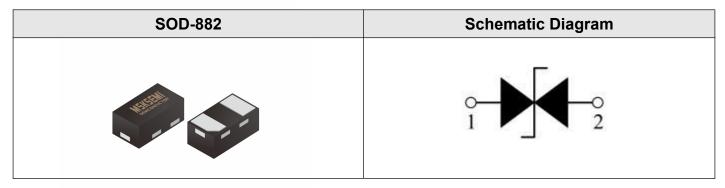
Features

- Ultra-Low capacitance:0.05pF(typ.)
- Low leakage current(<100nA)
- Fast response time(<1ns)
- Bi-directional, single line protection
- IEC 61000-4-2 (ESD Air): 15kV
 IEC 61000-4-2 (ESD Contact): 8kV

Applications

- USB 3.0/3.1
- HDMI 1.3/1.4/2.0
- RF Antenna
- SATA and eSATA Interface

Reference News



Limiting Values(T_A = 25 °C, unless otherwise specified)

Symbol	Parameter	Conditions	Min	Max	Unit
Vesd	Electrostatic Discharge Voltage	IEC 61000-4-2; Contact Discharge	-	8	kV
VESD		IEC 61000-4-2; Air Discharge	-	15	kV
TA	Operating Temperature Range	-	-40	90	°C
Tstg	Storage Temperature Range	-	-55	125	°C

Electrical Characteristics(T_A = 25 °C unless otherwise specified)

Symbol	Parameter	Conditions	Min	Тур.	Max	Unit
VDC	Continuous Operating Voltage	-	-	-	18.0	V
VT	Trigger Voltage	IEC61000-4-2 8kV contact discharge	-	450	-	V
Vc	Clamping Voltage	IEC61000-4-2 8kV contact discharge	-	40	-	V
IL	Leakage Current	DC 18V shall be applied on component	-	-	100	nA
Cı	Capacitance	Measured at 10MHz	-	0.05	-	pF



TypicalCharacteristics

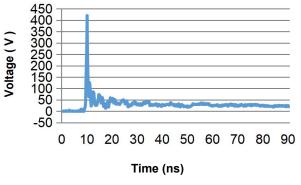


Fig.1 Typical ESD Response (IEC 61000-4-2, 8kV contact discharge)

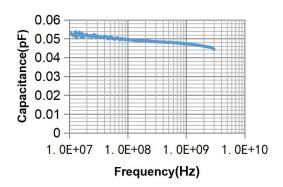


Fig.2 Typical Device Capacitance VS. Frequency

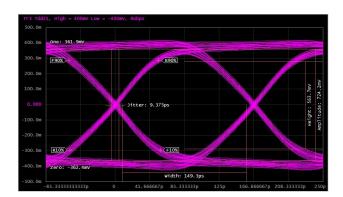


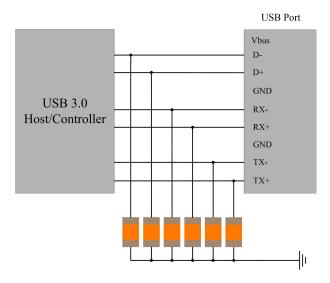
Fig.3 HDMI 2.0 Mask at 6.0 Gbps

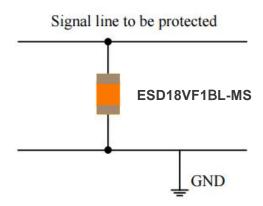


ESDProtectionforSignalLine

The PESD is designed for the protection of one bidirectional data line from ESD damage.

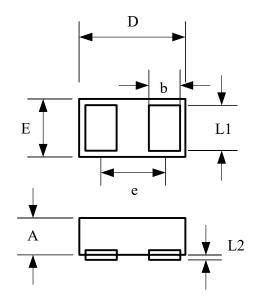
- Place the PESD as close to the input terminal or connector as possible.
- Minimize the path length between the PESD and the protected signal line.
- Use ground planes whenever possible.



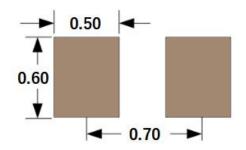




PackageDimension



Recommended Solder Pad Footprint



*Sizes in mm

Notes:

This solder pad layout is for reference purposes only.

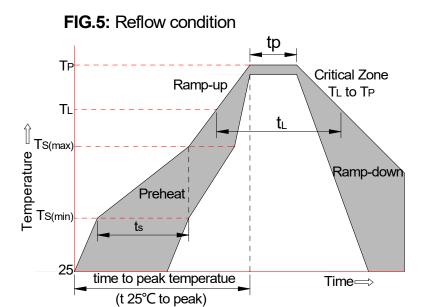
Dimension	Unit: Millimeters		
	Min.	Max.	
А	0.25	0.45	
b	0.20	0.40	
D	0.90	1.10	
E	0.40	0.60	
е	0.65BSC		
L1	0.30	0.50	
L2	0.00	0.05	

Order information

Orderable Device	Package	Packing Option
ESD18VF1BL-MS	SOD-882	10000PCS



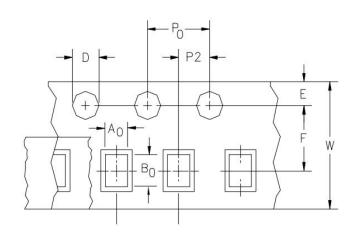
SolderingParameters



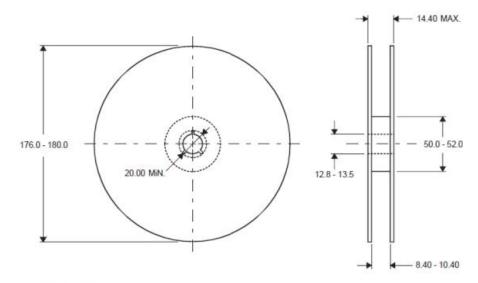
Reflow Condition		Pb-Free Assembly	
	-Temperature Min (T _{s(min)})	+150°C	
Pre-heat	-Temperature Max(T _{s(max)})	+200°C	
	-Time (Min to Max) (ts)	60-180 secs.	
Average ramp up rate (Liquid us Temp (T _L) to peak)		3°C/sec. Max	
T _{s(max)} to T _L - Ramp-up Rate		3°C/sec. Max	
Reflow	-Temperature(T _L)(Liquid us)	+217°C	
	-Temperature(t∟)	60-150 secs.	
Peak Temp (T _p)		+260(+0/-5)°C	
Time within 5°C of actual Peak Temp (tp)		30 secs. Max	
Ramp-down Rate		6°C/sec. Max	
xTime 25°C to Peak Temp (T _P)		8 min. Max	
Do not exceed		+260°C	



PackageInformation



Dimension	Typical	Unit
A0	0.75	
В0	1.22	
D	1.55	
P0	4.00	
P1	2.00	mm
P2	2.00	
Е	1.75	
F	3.50	
W	8.00	



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