

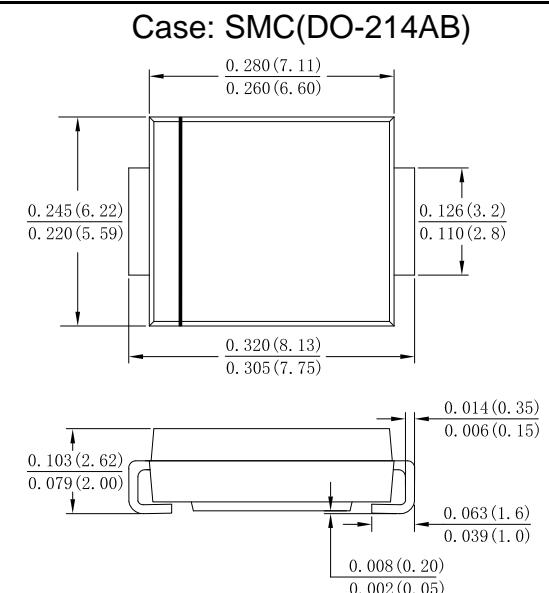


Features

- Glass passivated junction chip
- Low Power Loss, High Efficiency
- Ideally Suited for Automatic Assembly
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-0

Mechanical Data

- Case: Molded plastic SMC
- Terminals: Plated leads solderable per MIL-STD-750, Method 2026 guaranteed
- Polarity: Color band dentes cathode end
- Mounting Position: Any
- Making: Type Number



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

Type Number	Symbols	ER5AC	ER5BC	ER5DC	ER5GC	ER5JC	ER5KC	Units		
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	V		
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	V		
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	V		
Average Rectified Output Current @ $T_L = 100^\circ C$	$IF(AV)$	5.0						A		
Non-Repetitive Peak Forward Surge @ $T_j = 25^\circ C$ Current 8.3ms Single half sine-wave @ $T_j = 125^\circ C$ Superimposed On Rated Load (JEDEC Method)	I_{FSM}	150 120						A		
Non-Repetitive Peak Forward Surge @ $T_j = 25^\circ C$ Current 1.0ms Single half sine-wave @ $T_j = 125^\circ C$ Superimposed On Rated Load (JEDEC Method)	I_{FSM}	300 240						A		
10000 times of the wave surge current (time width 1ms, time interval 3s)	I_{FSM}	112.5						A		
I^2t Rating for Fusing ($t < 8.3ms$)	I^2t	93.375						A^2S		
Forward Voltage @ $IF=5A$	V_F	0.95		1.3	1.7	1.9	V			
Peak Reverse Current @ $T_A = 25^\circ C$	I_R	3.0						uA		
At Rated DC Blocking Voltage @ $T_A = 125^\circ C$		100								
Maximum Reverse Recovery Time (Note 1)	Tr	35						ns		
Typical Junction Capacitance (Note 2)	C_J	45		30		pF				
Typical Thermal Resistance	$R_{\theta JL}$	17						$^\circ C/W$		
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150						$^\circ C$		

Note:

1. Reverse Recovery Test Conditions: $IF=0.5A, IR=1.0A, IRR=0.25A$.

2. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C.

Fig. 1 Forward Current Derating Curve

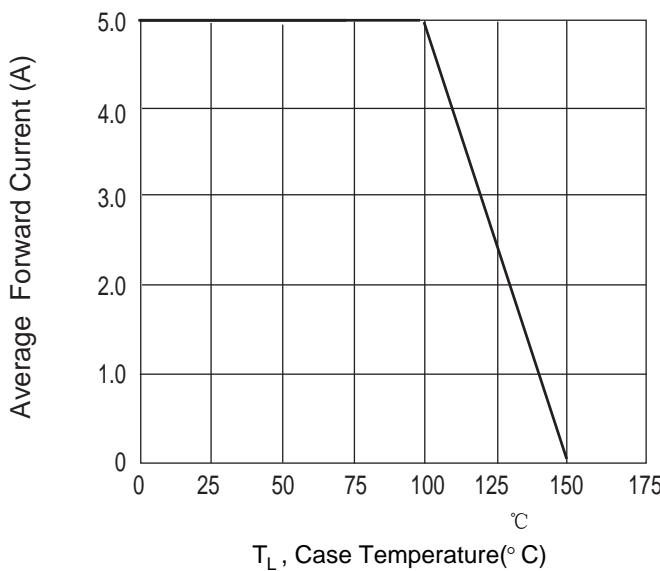


Fig. 2 Typ. Forward Characteristics

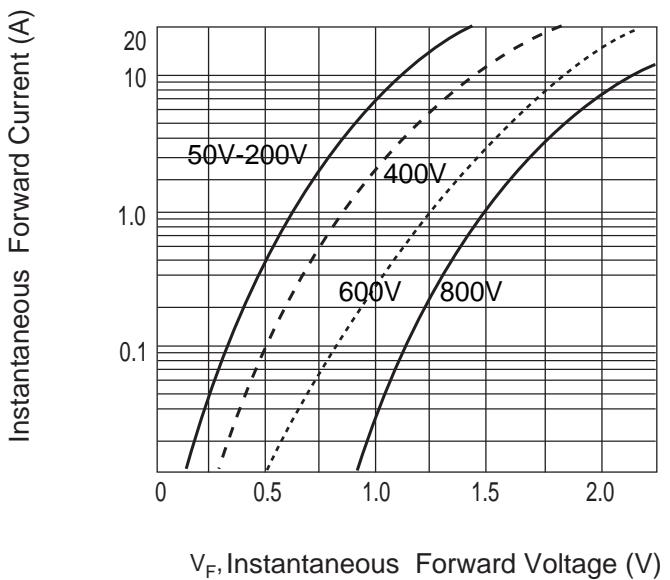


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

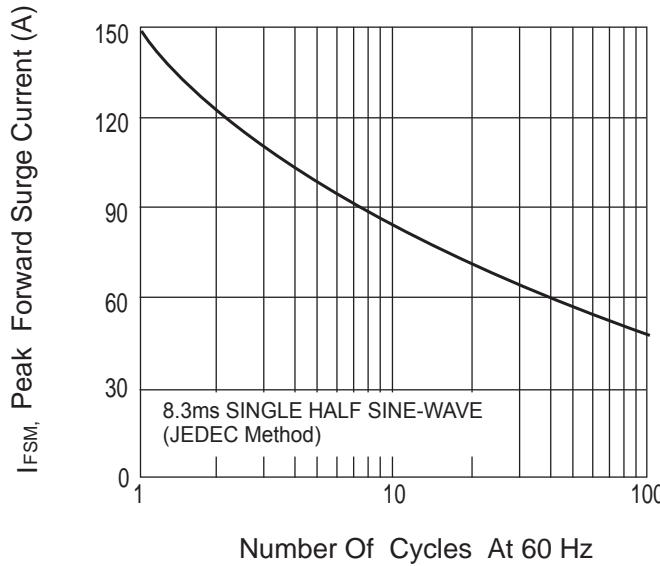


Fig. 4 Typical Reverse Characteristics

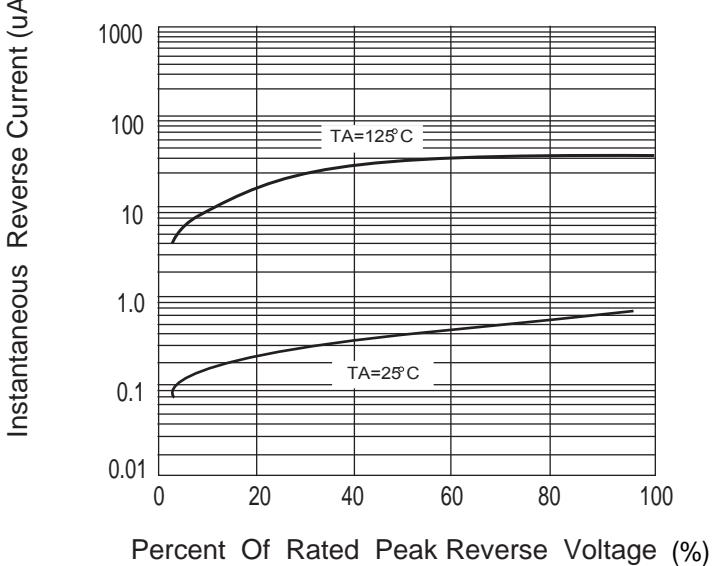
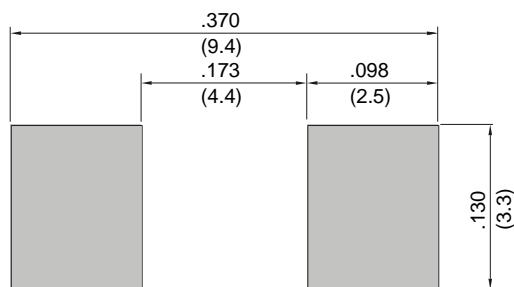


Fig.5 Mounting PAD Layout





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