MSKSEMI 美森科













ESD

MOV

SRV05-4HTG-MS

Product specification





Features

- 150Watts peak pulse power (tp = 8/20µs)
- SOT-23-6 package
- Solid-state silicon-avalanche technology
- Low clamping voltage
- Low leakage current
- Low capacitance (Cj=0.3pF typ. IO to IO)
- Protection one data/power line to:
- IEC 61000-4-2 ±12kV contact ±15kV air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 5A (8/20µs)

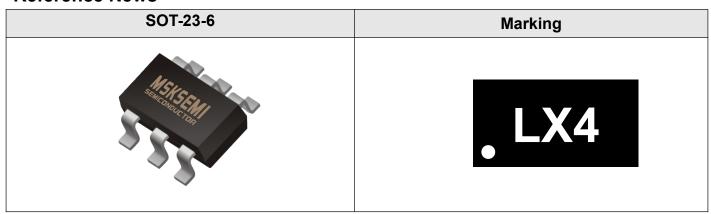
Mechanical Data

- SOT23-6 package
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel
- RoHS/WEEE Compliant

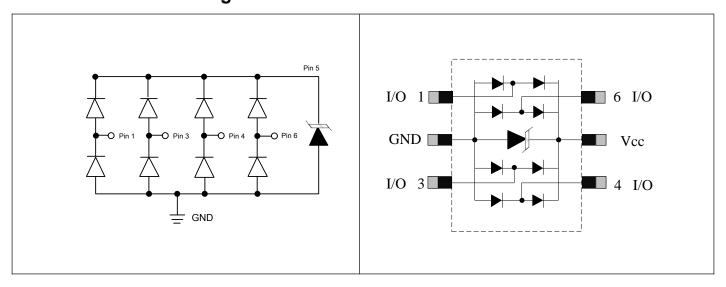
Applications

- Ethernet
- Digital Visual Interface (DVI)
- USB2.0
- Notebook and PC Computers

Reference News



Schematic & PIN Configuration





Absolute Maximum Rating

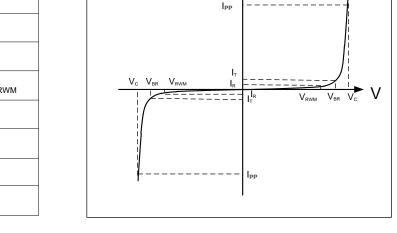
Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20μs)	P _{PP}	150	Watts
Peak Pulse Current (t _p =8/20µs) (note1)	I _{pp}	5.0	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	15 12	kV
Lead Soldering Temperature	TL	260(10seconds)	°C
Junction Temperature	TJ	-55 to + 125	°C
Storage Temperature	T_{stg}	-55 to + 125	$^{\circ}$ C

Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V_{RWM}				5.0	V
Reverse Breakdown Voltage	V_{BR}	I _T =1mA	6			V
Reverse Leakage Current	I _R	V _{RWM} =5V,T=25℃			1.0	uA
Peak Pulse Current	I _{PP}	tp =8/20µs			5.0	Α
Clamping Voltage	Vc	I _{PP} =5.0A,t _p =8/20µs			16	V
Junction Capacitance	C _j	V _R = 0V, f= 1MHz IO to IO		0.3	0.45	pF
		V _R = 0V, f= 1MHz IO to GND		0.6	0.9	

Electrical Parameters (TA = 25°C unless otherwise noted)

Parameter	
Maximum Reverse Peak Pulse Current	
Clamping Voltage @ IPP	
Working Peak Reverse Voltage	
Maximum Reverse Leakage Current @ VRWM	
Breakdown Voltage @ I⊤	
Test Current	



Note:. $8/20\mu s$ pulse waveform.



Typical Characteristic Curves

Fig.1 Peak Pulse Power Rating Curve

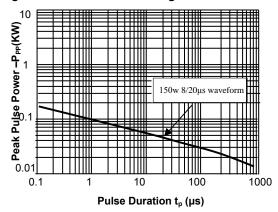


Fig.3 Pulse Waveform-8/20µs

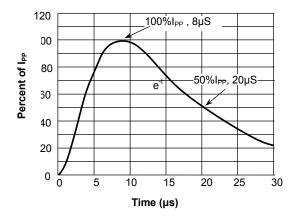


Fig.2 Pulse Derating Curve

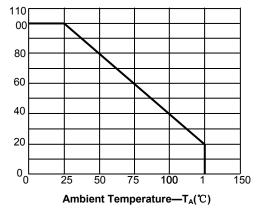
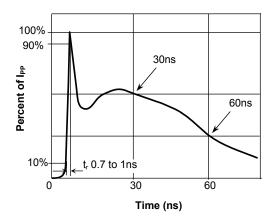
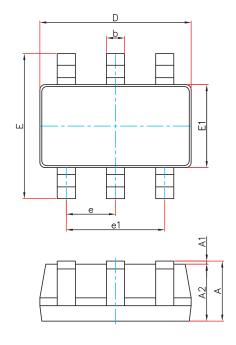


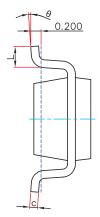
Fig.4 Pulse Waveform-ESD(IEC61000-4-2)





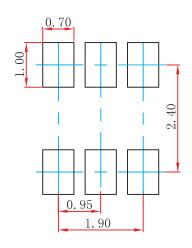
Package Outline Dimensions





Symbol	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037((BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

Order information

Orderable Device	Package	Packing Option
SRV05-4HTG-MS	SOT-23-6	3000PCS



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