

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
-20V	400mΩ@-4.5V	-0.75A
	550mΩ@-2.5V	



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Feature

- High power and current handing capability
- Surface mount package
- ESD protected 2KV

Application

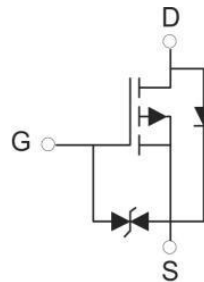
- Battery Switch
- DC/DC Converter

Package

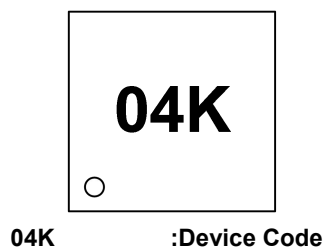


DFN1212-3L

Circuit diagram



Marking



Order Information

Device	Package	Unit/Tape
SP2004KND	DFN1212-3L	5000

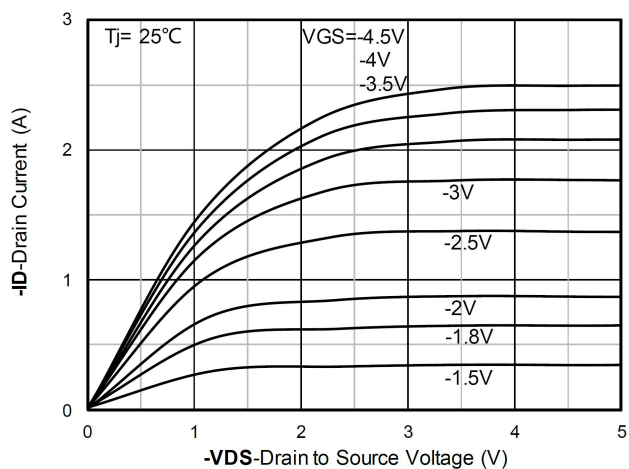
Absolute maximum ratings (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	-0.75	A
Pulse Drain Current Tested	I_{DM}	-3	A
Power Dissipation	P_D	420	mW
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	297	°C/W
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

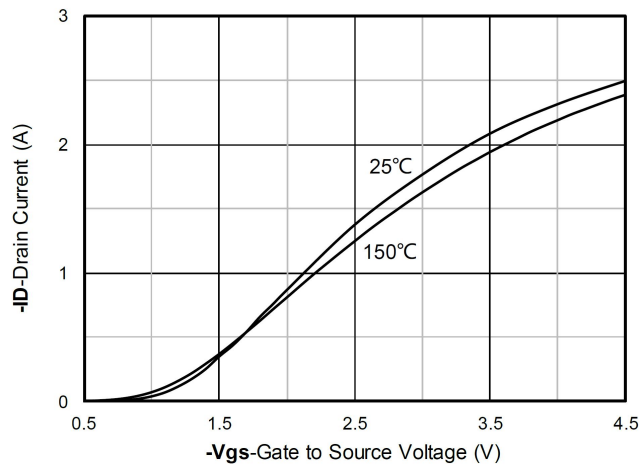
Electrical characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V , ID=-250μA	-20	-	-	V
Drain-Source Leakage Current	IDSS	VDS=-16V , VGS=0V	-	-	-1	uA
Gate-Source Leakage Current	IGSS	VGS=±12V , VDS=0V	-	-	±10	uA
Gate Threshold Voltage	VGS(th)	VDS=VGS , ID=-250μA	-0.35	-0.65	-1.00	V
Static Drain-Source On-Resistance	RDS(on)	VGS=-4.5V , ID=-500mA	-	400	550	mΩ
		VGS=-2.5V , ID=-200mA	-	550	700	
Dynamic characteristics						
Input Capacitance	Ciss	VDS=-16V , VGS=0V , f=1MHz	-	113	-	pF
Output Capacitance	Coss		-	15	-	
Reverse Transfer Capacitance	Crss		-	9	-	
Total Gate Charge	Qg	VDS=-10V , VGS=-4.5V , ID=-300mA	-	1.9	-	nC
Gate-Source Charge	Qgs		-	0.4	-	
Gate-Drain Charge	Qgd		-	0.31	-	
Switching Characteristics						
Turn-On Delay Time	td(on)	VDD=-10V VGS=-4.5V , RG=10Ω , ID=-200mA	-	9	-	nS
Turn-On Rise Time	tr		-	5.7	-	
Turn-Off Delay Time	td(off)		-	32.6	-	
Turn-Off Fall Time	tr		-	20.3	-	
Source-Drain Diode characteristics						
Diode Forward Voltage	VSD	VGS=0V , IS=-1A , TJ=25℃	-	-	-1.2	V

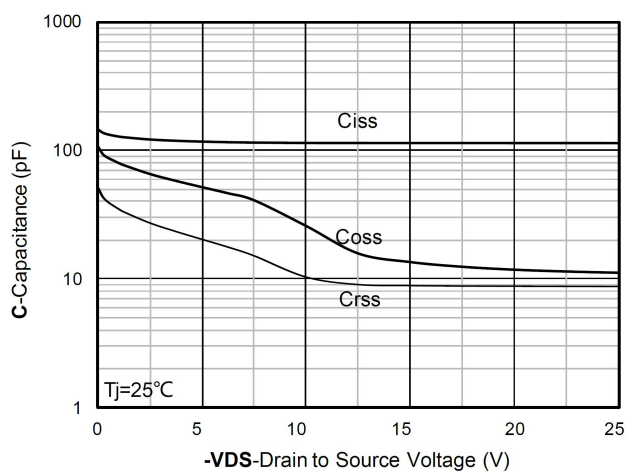
Typical Characteristics



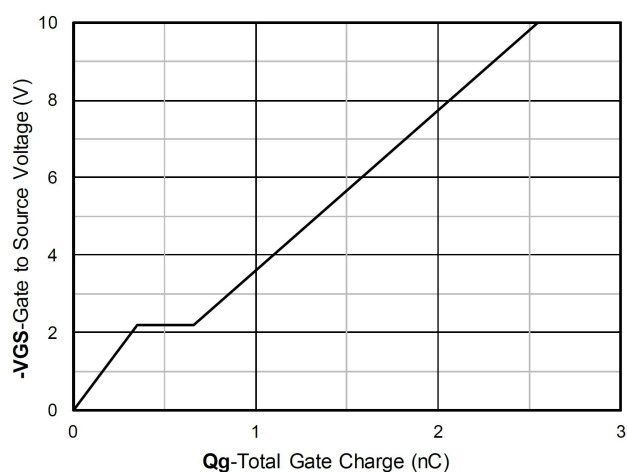
Output Characteristics



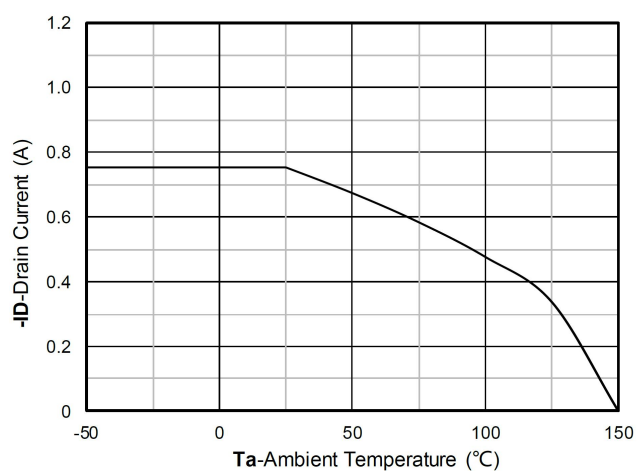
Transfer Characteristics



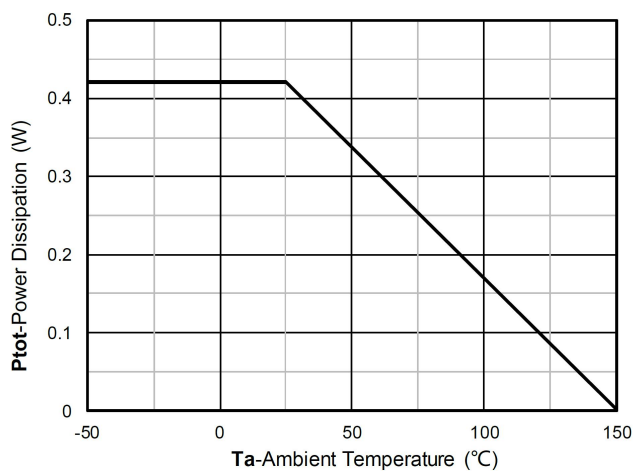
Capacitance Characteristics



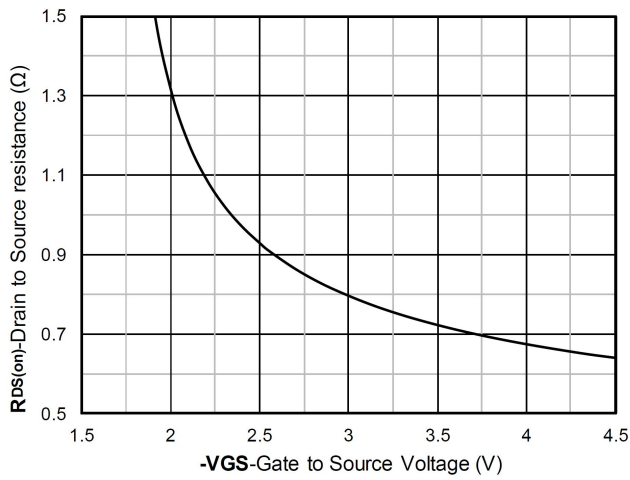
Gate Charge



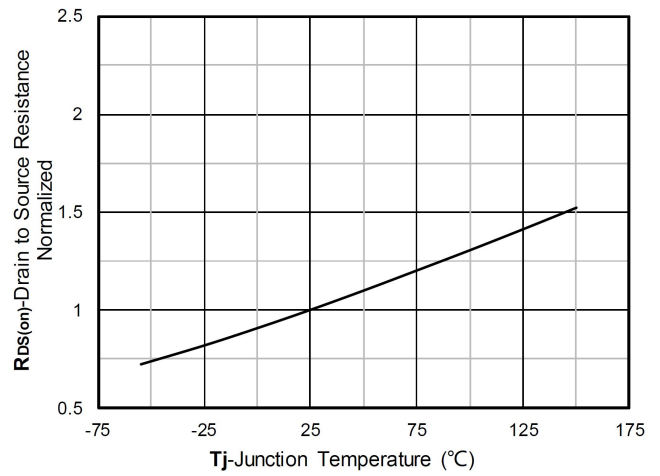
Current dissipation



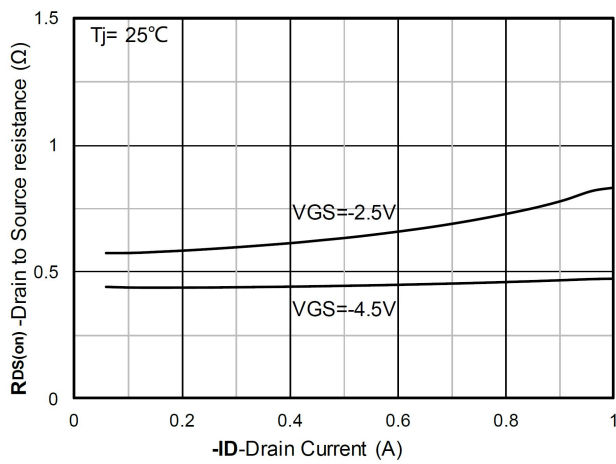
Power dissipation



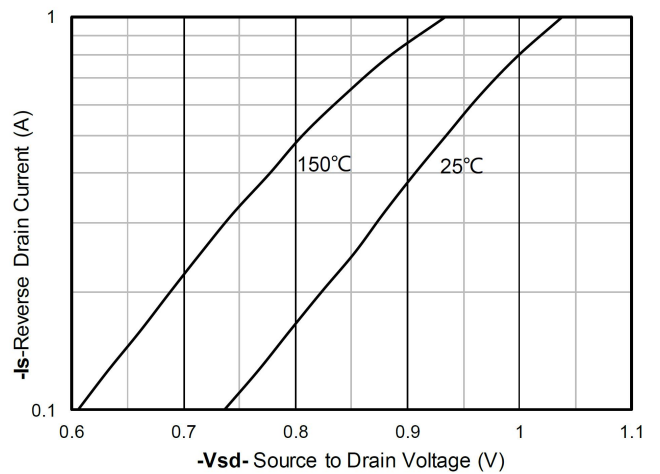
On-Resistance vs Gate to Source Voltage



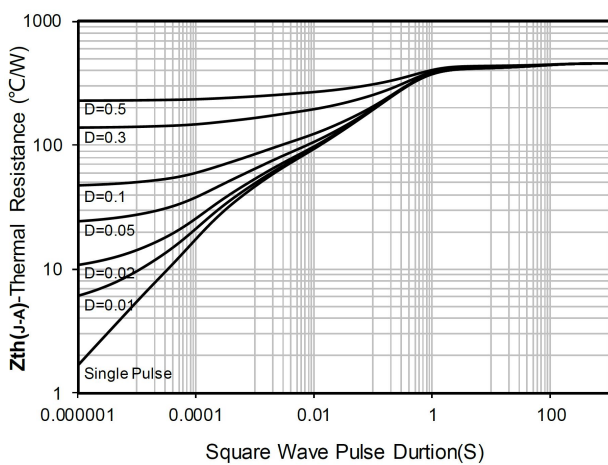
Normalized On-Resistance



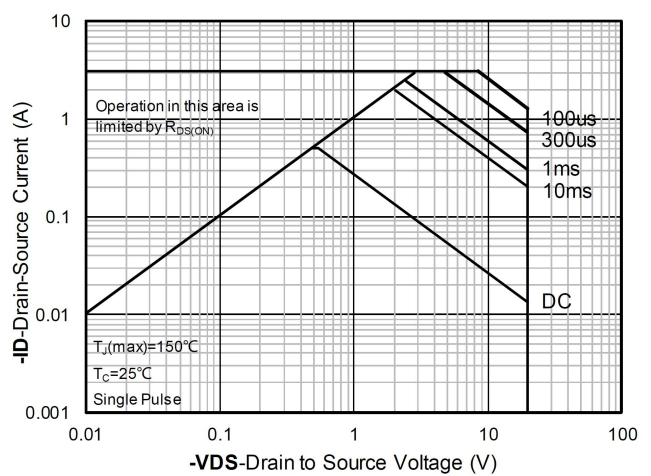
$R_{DS(on)}$ VS Drain Current



Forward characteristics of reverse diode

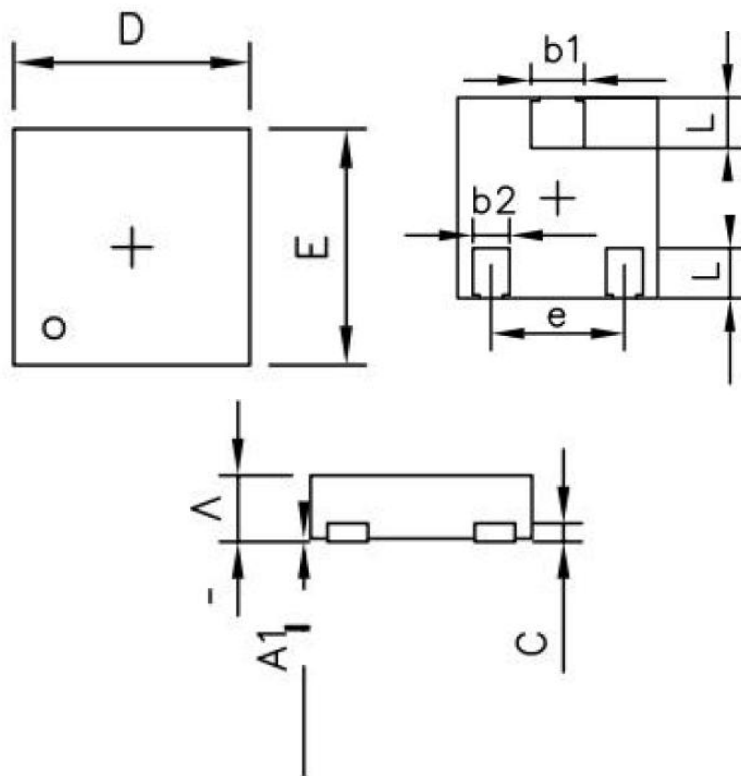


Maximum Transient Thermal Impedance



Safe Operation Area

DFN1212-3L Package Information



PDFN1212-3L POD				
Symbol	Dimension	Min(mm)	TYP(mm)	Max (mm)
A		0.45	0.50	0.55
A1		0.00	0.03	0.05
C		0.152		
b1		0.27	0.32	0.37
L		0.25	0.30	0.35
D		1.15	1.20	1.25
e		0.80		
E		1.15	1.20	1.25
b2		0.17	0.22	0.27