

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
40V	13mΩ@10V	9A
	18mΩ@4.5V	
-40V	26mΩ@-10V	-7A
	35mΩ@-4.5V	



合肥矽普半导体

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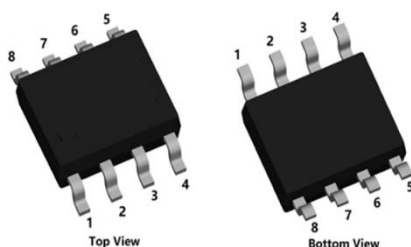
Feature

- High power and current handing capability
- Lead free product is acquired
- Surface mount package
- 100% Single Pluse avalanche energy Test

Applications

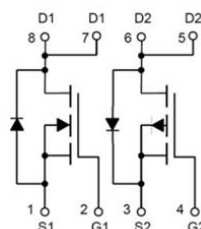
- Battery Protection
- Load Switch
- Power Management

Package

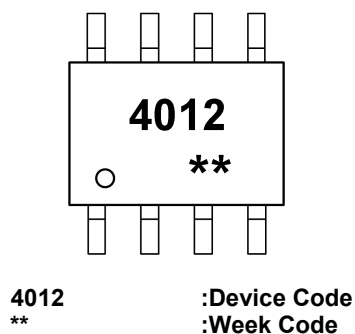


SOP-8L

Circuit diagram



Marking



Order Information

Device	Package	Unit/Tape
SP4012CP8	SOP-8L	4000

Absolute maximum ratings (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Value		Units
		N-Channel	P-Channel	
Drain-Source Voltage	V_{DS}	40	-40	V
Gate-Source Voltage	V_{GS}	± 20	± 20	V
Continuous Drain Current	I_D	9	-7	A
Pulsed Drain Current	I_{DM}	36	-28	A
Single Pulse Avalanche Energy ¹	E_{AS}	18	36	mJ
Power Dissipation	P_D	2.5		W
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	50		°C/W
Storage Temperature Range	T_{STG}	-55 to 150		°C
Operating Junction Temperature Range	T_J	-55 to 150		°C

N-Channel Electrical characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V , ID=250uA	40	-	-	V
Drain-Source Leakage Current	IDSS	VDS=32V , VGS=0V , TJ=25℃	-	-	1	uA
Gate-Source Leakage Current	IGSS	VGS=±20V , VDS=0V	-	-	±100	nA
Gate Threshold Voltage	VGS(th)	VGS=VDS , ID =250uA	1.0	1.5	2.5	V
Static Drain-Source On-Resistance	RDS(ON)	VGS=10V , ID=7A	-	13	18	mΩ
		VGS=4.5V , ID=4A	-	18	25	
Dynamic characteristics						
Input Capacitance	Ciss	VDS=15V , VGS=0V , f=1MHz	-	1061	-	pF
Output Capacitance	Coss		-	110	-	
Reverse Transfer Capacitance	Crss		-	95	-	
Total Gate Charge	Qg	VDS=32V , VGS=4.5V , ID=7A	-	25	-	nC
Gate-Source Charge	Qgs		-	6.3	-	
Gate-Drain Charge	Qgd		-	4.6	-	
Switching Characteristics						
Turn-On Delay Time	Td(on)	VDD=20V, VGS=10V , RG=3Ω, ID=7A	-	7.8	-	nS
Rise Time	Tr		-	10.7	-	
Turn-Off Delay Time	Td(off)		-	25.8	-	
Fall Time	Tf		-	4.6	-	
Diode Characteristics						
Diode Forward Voltage	VSD	VGS=0V , IS=1A , TJ=25℃	-	-	1.2	V
Maximum Body-Diode Continuous Current	IS		-	-	9	A
Reverse Recovery Time	Trr	IS=6A, di/dt=100A/us, TJ=25℃	-	15	-	nS
Reverse Recovery Charge	Qrr		-	5	-	nC

Note :

1. The EAS test condition is $V_{DD}=20V, V_{GS}=10V, L=0.5mH, R_G=25\Omega$

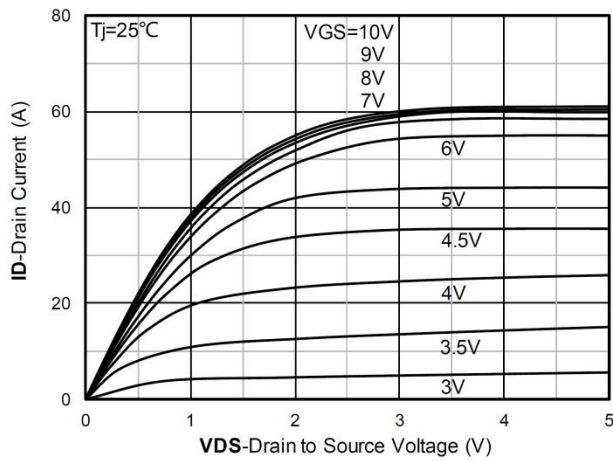
P-Channel Electrical characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V , ID=-250uA	-40	-	-	V
Drain-Source Leakage Current	IDSS	VDS=-32V , VGS=0V , TJ=25℃	-	-	-1	uA
Gate-Source Leakage Current	IGSS	VGS=±20V , VDS=0V	-	-	±100	nA
Gate Threshold Voltage	VGS(th)	VGS=VDS , ID =-250uA	-1	-1.5	-2.5	V
Static Drain-Source On-Resistance	RDS(ON)	VGS=-10V , ID=-5A	-	26	35	mΩ
		VGS=-4.5V , ID=-4A	-	35	45	
Dynamic characteristics						
Input Capacitance	Ciss	VDS=-15V , VGS=0V , f=1MHz	-	1415	-	pF
Output Capacitance	Coss		-	134	-	
Reverse Transfer Capacitance	Crss		-	102	-	
Total Gate Charge	Qg	VDS=-15V , VGS=-4.5V , ID=-1A	-	23.5	-	nC
Gate-Source Charge	Qgs		-	3.4	-	
Gate-Drain Charge	Qgd		-	4.3	-	
Switching Characteristics						
Turn-On Delay Time	Td(on)	VDD=-15V VGS=-10V , RG=3Ω, ID=-1A	-	11	-	nS
Rise Time	Tr		-	16.7	-	
Turn-Off Delay Time	Td(off)		-	35	-	
Fall Time	Tf		-	19	-	
Diode Characteristics						
Diode Forward Voltage	VSD	VGS=0V , IS=-1A , TJ=25℃	-	-	-1.2	V
Maximum Body-Diode Continuous Current	IS		-	-	-7	A
Reverse Recovery Time	Trr	IS=-6A, di/dt=-100A/us, Tj=25℃	-	23	-	nS
Reverse Recovery Charge	Qrr		-	13		nC

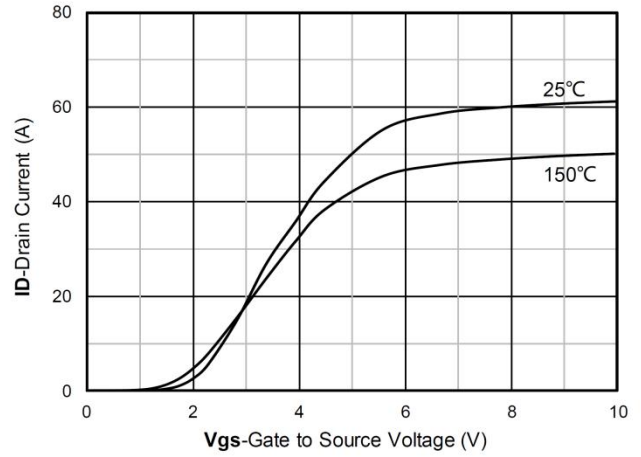
Note :

1. The EAS test condition is $V_{DD}=-20V$, $V_{GS}=-10V$, $L=0.5mH$, $R_G=25\Omega$

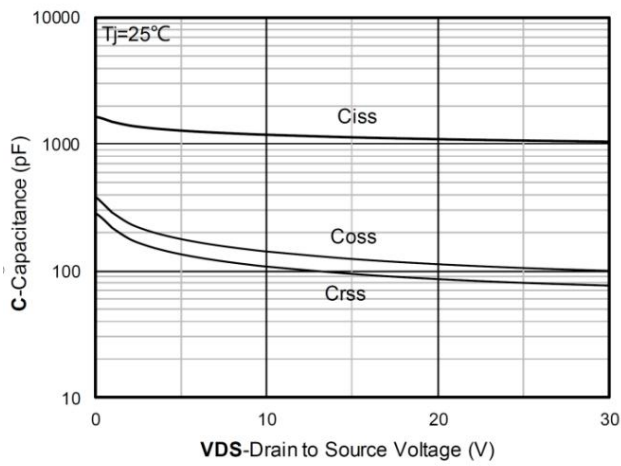
N-Channel Typical Characteristics



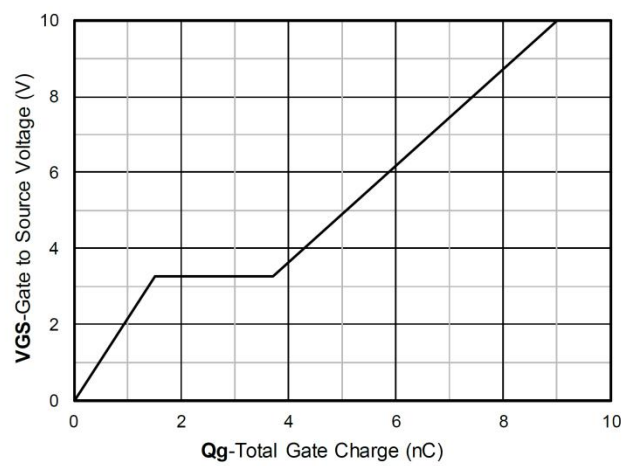
Output Characteristics



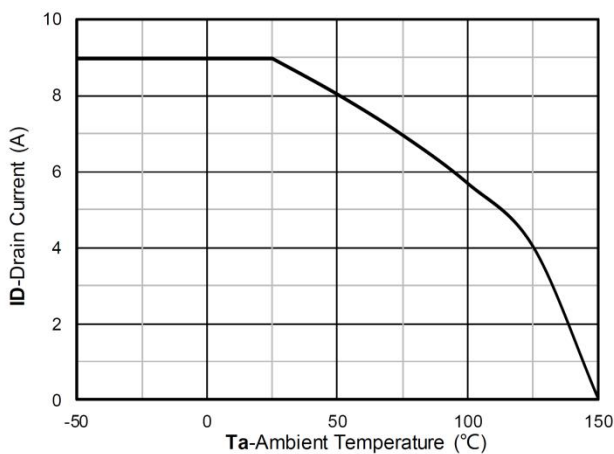
Transfer Characteristics



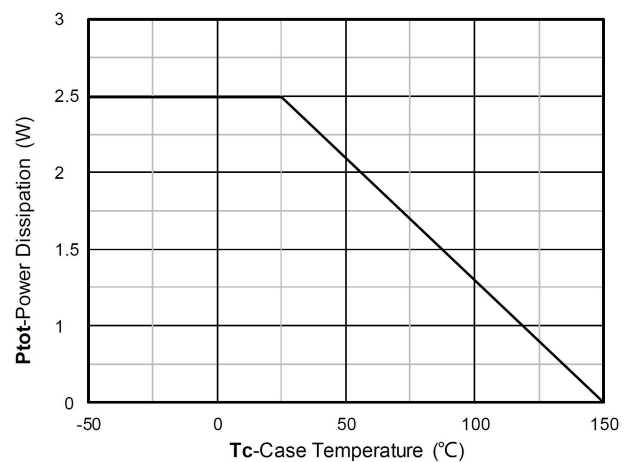
Capacitance Characteristics



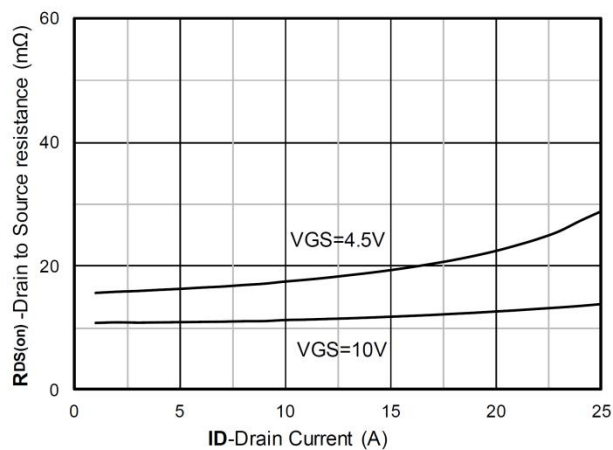
Gate Charge



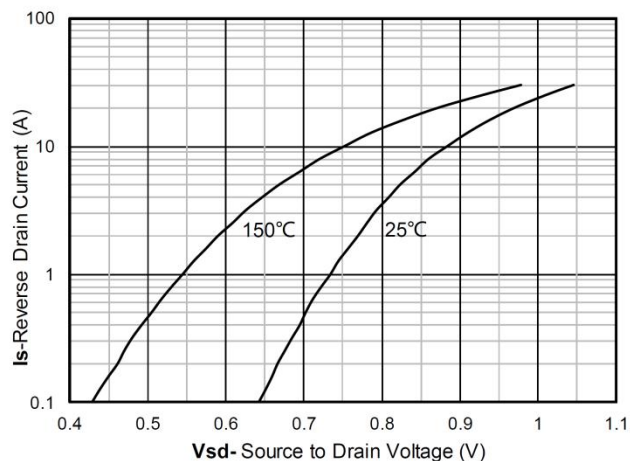
Current dissipation



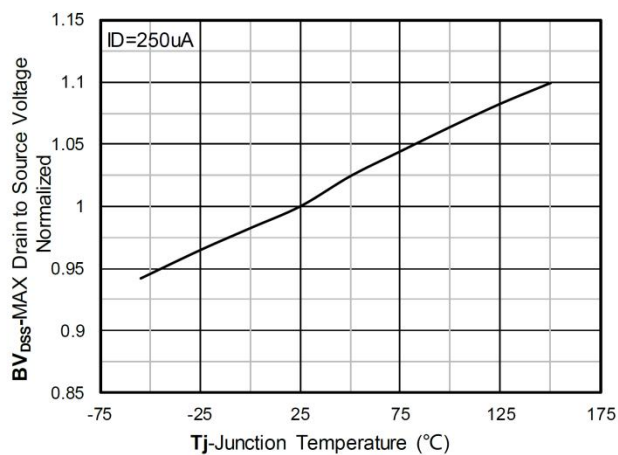
Power dissipation



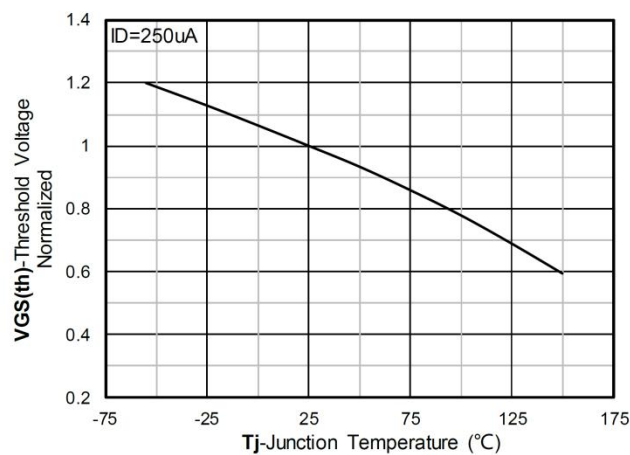
RDS(on) VS Drain Current



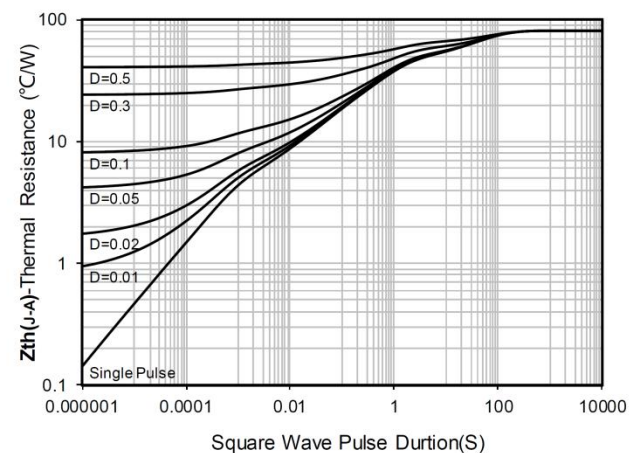
Forward characteristics of reverse diode



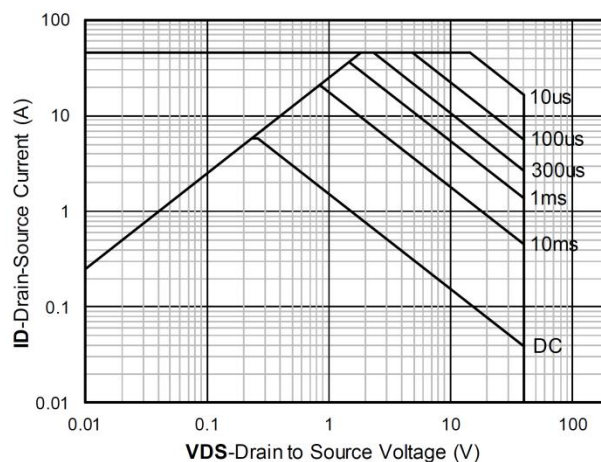
Normalized breakdown voltage



Normalized Threshold voltage

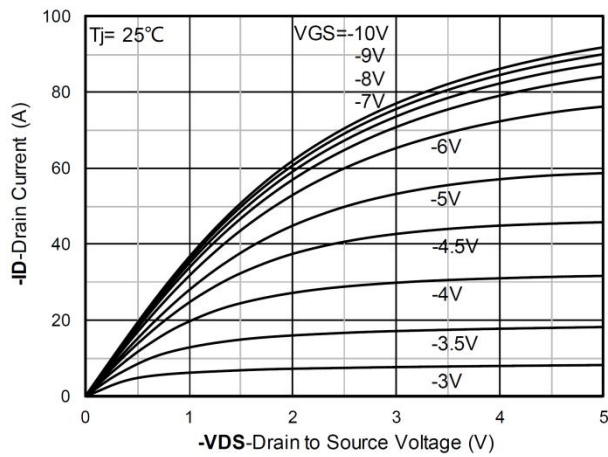


Maximum Transient Thermal Impedance

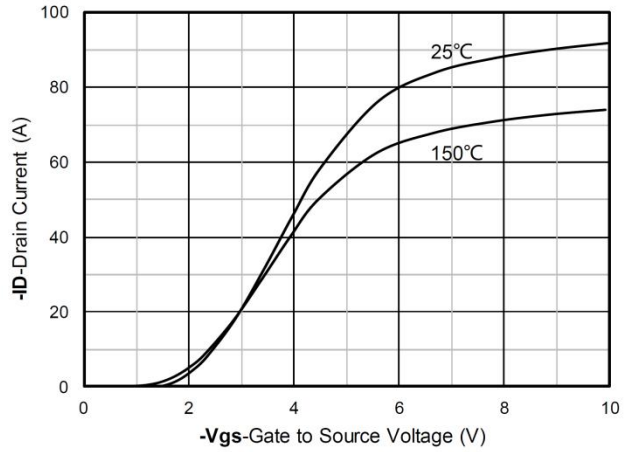


Safe Operation Area

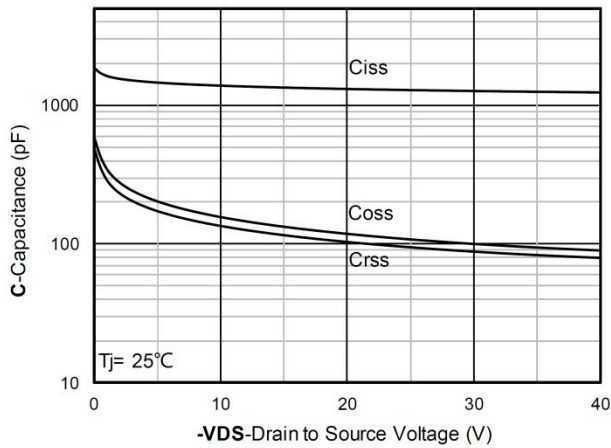
P-Channel Typical Characteristics



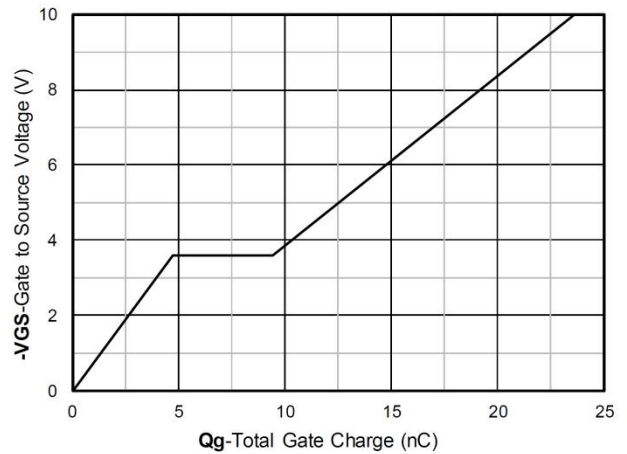
Output Characteristics



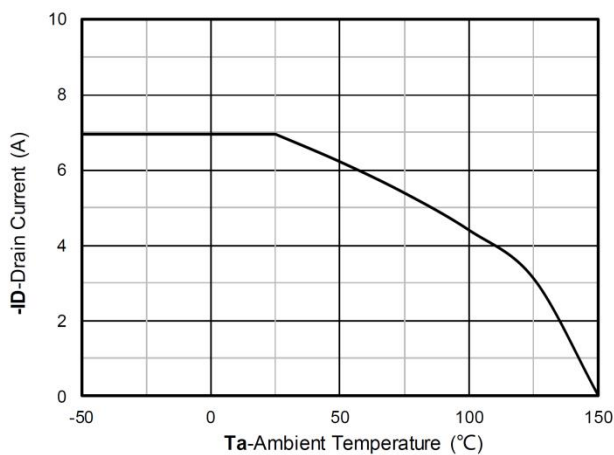
Transfer Characteristics



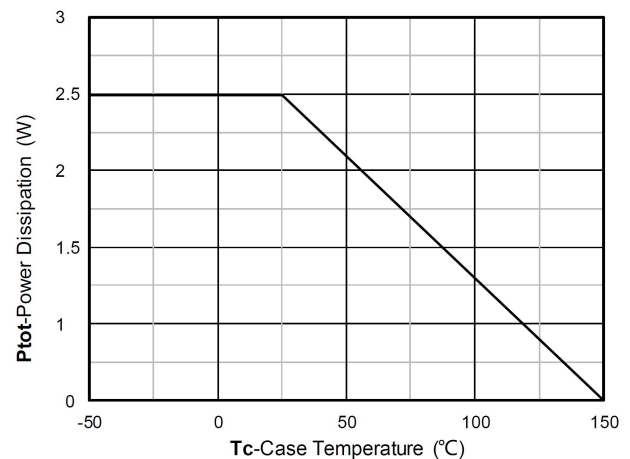
Capacitance Characteristics



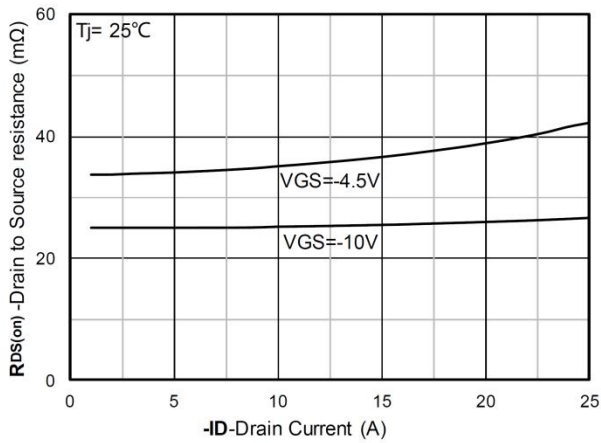
Gate Charge



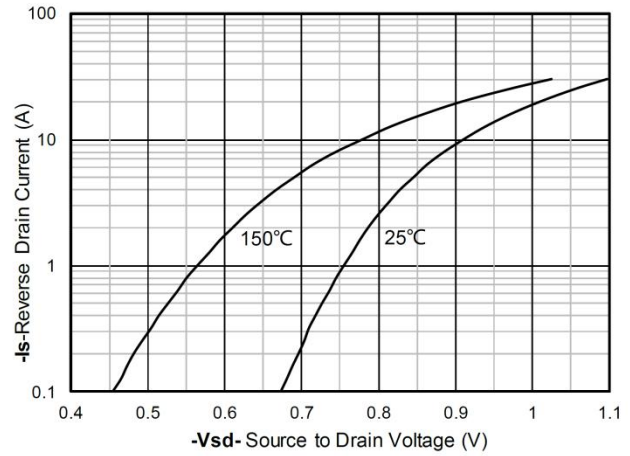
Current dissipation



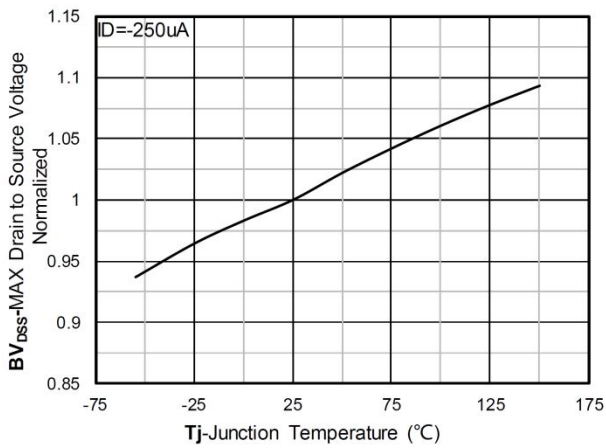
Power dissipation



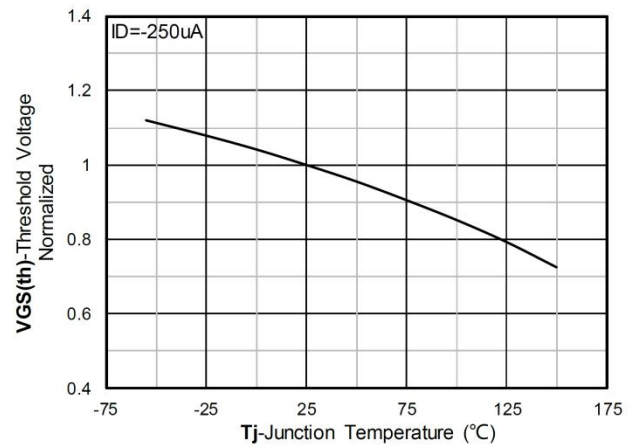
$R_{DS(on)}$ VS Drain Current



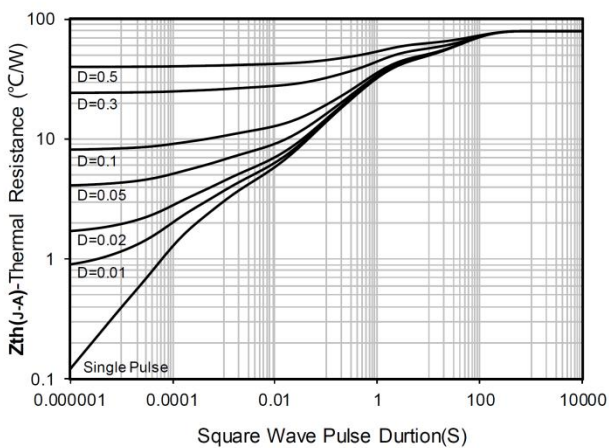
Forward characteristics of reverse diode



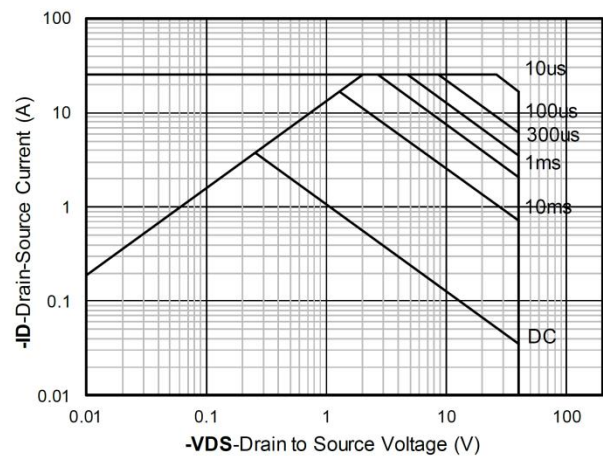
Normalized breakdown voltage



Normalized Threshold voltage

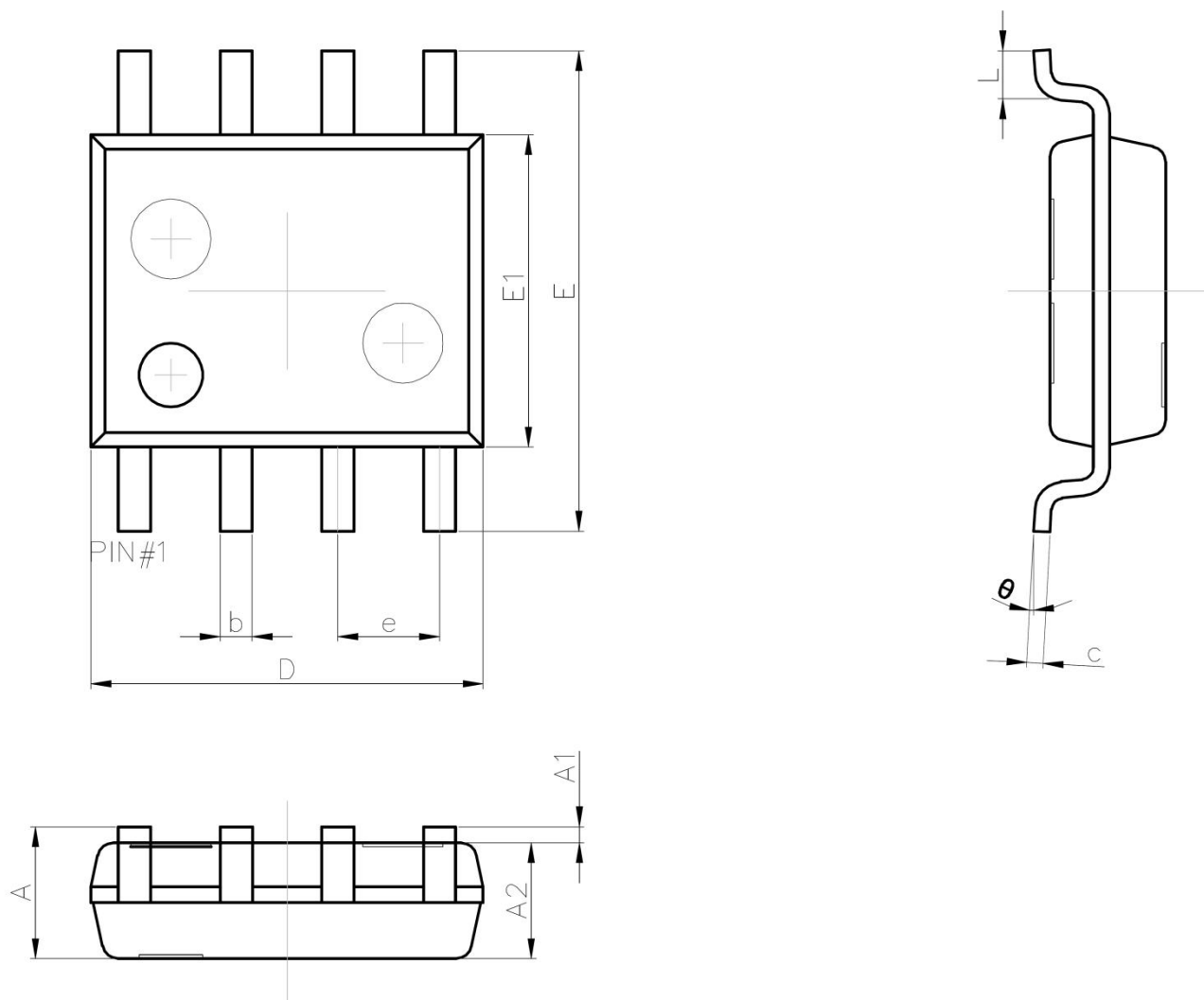


Maximum Transient Thermal Impedance



Safe Operation Area

SOP-8L Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	1.35	1.75
A1	0.10	0.25
A2	1.35	1.55
b	0.33	0.51
c	0.17	0.25
D	4.80	5.00
e	1.27 REF.	
E	5.80	6.20
E1	3.80	4.00
L	0.40	1.27
θ	0°	8°